

A
版本号

变更内容

V01

创建文件

V02

A

A

B

B

C

C

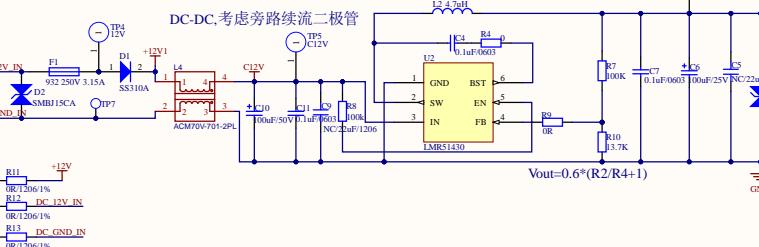
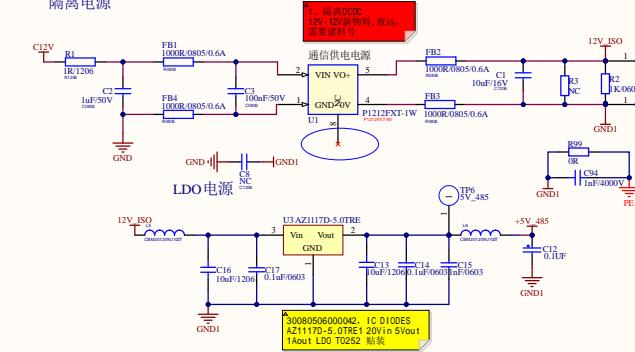
D

D

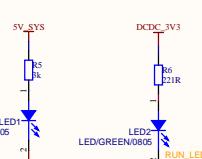
01.Version

Size:A4	Number:1/14	Revision:V01
Date:2025/6/21	Time: 9:33:38	DrawnBy:

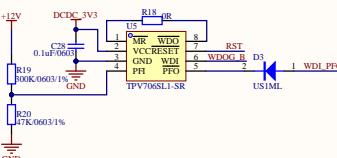
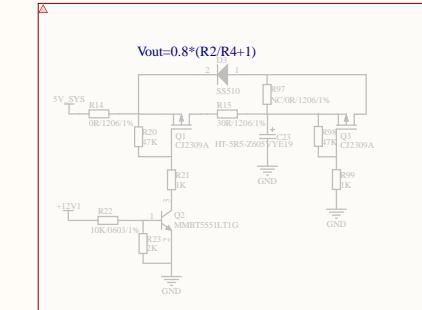
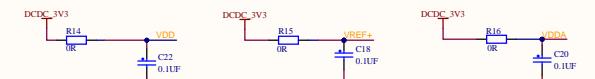
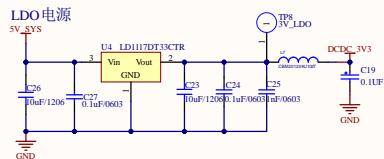
隔离电源



USER LED

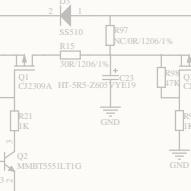


LDO Power



1. 看门狗电路，可以通过复位开关选择。Tdi: 200ms, Test: 1.6s, WDI是高阻态，输出看门狗复位。
2. 支持掉电检测，当12V电源电压掉到10.65V时PFO由低变高，复位延时1.25s(1.47*380/37)=0.28s

$$V_{out} = 0.8 * (R2/R4 + 1)$$



02.Power

Size:	A4	Number:	2/14	Revision:	V01
Date:	2025/6/21	Time:	9:33:38	DrawnBy:	

- VDD脚必须外接电容 (N*100pF陶瓷电容+不少于4.7uF钽电容，至少一个VDD脚需要接不小于4.7uF钽电容到GND，其他VDD引脚接100nF)；
- VDDA脚必须直接接电源 (建议10nF+1uF陶瓷电容)；
- I2C引脚可直接连至VDDA，在I2S引脚处引脚可地连接10nF+1uF陶瓷电容

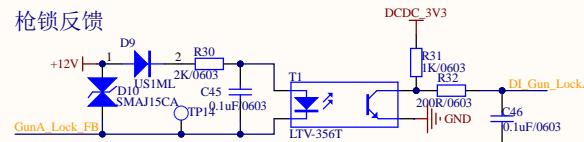
The diagram illustrates the connection of various capacitors (C29-C34, C35, C36) across power rails (VDD, VDDA, VBAT_IN) and ground. The connections are as follows:

- VDD:** C29 is connected between VDD and ground. C30, C31, C32, C33, and C34 are connected in series between VDD and VDDA.
- VDDA:** C35 is connected between VDDA and ground. C36 is connected between VDDA and VBAT_IN.
- VBAT_IN:** C36 is connected between VBAT_IN and ground.

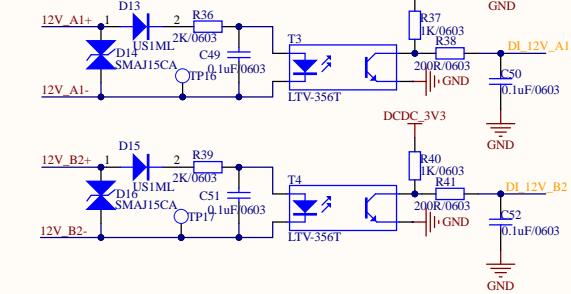
10 of 10

03.MCU RTT1021

枪锁反馈



辅源反馈

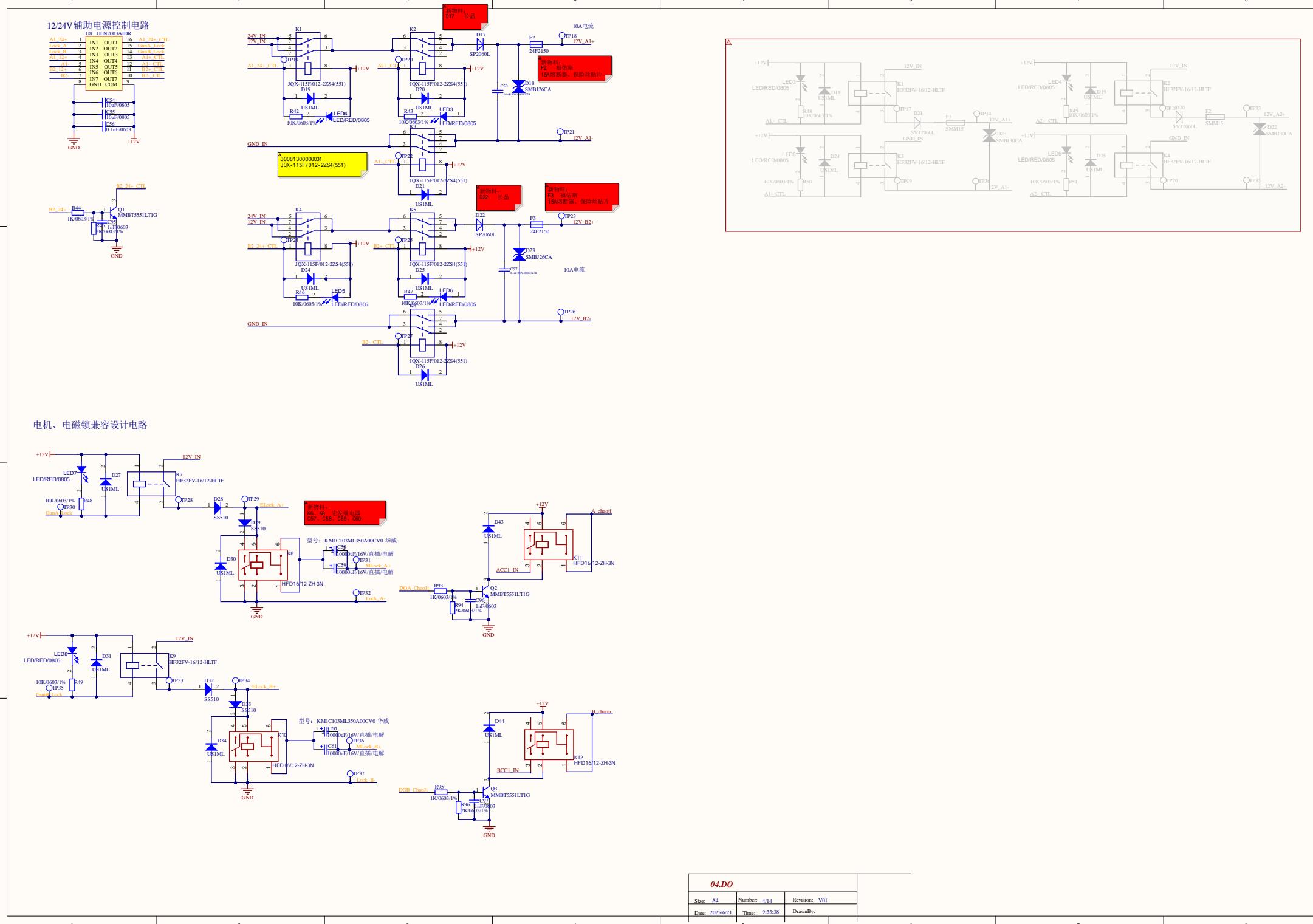


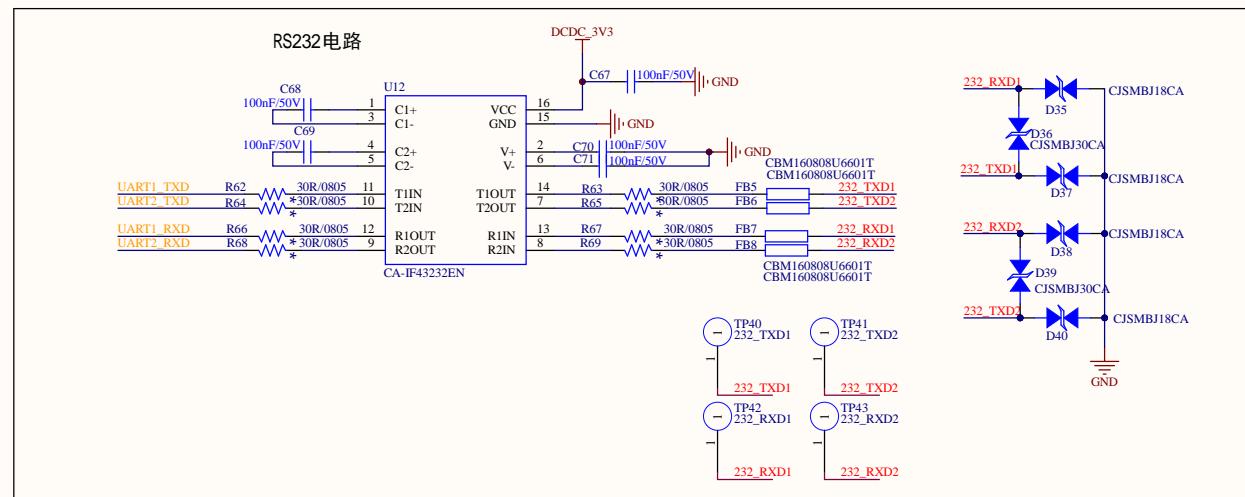
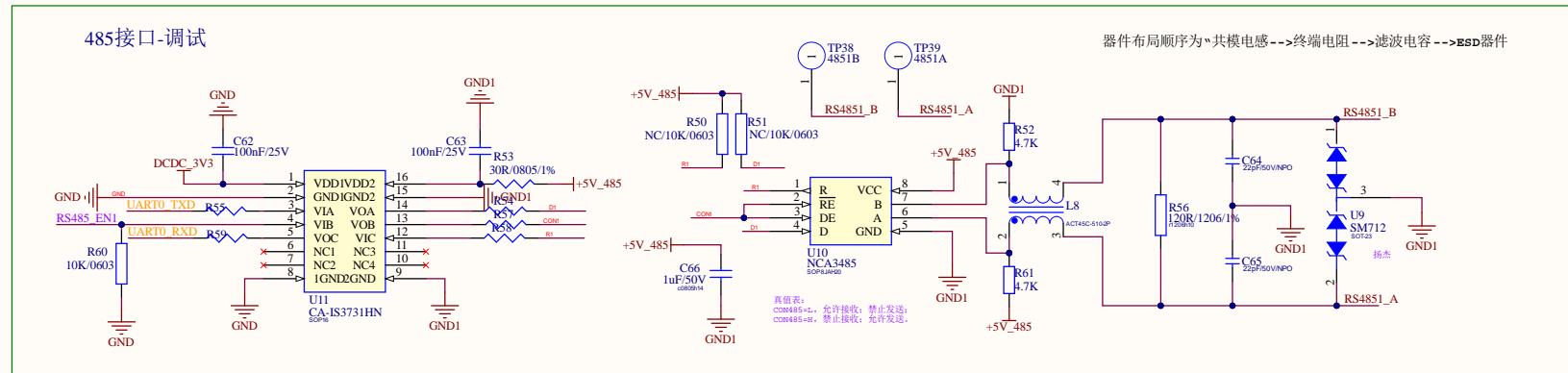
C

D

03.DI

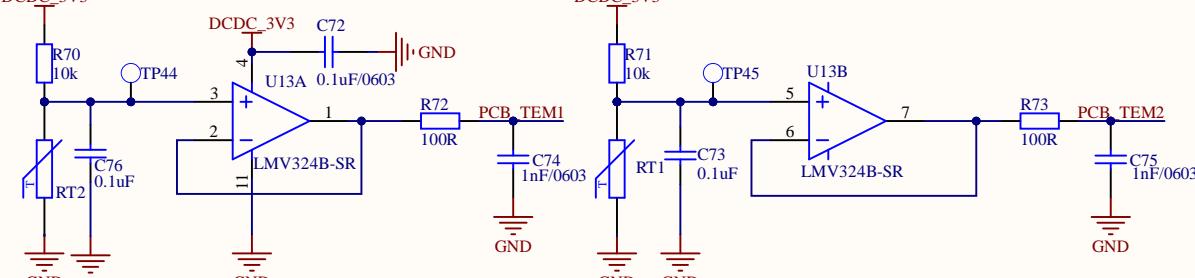
Size: A4	Number: 2/14	Revision: V01
Date: 2025/6/21	Time: 9:33:38	DrawnBy:





06 RS232		
Size: A4	Number: 11/14	Revision: V01
Date: 2025/6/21	Time: 9:33:39	DrawnBy:

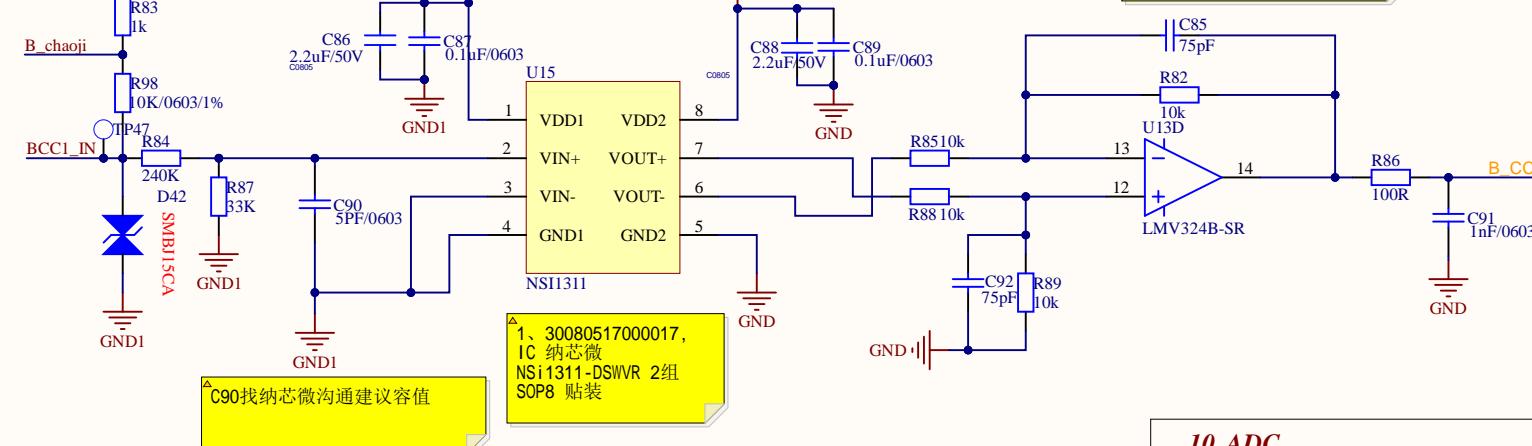
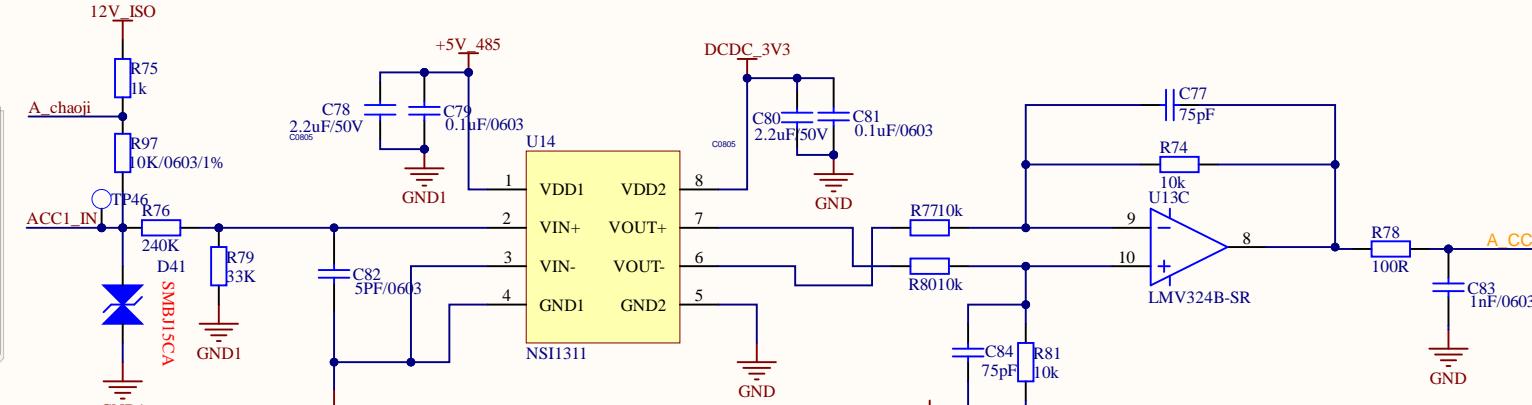
主板温度检测 DCDC_3V3



CC1接口

△关注点:
1、CC1分压过大，降低采样精度，(应该是考虑隔离运放输入0.1-2V)
2、隔离运放共地，失去隔离意义，且CC1电压不高隔离意义不大，关键是滤波。

CC1接口

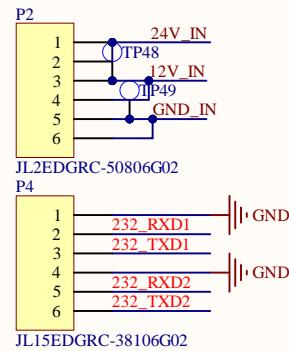
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Size: A4	Number: 14/14	Revision: V01
Date: 2025/6/21	Time: 9:33:39	DrawnBy:

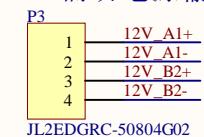
12V电源输入



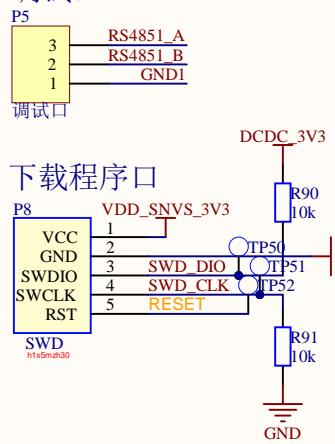
12V辅助电源输入



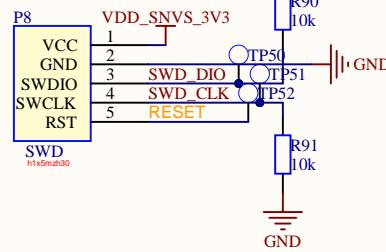
12V辅助电源输出



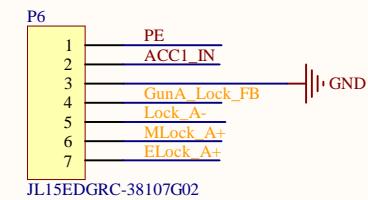
调试口



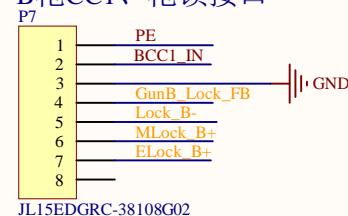
下载程序口



A枪CC1、枪锁接口



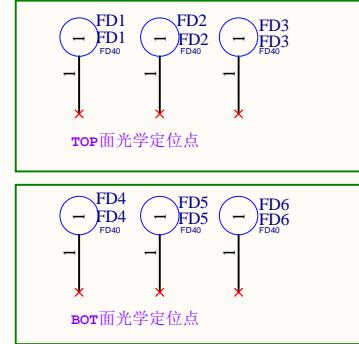
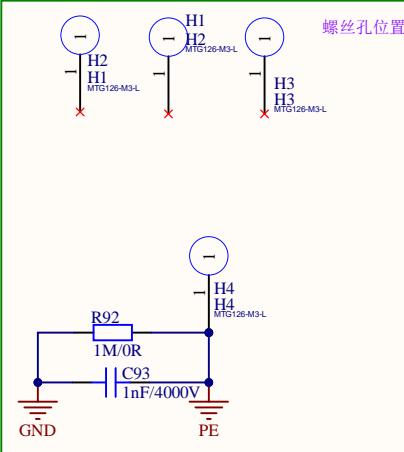
B枪CC1、枪锁接口



11 Connect

Size: A4	Number: 4/14	Revision: V01
Date: 2025/6/21	Time: 9:33:39	DrawnBy:

A



B

MARK1 MARK2 MARK3

PCB1



DH-DC-A7FloorBoard--V1.0-PCB

LOGO4

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BarCode

LOGO5

Cannot open file
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BarCode

12 Logo

Size: A4	Number: 4/14	Revision: V01
Date: 2025/6/21	Time: 9:33:39	DrawnBy:

A

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