



嵌入式系统开发

赵翔

电子信息工程学院电工电子国家级实验教学示范中心







Keil集成开发环境





集成开发环境

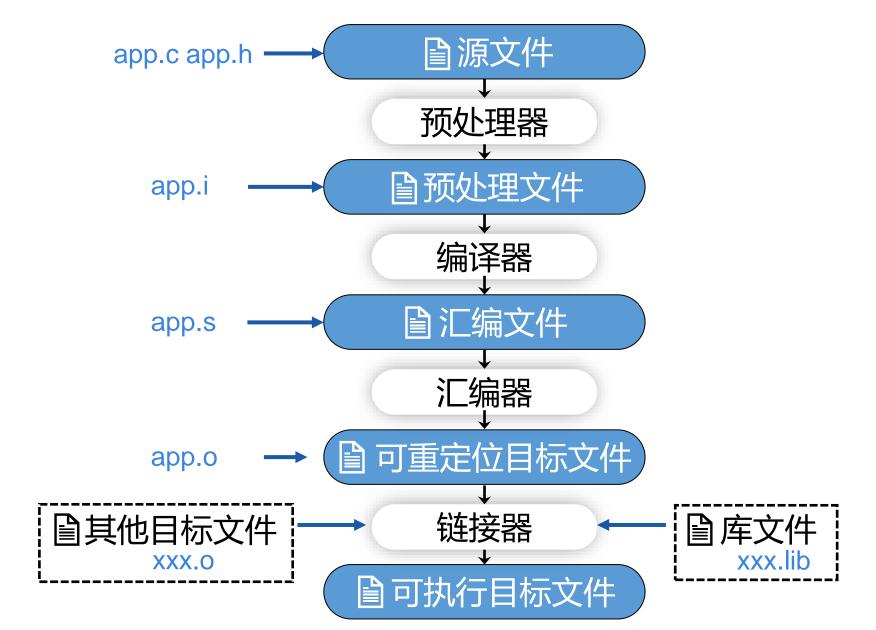


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嵌入式软件生成过程



预处理器

编译器

汇编器

链接器

C/C++运行库

代码编辑器

调试器

项目管理器

软件开发包管理工具

集成开发环境

armkeil

fi

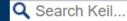
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Embedded Development Tools



ULINK*plus*

Debug adapter for power optimization and test automation

arm DESIGNSTART



Creating a custom chip has never been easier...

arm KEIL MDK

Best tools for STM32F0/G0/L0 available for free!

◆ Downloads



MDK Microcontroller Development Kit

Keil MDK is the complete software development environment for a range of Arm Cortex-M based microcontroller devices. MDK includes the μ Vision IDE and debugger, Arm C/C++ compiler, and essential middleware components. It supports all silicon vendors with more than 9,500 devices and is easy to learn and use.

Watch the workshop from Arm DevSummit 2021 introducing Keil Studio:

Introducing Keil Studio and Cloud-based Development for IoT and Embedded Applications

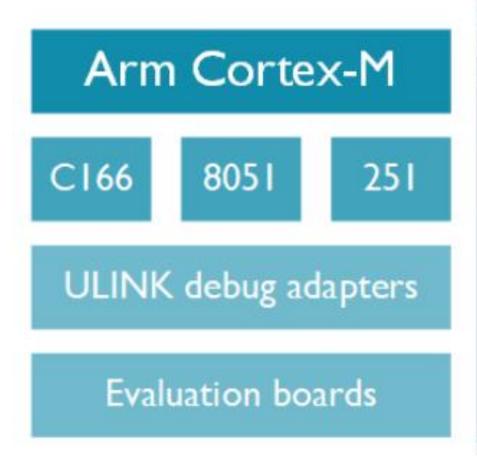
News

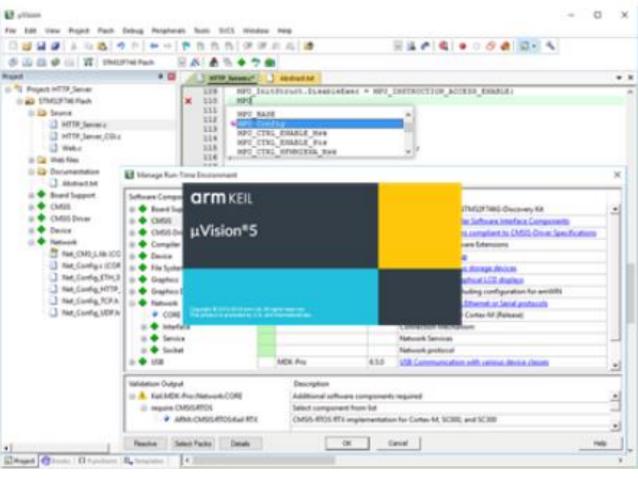
- Arm introduces MDK-Community edition
- Keil MDK supports Arm Virtual Hardware
- Keil Studio workshop available

Updates

- MDK-ARM V5.37
- Notify Me!
 Receive e-mail when new updates are available.

Events







MDK-Arm

Version 5.37 (May 2022)

Development environment for Cortex and Arm devices.



C51

Version 9.60a (May 2019)

Development tools for all 8051 devices.



C251

Version 5.60 (May 2018)

Development tools for all 80251 devices.



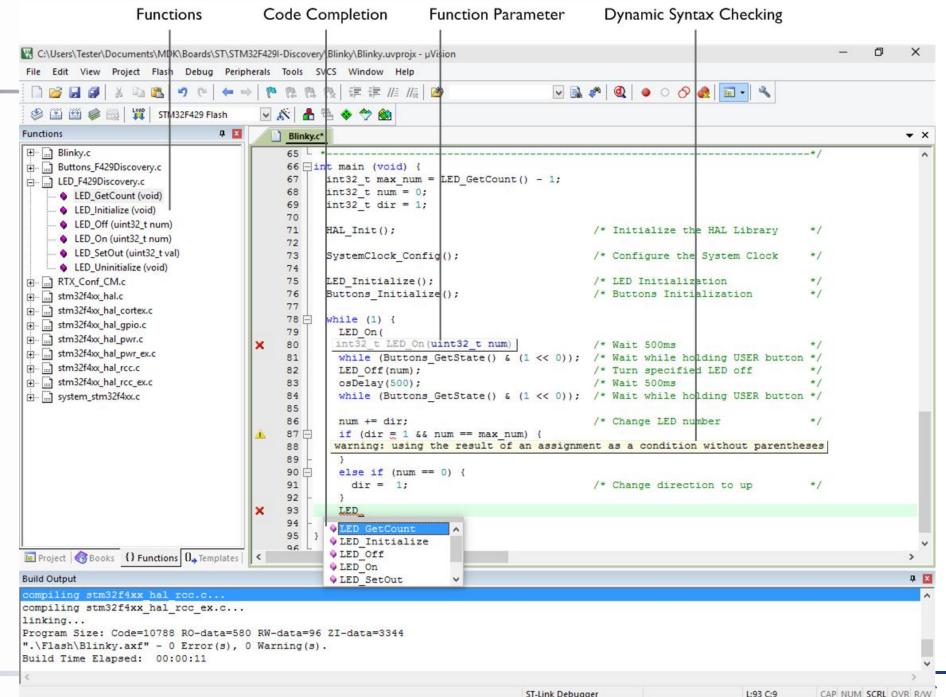
C166

Version 7.57 (May 2018)

Development tools for C166, XC166, & XC2000 MCUs.

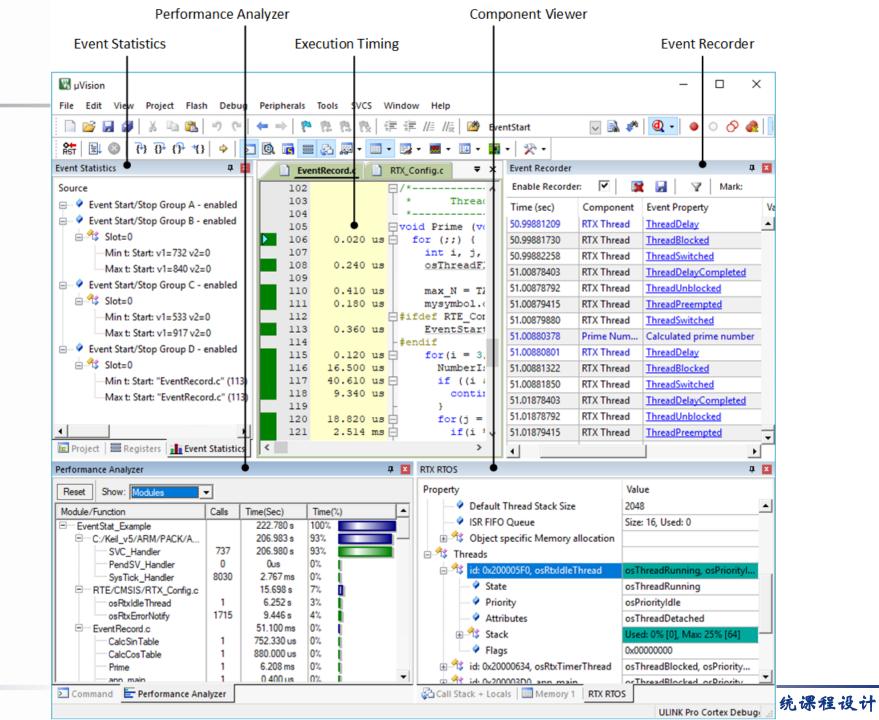


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- •编辑窗口
- •信息窗口
- 状态栏

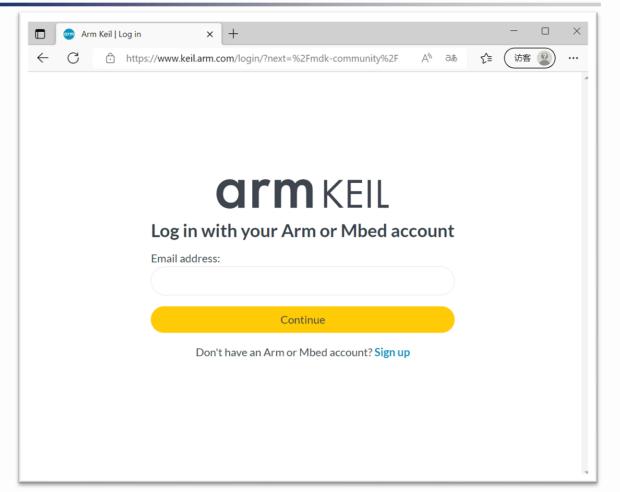




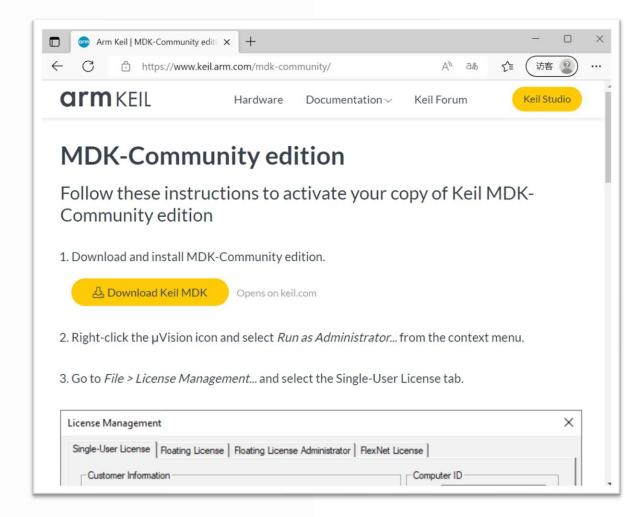
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社区版

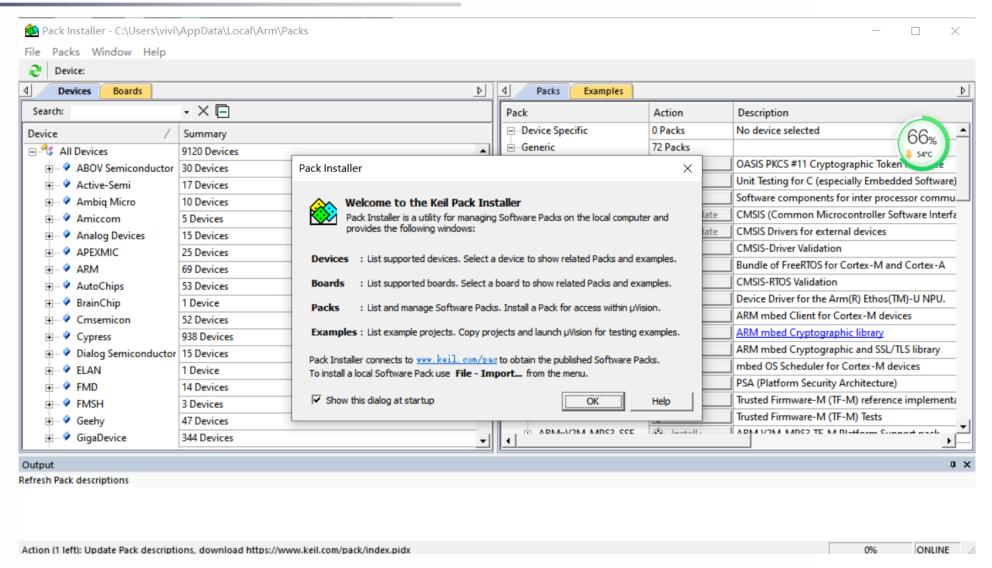


https://www.keil.arm.com/mdk-community/





Pack







嵌入式软件生成主要步骤

- ▲ 创建项目
- ▲ 给项目添加文件
 - 加入已有的源文件、编译过的目标代码
 - 建立新文件并加入项目
- ▲ 设置编译和连接参数
 - 选择处理器 (内核/芯片)
 - 选择运行库配置和选项
 - 设置编译器预处理命令、优化参数、头文件路径等
- ▲ 设置调试/下载方式
 - 选择调试器驱动
 - 设定下载、调试运行环境







STM32驱动函数库







实现PBO输出高电平:

- GPIOB->BSRR = 0×00001 ;
- HAL_GPIO_WritePin(GPIOB,GPIO_PIN_0, 1);

寄存器

VS

HAL库



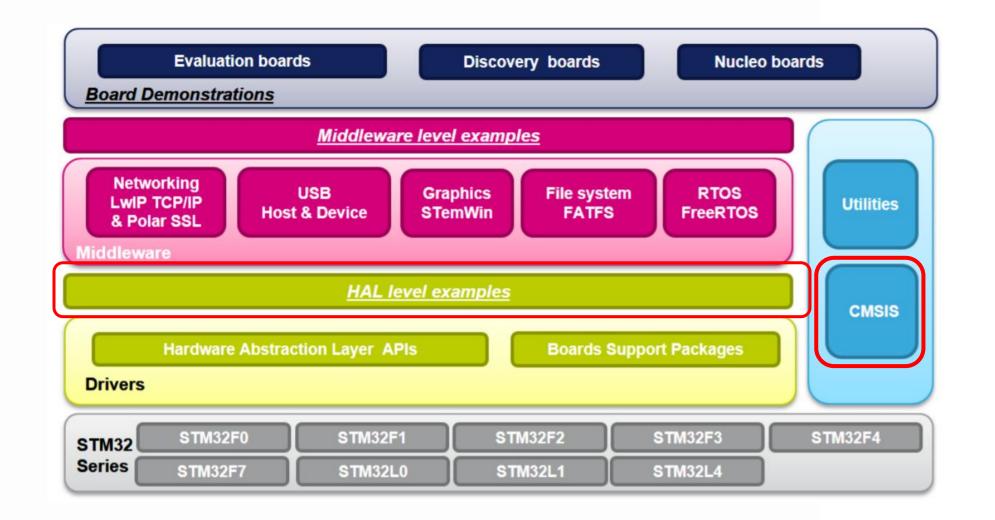


驱动函数库

```
/* IO operation functions ***********************************
GPIO PinState HAL GPIO ReadPin(GPIO TypeDef* GPIOx, uint16 t GPIO Pin);
void HAL GPIO WritePin (GPIO TypeDef* GPIOx, uint16 t GPIO Pin, GPIO PinState PinState);
void HAL GPIO TogglePin (GPIO TypeDef* GPIOx, uint16 t GPIO Pin);
HAL StatusTypeDef HAL GPIO LockPin(GPIO TypeDef* GPIOx, uint16 t GPIO Pin);
void HAL GPIO EXTI IRQHandler (uint16 t GPIO Pin);
void HAL GPIO EXTI Callback (uint16 t GPIO Pin);
HAL_StatusTypeDef HAL_ADC_Start(ADC_HandleTypeDef* hadc);
HAL StatusTypeDef HAL ADC Stop(ADC HandleTypeDef* hadc);
HAL StatusTypeDef HAL ADC PollForConversion(ADC HandleTypeDef* hadc, uint32 t Timeout);
HAL StatusTypeDef HAL ADC PollForEvent(ADC HandleTypeDef* hadc, uint32 t EventType, uint32 t Timeout);
HAL StatusTypeDef HAL ADC Start IT (ADC HandleTypeDef* hadc);
HAL StatusTypeDef HAL ADC Stop IT(ADC HandleTypeDef* hadc);
                 HAL ADC IRQHandler (ADC HandleTypeDef* hadc);
void
HAL StatusTypeDef HAL ADC Start DMA(ADC HandleTypeDef* hadc, uint32 t* pData, uint32 t Length);
HAL StatusTypeDef HAL ADC Stop DMA(ADC HandleTypeDef* hadc);
uint32 t
                HAL ADC GetValue(ADC HandleTypeDef* hadc);
          HAL ADC ConvCpltCallback(ADC HandleTypeDef* hadc);
void
          HAL ADC ConvHalfCpltCallback(ADC HandleTypeDef* hadc);
void
          HAL ADC LevelOutOfWindowCallback(ADC_HandleTypeDef* hadc);
void
          HAL ADC ErrorCallback(ADC HandleTypeDef *hadc);
void
```



驱动函数库

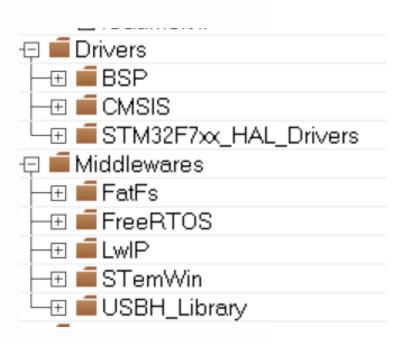






STM32的HAL驱动函数库

- > STM32Cube_FW_F7_V1.15.0 > Drivers
 - 名称
 - BSP
 - CMSIS
 - STM32F7xx_HAL_Driver





STM32的HAL驱动函数库

HAL overview HAL file components

File	Description
stm32f4xx_hal_ppp.c/.h	peripheral driver with portable APIs
stm32f4xx_hal_ppp_ex.c/.h	extended peripheral features APIs
stm32f4xx_hal.c	contains HAL common APIs (HAL_Init, HAL_DeInit, HAL_Delay,)
stm32f4xx_hal.h	HAL header file, it should be included in user code
stm32f4xx_hal_conf.h	config file for HAL, should be customized by user to select the peripherals to be included
stm32f4xx_hal_def.h	contains HAL common typedefs and macros
stm32f4xx_ll_ppp.c	implements low level functions in case of some complex peripherals, they are called from stm32f4xx_hal_ppp.c







STM32CubeMX





```
IO uint32 t CR1;
                                                                                                                             /*! < TIM control register 1,
                                                                                                                                                                       Address offset: 0x00 */
                                                                                                   __IO uint32 t CR2;
                                                                                                                             /*! < TIM control register 2,
                                                                                                                                                                       Address offset: 0x04 */
                                                                                                   IO uint32 t SMCR;
                                                                                                                             /*! < TIM slave mode control register,
                                                                                                                                                                       Address offset: 0x08 */
                                                                                                  IO uint32 t DIER;
                                                                                                                             /*!< TIM DMA/interrupt enable register,
                                                                                                                                                                      Address offset: 0x0C */
                                                                                                   IO uint32 t SR;
                                                                                                                             /*! < TIM status register,
                                                                                                                                                                       Address offset: 0x10 */
                                                                                                    IO uint32 t EGR;
                                                                                                                             /*! < TIM event generation register,
                                                                                                                                                                       Address offset: 0x14 */
                                                                                                    IO uint32 t CCMR1;
                                                                                                                             /*! < TIM capture/compare mode register 1, Address offset: 0x18 */
                                                                                                   IO uint32 t CCMR2;
                                                                                                                             /*!< TIM capture/compare mode register 2, Address offset: 0x1C */
                                                                                                   IO uint32 t CCER;
                                                                                                                             /*! TIM capture/compare enable register, Address offset: 0x20 */
  * @brief Initializes the TIM Time base Unit according to the specified
                                                                                                   IO uint32 t CNT;
                                                                                                                             /*! < TIM counter register,
                                                                                                                                                                       Address offset: 0x24 */
             parameters in the TIM Handle Type Def and initialize the associated handle.
                                                                                                  __IO uint32 t PSC;
                                                                                                                             /*!< TIM prescaler,
                                                                                                                                                                       Address offset: 0x28 */
            Switching from Center Aligned counter mode to Edge counter mode (or reverse)
                                                                                                   IO uint32 t ARR;
                                                                                                                             /*! < TIM auto-reload register,
                                                                                                                                                                       Address offset: 0x2C */
             requires a timer reset to avoid unexpected direction
                                                                                                  IO uint32 t RCR;
                                                                                                                             /*!< TIM repetition counter register,
                                                                                                                                                                       Address offset: 0x30 */
            due to DIR bit readonly in center aligned mode.
                                                                                                   IO uint32 t CCR1;
                                                                                                                             /*! < TIM capture/compare register 1,
                                                                                                                                                                       Address offset: 0x34 */
            Ex: call @ref HAL TIM Base DeInit / before HAL TIM Base Init()
                                                                                                    IO uint32 t CCR2;
                                                                                                                             /*! < TIM capture/compare register 2,
                                                                                                                                                                       Address offset: 0x38 */
  * @param htim TIM Base handle
                                                                                                   IO uint32 t CCR3;
                                                                                                                             /*! < TIM capture/compare register 3,
                                                                                                                                                                       Address offset: 0x3C */
  * @retval HAL status
                                                                                                   IO uint32 t CCR4;
                                                                                                                             /*! < TIM capture/compare register 4,
                                                                                                                                                                       Address offset: 0x40 */
                                                                                                    IO uint32 t BDTR;
                                                                                                                             /*!< TIM break and dead-time register,
                                                                                                                                                                       Address offset: 0x44 */
HAL StatusTypeDef HAL TIM Base Init
                                                                                                    IO uint32 t DCR;
                                                                                                                             /*! < TIM DMA control register,
                                                                                                                                                                       Address offset: 0x48 */
                                                                                                    IO uint32 t DMAR;
                                                                                                                             /*!< TIM DMA address for full transfer, Address offset: 0x4C */
  /* Check the TIM handle
                            allocation */
                                                                                                    IO uint32 t OR;
                                                                                                                             /*! < TIM option register,
                                                                                                                                                                       Address offset: 0x50 */
  if (htim == NULL
                                                                                                   IO uint32 t CCMR3;
                                                                                                                             /*! < TIM capture/compare mode register 3,
                                                                                                                                                                             Address offset: 0x54 */
                   typedef struct
                     endi / /* USE_HAL_TIM_REGISTER_CALLBACKS */
                                                                                                   IO uint32 t CCR5;
                                                                                                                             /*! < TIM capture/compare mode register5,
                                                                                                                                                                             Address offset: 0x58 */
    return HAL ER
                                                                                                   IO uint32_t CCR6;
                                                                                                                             /*!< TIM capture/compare mode register6,
                                                                                                                                                                             Address offset: 0x5C */
                                                         /*! < Register base address
                                                         /*! TIM Time Base required parameters */
                     TIM Base InitTypeDef
                                                                                                  TIM TypeDef;
                     AL TIM ActiveChannel
                                            Channel;
                                                           *!< Active channel
  /* Check the par
                                            *hdma[7];
                                                         /*! < DMA Handlers are
                     DNA HandleTypeDef
  assert param(IS
                                                             This array is accessed by a gre
                                                                                         DMA Handle index */
                     HAL LockTypeDef
                                            Lock:
                                                         /*! < Locking object
  assert param(IS
                      IO HAL TIM StateTypeDef State;
                                                         /*! < TIM operation state
  assert_param(IS
  assert param(IS
                   #if (USE HAL TIM REGISTER CALLBACKS == 1)
                     void (* Base_MspInitCallback) (struct __TIM_HandleTypeDef *htim);
                                                                                       /*! < TIM Base Msp Init Callback
                                                                                                                                          +1
                     void (
 if (htim->State
                             typedef struct
                     void (
                     void (*
    /* Allocate 1
                     void (
                               uint32 t Prescaler;
                                                            /*!< Specifies the prescaler value used to divide the TIM clock.
                     void (
   htim->Lock =
                                                                  This parameter can be a number between Min Data = 0x0000 and Max Data = 0xFFFF */
                     void (*
                     void (*
#if (USE HAL TIM )
                     void (*
                               uint32 t CounterMode;
                                                            /*! < Specifies the counter mode.
    /* Reset inte
                     void (*
                                                                  This parameter can be a value of @ref TIM Counter Mode */
                     void (*
    TIM ResetCallb
                     void (
                               uint32 t Period;
                                                            /*!< Specifies the period value to be loaded into the active
    if (htim->Base
                     void (*
                                                                  Auto-Reload Register at the next update event.
                     void (*
                                                                  This parameter can be a number between Min Data = 0x0000 and Max Data = 0xFFFF. */
                     void (*
      htim->Base 1
                     void (*
                     void (*
                               uint32 t ClockDivision;
                                                             /*!< Specifies the clock division.
                     void (*
    /* Init the .
                                                                  This parameter can be a value of @ref TIM ClockDivision */
                     void (*
    htim->Base Ms
                     void (*
#else
                               uint32 t RepetitionCounter; /*!< Specifies the repetition counter value. Each time the RCR downcounter
    /* Init the 1
                     void (*
                                                                   reaches zero, an update event is generated and counting restarts
                     void (*
    HAL TIM Base
                                                                   from the RCR value (N).
                     void (*
#endif /* USE HAL
                     void (*
                                                                   This means in PWM mode that (N+1) corresponds to:
                     void (*
                                                                       - the number of PWM periods in edge-aligned mode
                     void (*
                                                                       - the number of half PWM period in center-aligned mode
                    endif /
                     TIM Hand
                                                                    GP timers: this parameter must be a number between Min Data = 0x00 and Max Data = 0xFF.
                                                                    Advanced timers: this parameter must be a number between Min Data = 0x0000 and Max Data = 0xFFFF. */
                               uint32 t AutoReloadPreload; /*! < Specifies the auto-reload preload.
                                                                  This parameter can be a value of @ref TIM AutoReloadPreload */
                              TIM Base InitTypeDef;
```

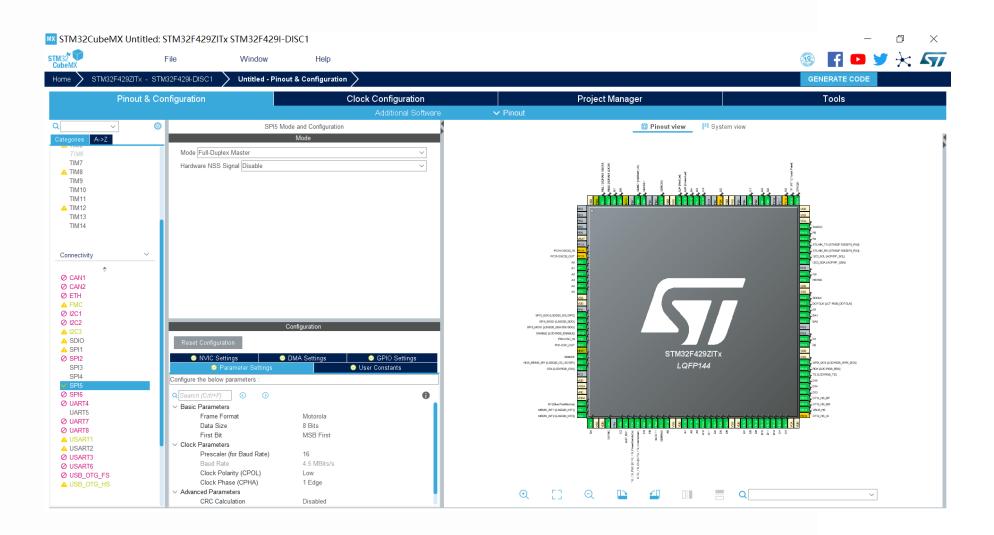
ypedef struct







ST - CubeMX







```
void HAL_GPIO_Init(GPIO_TypeDef *GPIOx, GPIO_InitTypeDef *GPIO_Init)
  f
uint32_t position = 0x00;
uint32_t ioposition = 0x00;
uint32_t iocurrent = 0x00;
uint32_t temp = 0x00;
  /* Check the parameters */
assert param(IS GFIO_ALL INSTANCE(GFIOX));
assert_param(IS_GFIO_FIN(GFIO_Init->Pin));
assert_param(IS_GFIO_MODE(GFIO_Init->Mode));
    assert param(IS GPIO PULL(GPIO Init->Pull));
    /* Configure the port pins */
for(position - 0; position < GPIO_NUMBER; position++)
       ioposition = ((uint32_t)0x01) << position;
/* Get the current IO position */
iocurrent = (uint32_t)(GPIO_Init->Pin) & ioposition;
        if(iocurrent -- ioposition)
             temp |- ((uint32_t)(GPIO_Init->Alternate) << (((uint32_t)position & (uint32_t)0x07) * 4));
GPIOx->AFR[position >> 3] - temp;
         /* Configure 20 Direction mode (Imput, Output, Alternate or Analog) */
temp - GTION-MMOREN MODERS << (position * 2));
temp 1 - (GCTO_Init-Media (GTIO_MODER) << (position * 2));
GTION-MMOREN t-temp;
          /* In case of Output or Altornate function mode selection */
11(CPTO_Init->Mode -- CPTO_MODE_OUTPUT_PP) || (CPTO_Init->Mode -- CPTO_MODE_AF_PP) ||
(CPTO_Init->Mode -- CPTO_MODE_OUTPUT_OD) || (CPTO_Init->Mode -- CPTO_MODE_AF_OD))
             /* Check the Speed parameter */
assert_param(IS_GFIO_SPEED(GFIO_Init->Speed));
/* Configure the IO_Speed '',
temp - GFIOA->OSFEEDER;
temp 4- (GFIO_OSFEEDER) < (position * 2));
temp 1- (GFIO_Init->Speed < (position * 2));
CFIOA->OSFEEDER
             /* Configure the so varyor. *;pr ,
tamp = GFID-OTTFER;
tamp s - (GFID OTTFER OT 0 << position);
tamp l - ((GFID_Init-Needs & GFID_OUTFOT_TYPE) >> 4) << position);
GFIOR->OTTFER = tamp;
           /* Activate the Pull-up or Pull down resistor for the current IO */
          tomp 4 - (GPIO_PUPDR_PUPDRO << (position * 2));
tomp |- ((GPIO_Init->Pull) << (position * 2));
GPIOx->PUPDR = tomp;
          /* Configure the External Interrupt or event for the current IO */
if((GPIO_Init->Mode & EXTI_MODE) == EXTI_MODE)
             temp = STECPG-METTICR[position >> 2];
temp s - (((uint2; 1)0x0F) << (4 * (position & 0x03)));
temp l - ((uint2; 1)(0FDC over INDEX(GFOxs)) << (4 * (position & 0x03)));
STECPG-METTICR[position >> 2] - temp;
               /* Clear EXTI line configuration */
              temp = EXTI->IMR;
temp &= -((uint32_t)iocurrent);
if((GPIO_Init->Mode & GPIO_MODE_IT) == GPIO_MODE_IT)
                   temp |- iocurrent;
               EXTI->IMR - temps
              temp = EXTI->EMR;
temp &= -((uin:32_t))ocurrent);
1f((GPIO_Init->Mode & GPIO_MODE_EVT) -- GPIO_MODE_EVT)
                    temp |- iocurrent;
                EXTI->EMR - temp;
                /* Clear Rising Falling edge configuration */
               temp 4- -((uint32 t)iocurrent);
1f((GPIO_Init->Mode & RISING_EDGE) -- RISING_EDGE)
                    temp |- iocurrent;
```

工作代码

void HAL_GPIO_WritePin(GPIO_TypeDef* GPIOx, uint16_t GPIO_Pin, GPIO_PinState PinState)

/* Check the parameters */
assert_param(IS_GPIO_PIN(GPIO_Pin));
assert_param(IS_GPIO_PIN_ACTION(PinState));

if (PinState != GPIO_PIN_RESET)
{
 GPIOx->BSRR = GPIO_Pin;
}
else

GPIOx->BSRR = (uint32_t)GPIO_Pin << 16;</pre>







STM32CubeMX







STM32CubeMX

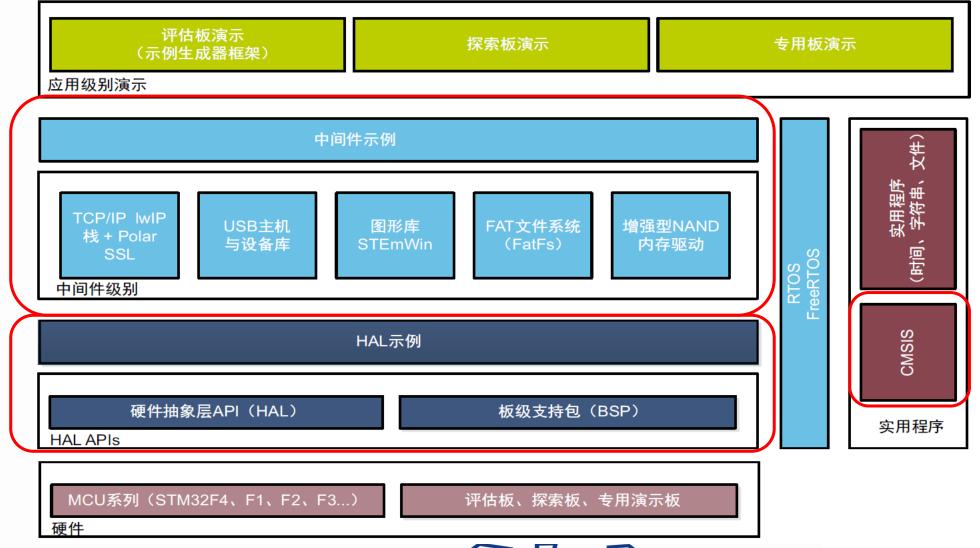
→ STM32CubeMX是用于STM32微控制器的图形工具。它既可作为独立应用,也可作为Eclipse插件集成到集成开 发环境(IDE)中。

♣ STM32CubeMX 有以下主要特性:

- · 微控制器选择方便,覆盖整个STM32产品
- 可从一系列意法半导体的开发板中选择板子
- 微控制器配置简单(引脚、时钟树、外设、中间件)
- 将以前保存的配置导入新的MCU项目即可轻松地转换到其他微控制器
- · 将当前配置轻松地导出到兼容的MCU
- 生成配置报告
- 为IAR KEIL GCC等开发环境生成嵌入C项目包括生成的初始化代码、HAL驱动程序、中间件协议栈,以及所有相关文件。









```
int main(void)
  /* USER CODE BEGIN 1 */
  /* USER CODE END 1 */
  /* MCU Configuration-----
  /* Reset of all peripherals, Initializes the Flash interface and the Systick. |*/
 HAL_Init();
  /* USER CODE BEGIN Init */
  /* USER CODE END Init */
  /* Configure the system clock */
  SystemClock Config();
  /* USER CODE BEGIN SysInit */
  /* USER CODE END SysInit */
```





需完成的实验

- → 04 嵌入式系统软件开发工具
 - 4-5 实验-Keil集成开发环境
 - 4-8 实验-STM32CubeMX图形化开发工具

