**5.2.3.3. Pin Assignment**

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| **Pin Name** | **Assignment** | **Description** |
| RA0:1 | AN5, AN6 | LDR Sensors |
| RA2:3 | Digital I/O | DC motor enable pins (2) |
| RA4:7 | Digital I/O | Solenoid control (B,W,S,N) |
| RB0 |  |  |
| RB1 | INT1 (during standby) | Keypad interrupt |
| RB2:3 | Digital I/O | DC motor control |
| RB4:7 | Digital I/O | Keypad data |
| RC0:2 | Digital I/O | Vibration motor control (B, W, S) |
| RC3 | SCL | Serial clock I/O for I2C mode |
| RC4 | SDA | I2C data I/O |
| RC5:7 | Digital I/O | Microswitch data (B,W,S) |
| RD0 | Digital I/O | Vibration motor control |
| RD1 | Digital I/O | Microswitch data (N) |
| RD2:7 | Digital I/O | Character LCD data |
| RE0:1 | Digital I/O | DC motor control |
| RE2 |  |  |
| RE3 | MCLR | Master clear (reset) input |

**TRIS Assignment:**

A – 0x03

B – 11110011

C – 11100000

D – 00000010

E – 0x00

**TODO:**

FIX OPERATION TIME (RTC)

PC Interface