

School Of Computer Science University Of Petroleum and Energy Studies P.O. Bidholi, Via-Prem Nagar DEHRADUN-248007

Issue Date:_

Bachelors of Technolgy in Computer Science & Engineering

Minor	<	Major	
Project Number			

Project Title

Data Link Layer(L2 Routing) Implementation (TCP/ IP Stack Functionality)

Mentor Name

Ms. Avita Katal

S.No	Rollnumber	Branch	Name	Role	Signature
1.	R171218058	CSE-DEVOPS	Kshitiz Saini		Kshitiz Saini
2.	R110218131	CSE-CCVT	Saloni Saxena		Saloni Saxena
3.	R-110218107	CSE-CCVT	Pratyusha Agarwal		Pratyusha Agarwal

		Project Mentor										Head	l Of Departm	ent					
		Title Approval R.No Timly Submission		Synopsis Mid-Tei		Mid-Term MARKS	End-Term MARKS	Term Progress MARKS		Total Marks	Projec Authenticity Satisfactory								
															Activity Coordinator				
Synopsis Evaluation																			
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			Reviewer	1	Rev	viewer 2		Re	eviewer 3		Review	Reviewer 4			Reviewer 5				
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								End	d-Term Evalu					1					
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Bachelors of Technolgy in Computer Science Engineering

Minor	V	Maior	

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Project Title	Data Link Layer(L2 Routing) (TCP/ IP Stack Functionality	Implen)	nentatio	n			Mentor	Nam	e	Ms.	Avita	Katal
Abstract	In today's world, fast and efficient communication on the network between the sender and receiver is very important. For this communication, data is converted into packets and sent over the network using various routing algorithms. In a network or over multiple networks, routing refers to the process of determining a path for a packet to travel from. L2 Routing is based on the concept of Data Link layer and happens when data is to be transferred between the same subnet.											
	In this project, we aim to implement the functionality of Data Link Layer like creating ARP tables, L2 Routing, MAC learning, L2 Switching & implementing VLan forwarding.											ıg,
Objective	The main objective of the project is to imple of Thrashing, Collision and reducing the broken For this, we will implement the APR Tables	adcast do	main.			,	,	•			kle the i	ssue
Methodology	Agile methodology of software development will be will consist of parallel development by different me the product's backlog (if any). The project can acc for requirement analysis and designing of the proj Control (Git in our case) and Makefile. Each devel designed for the reviewing as well as retrospection	embers of th ommodate o ect. One spr opment spri	e team. Each changes if re- int is specific nt is followed	n sprir quirec ally d ⊢by U	nt is pro I at any esigned Init Test	vided ample stage of the d for setting ting and an	e time to com e project. The the environm Integration Te	plete sprir ent lil esting	itself a its 1, 2 ke mai at the	s well and 3 ntainir end.	as to ma are spe ng the Ve	intain cifically rsion
Progress 1												
		Marks	10	10	10	10	10	10	10	10	10	10
Mentor Remark		Rollno/Mar ks(10)	Step 1	Step 2	Step 3	Step 4	Step 5	Step 6	Step 7	•	Mid- term	End- Term
		Date/Ment										
		or Signature										
Progress 2		-										
110516332												
		Marks	10	10	10	10	10	10	10	10	10	10
Mentor Remark		Rollno	Step 1	Step 2	Step 3	Step 4	Step 5	Step 6	Step 7	Syno psis	Mid- term	End- Term
		Date/Ment										
		Or Signature										
		Signature										

Guideline: 1) A project group can be of maximum 4 members and no alteration in the group member will be entertained later.

Guideline: 2) Methodology should have following steps Step1: Literature Review; Step2: Identification Of Requirement (Type Of Data source, Amount Of Data, & Format of Data); Step3: Identification of Algorithm; Step4: Comparative study; Step5: Design and Development of System/Architecture; Step 6: Implementation; Step7: Results Guideline:3) Student should upload softcopies of all the documents (reports and power point presentations) in "Project Directory", 24 hrs prior to evaluation.

5: Outstanding

Guideline:4) Panel member will give feedback to individual on the scale of 1 to 5 and this scale will change for defaulter i.e. 1 to 3 scale.

1: Poor 2: Average 3: Good 4: Excellent