2023 Digital IC Design Homework 1

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| NAME | | 李亦軒 | | | | | | |
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| **Functional Simulation Result** | | | | | | | | |
| Stage 1 | Pass | | Stage 2 | Pass | Stage 3 | Pass | Stage 4 | Pass |
| **Stage 1** | | | | | | | | |
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| **Stage 2** | | | | | | | | |
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| **Stage 3** | | | | | | | | |
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| **Stage 4** | | | | | | | | |
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| **Description of your design** | | | | | | | | |
| MMS\_4nums : 利用兩個8-bits的值暫存輸入兩兩比較的結果(最大值或最小值)，再讓這兩個值比大小，並利用輸入訊號的改變讓result存取對應的輸出值。  MMS\_8nums : 利用hierarchy的方式將兩個MMS\_4nums組合起來再利用comparator比較兩者大小，配合選擇線輸出對應值。 | | | | | | | | |