



Track: use netclass width



Via: use netclass sizes



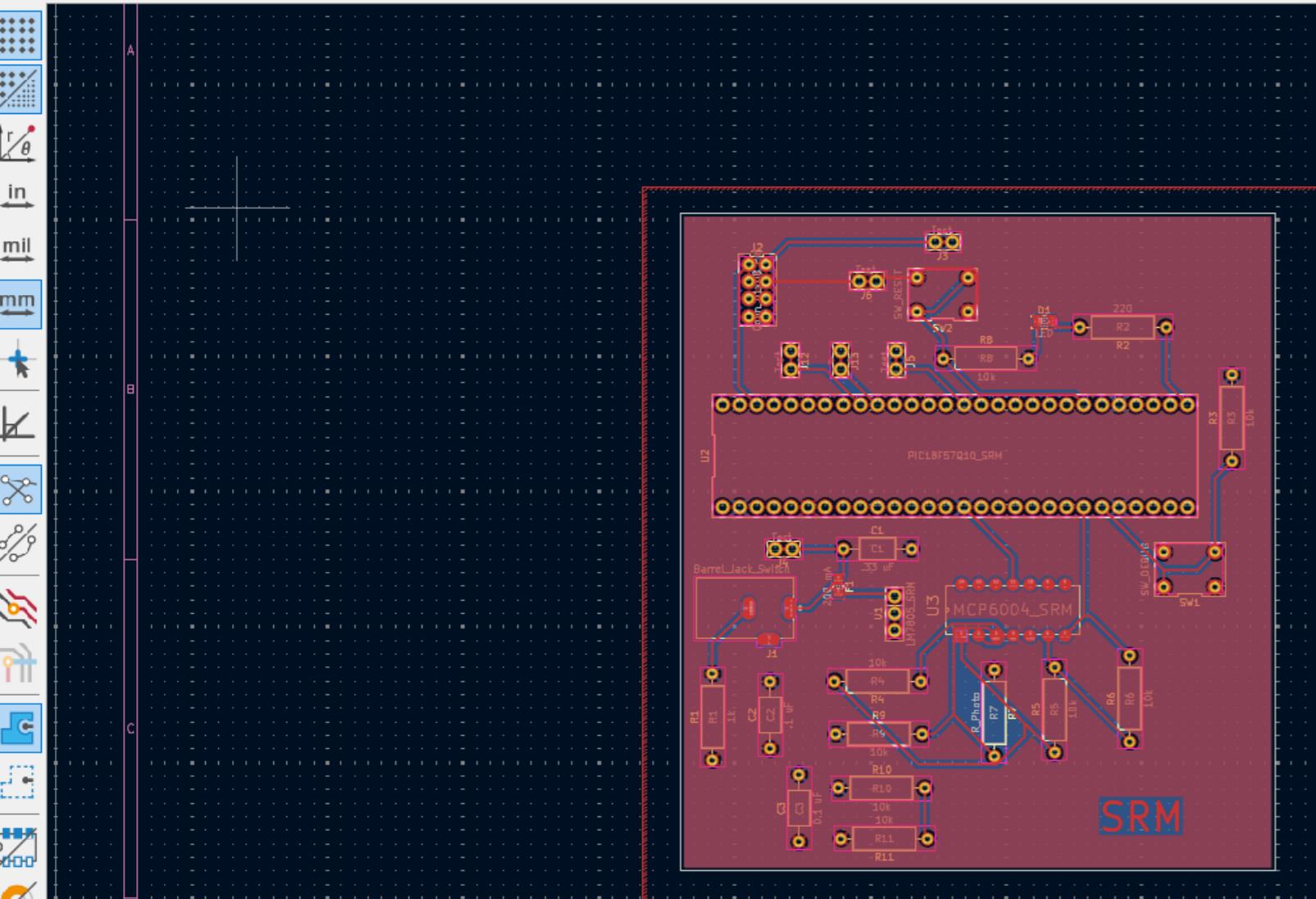
F.Cu (PgUp)



0.2000 mm (7.87 mils)



Zoom 1.50



Pads Vias Track Segments Nets Unrouted
136 0 87 73 0

file 'C:\Users\16238\OneDrive\Desktop\SubsystemDesign\SubsystemDesign.kicad_pc...' Z 1.44 X 26.8000 Y 58.4000 dx 26.8000 dy 58.4000 dist 64.2557 grid 0.2000 mm Select item(s)

Design Rules Checker

Refill all zones before performing DRC Test for parity between PCB and schematic
 Report all errors for each track

Violations (0) Unconnected Items (0) Schematic Parity (not run) Ignored Tests (5)

Show: All Errors (0) Warnings (0) Exclusions

Delete Marker Delete All Markers Run DRC Close

Presets (Ctrl+Tab):

Viewports (Shift+Tab):

Selection Filter

<input checked="" type="checkbox"/> All items	<input type="checkbox"/> Locked items
<input checked="" type="checkbox"/> Footprints	<input checked="" type="checkbox"/> Text
<input checked="" type="checkbox"/> Tracks	<input checked="" type="checkbox"/> Vias
<input checked="" type="checkbox"/> Pads	<input checked="" type="checkbox"/> Graphics
<input checked="" type="checkbox"/> Zones	<input checked="" type="checkbox"/> Rule Areas
<input checked="" type="checkbox"/> Dimensions	<input checked="" type="checkbox"/> Other items