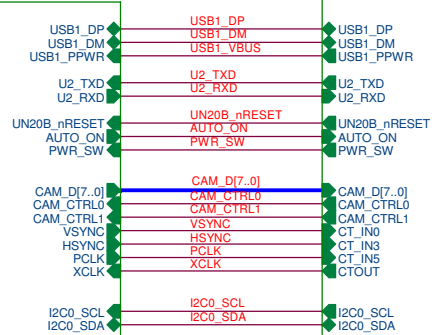
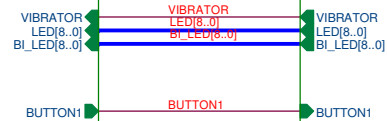


# Sensor Interface



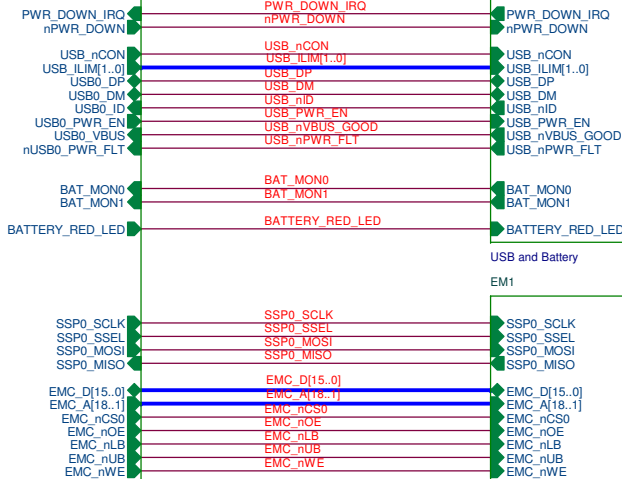
# Sensor Interface

# User Interface



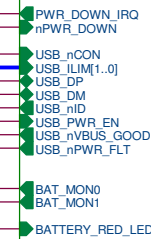
# User Interface

# CPU



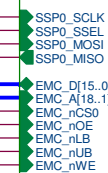
# CPU

# USB and Battery



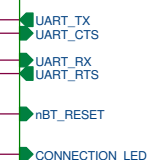
# USB and Battery

# EM1



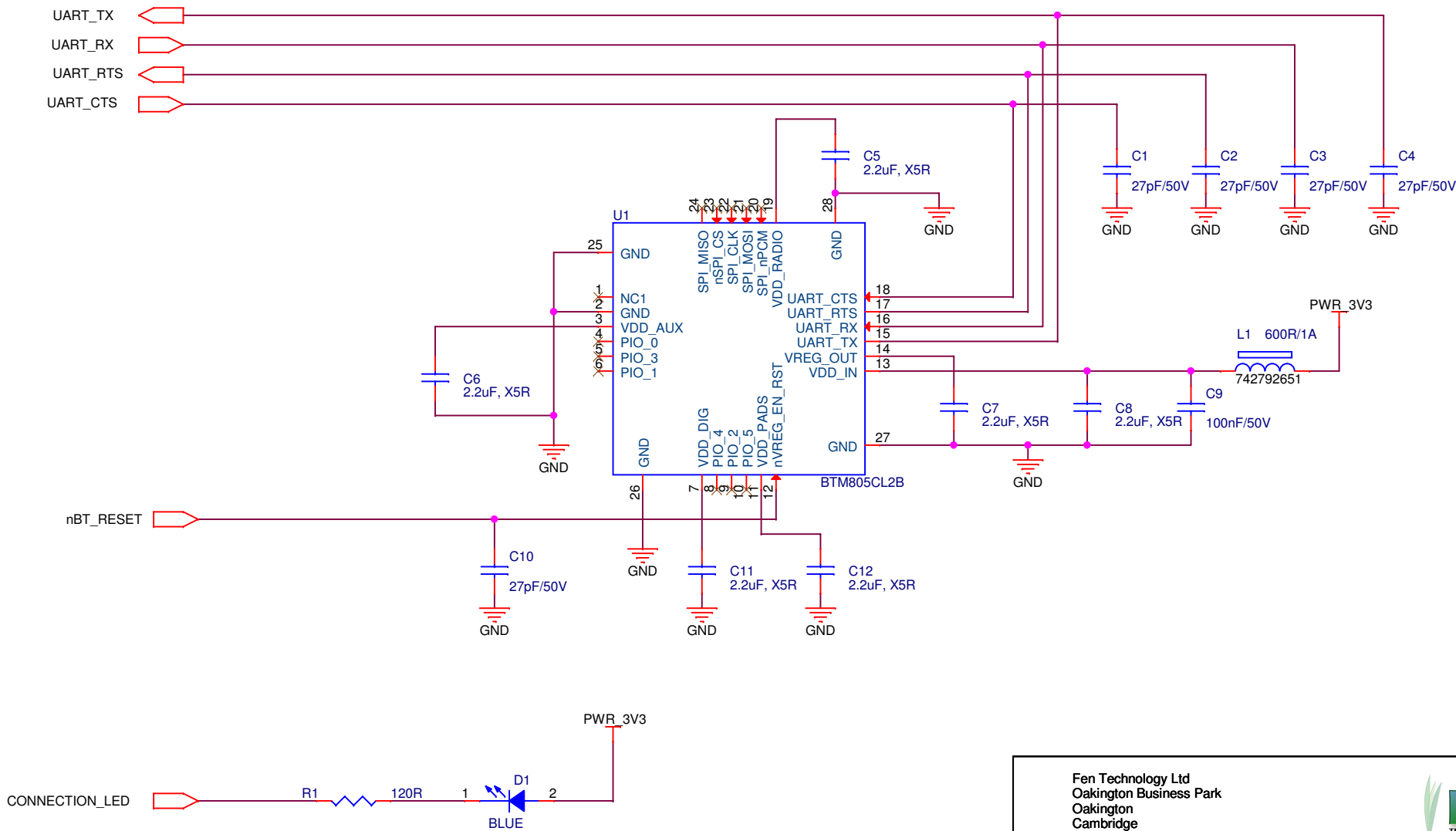
# External Memory


# Bluetooth

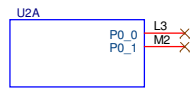


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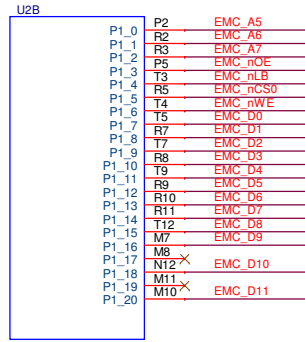
TPVIA2



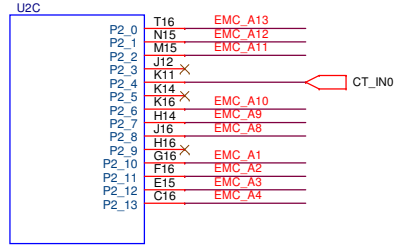
Fen Technology Ltd Oakington Business Park Oakington Cambridge CB24 3DQ			
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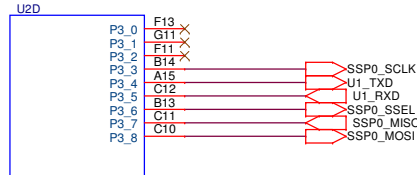
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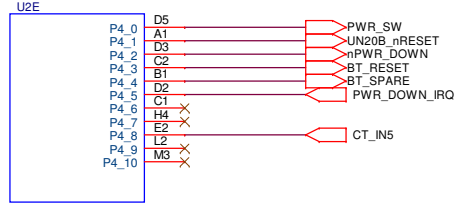
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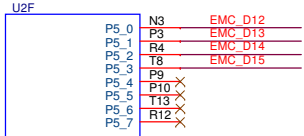
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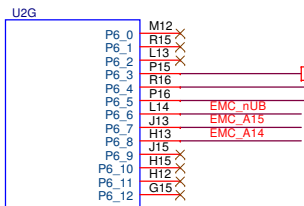
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LPC1833\_LBGA256



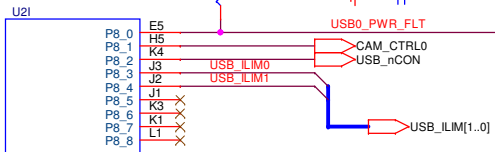
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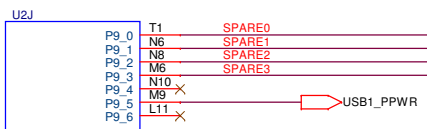
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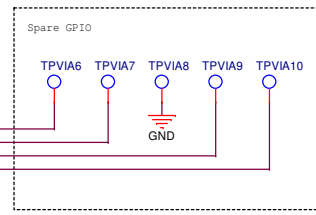
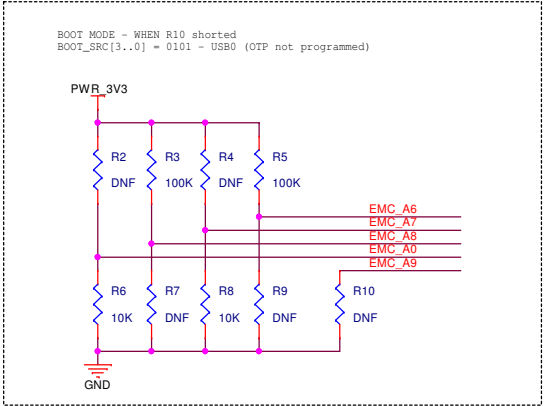
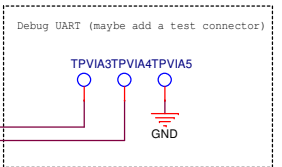
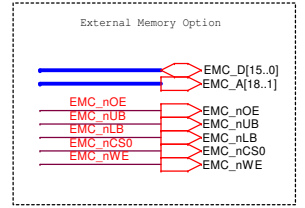
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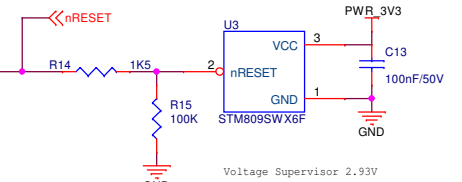
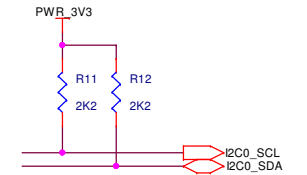
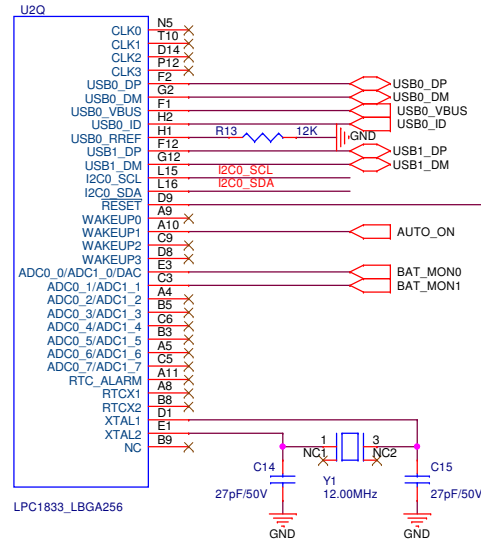
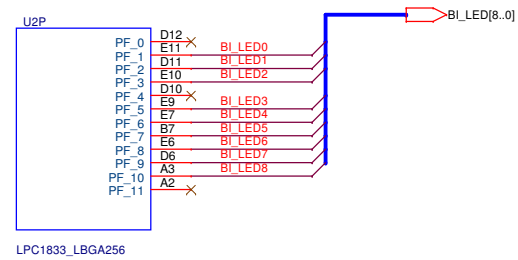
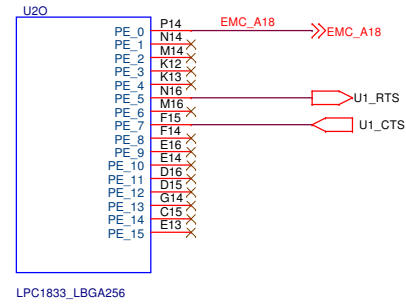
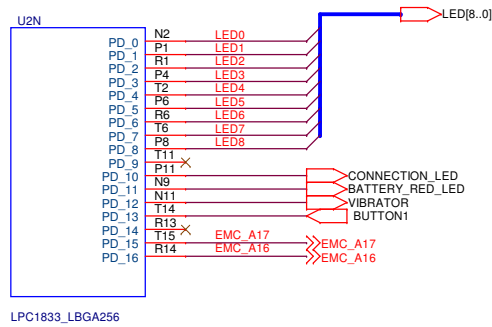
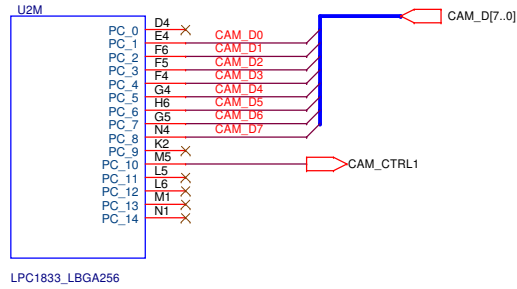
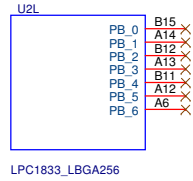
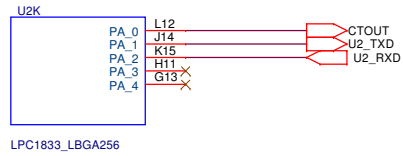


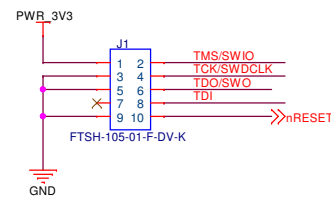
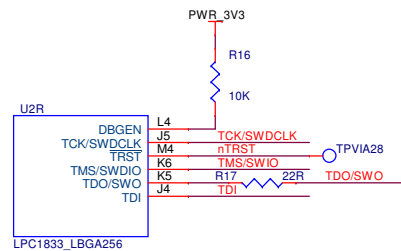
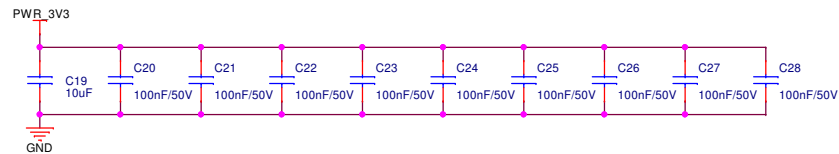
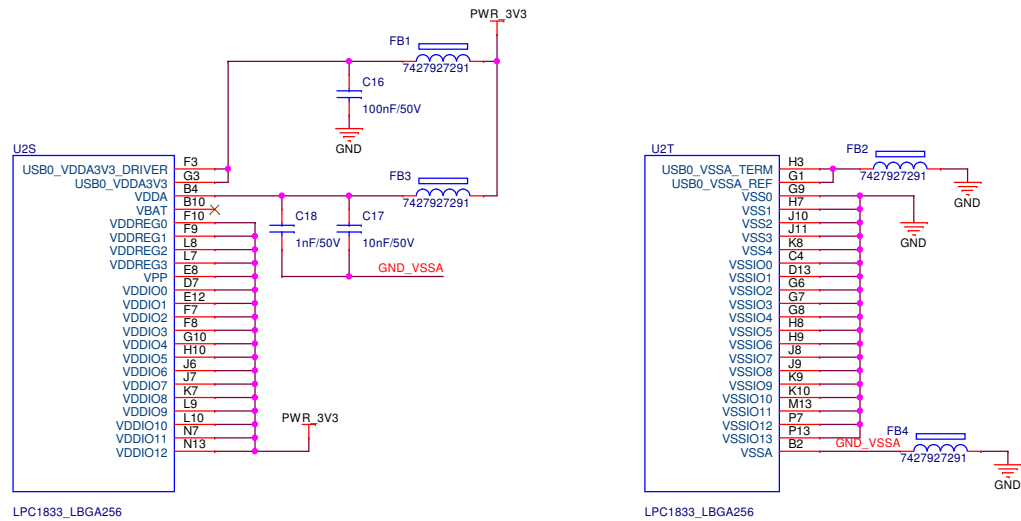
LPC1833\_LBGA256



LPC1833\_LBGA256



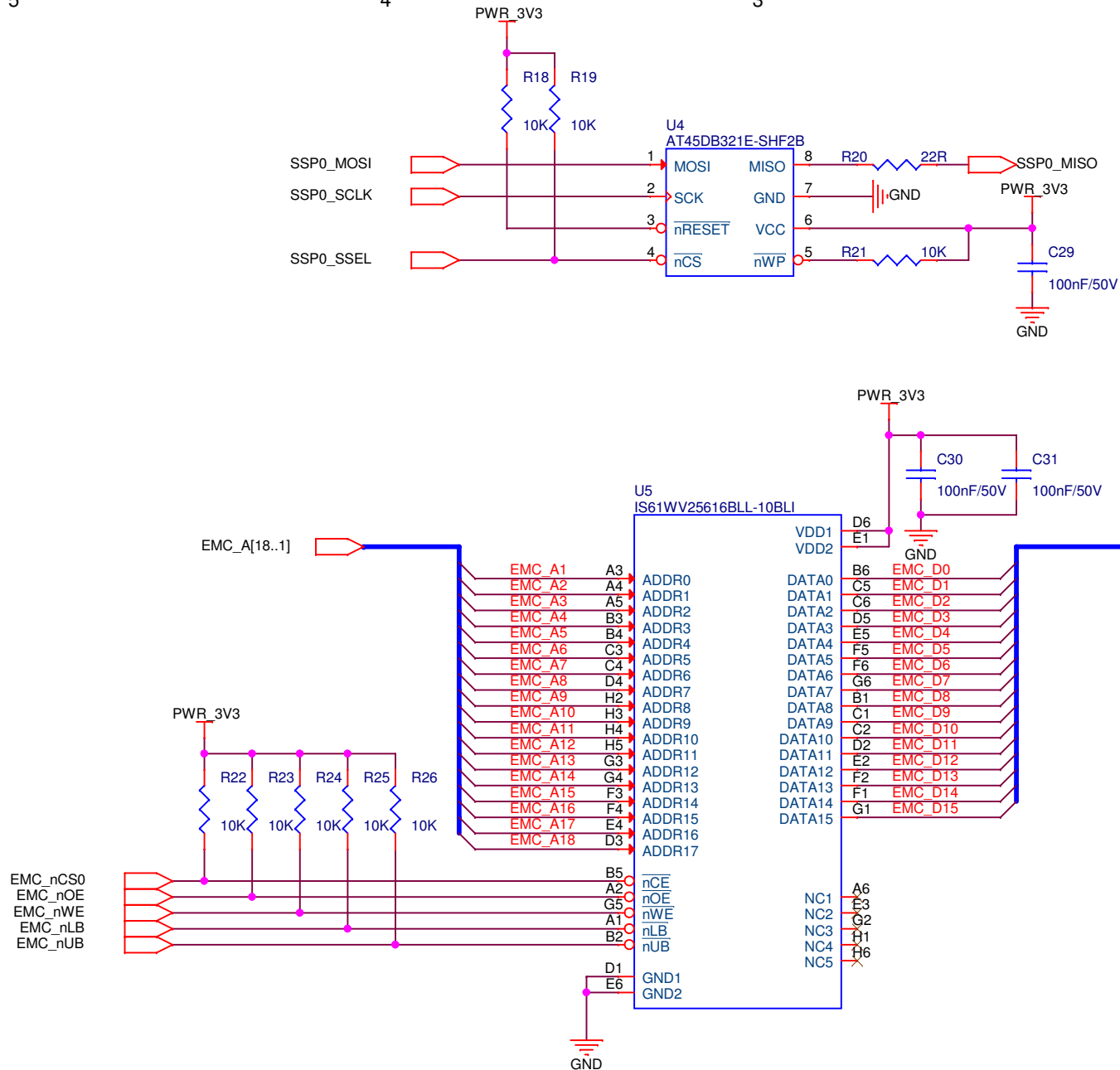




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Title

External Memory

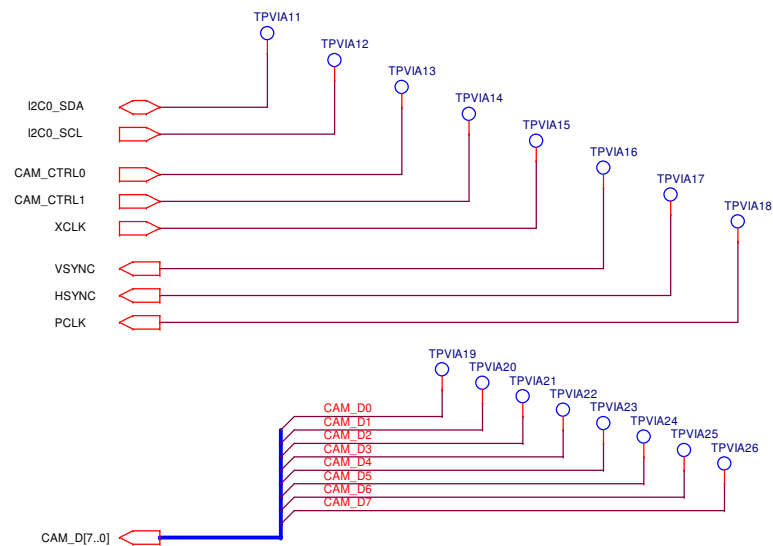
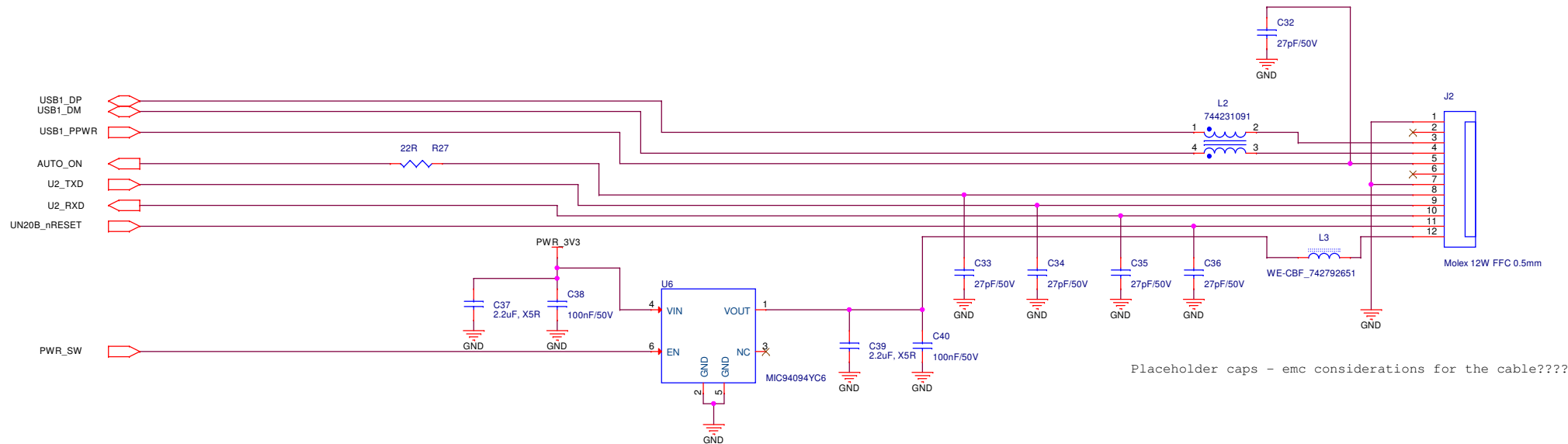
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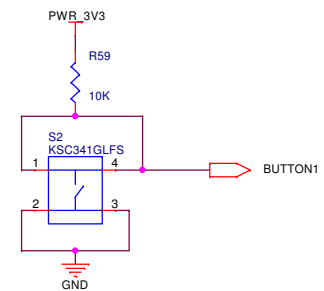
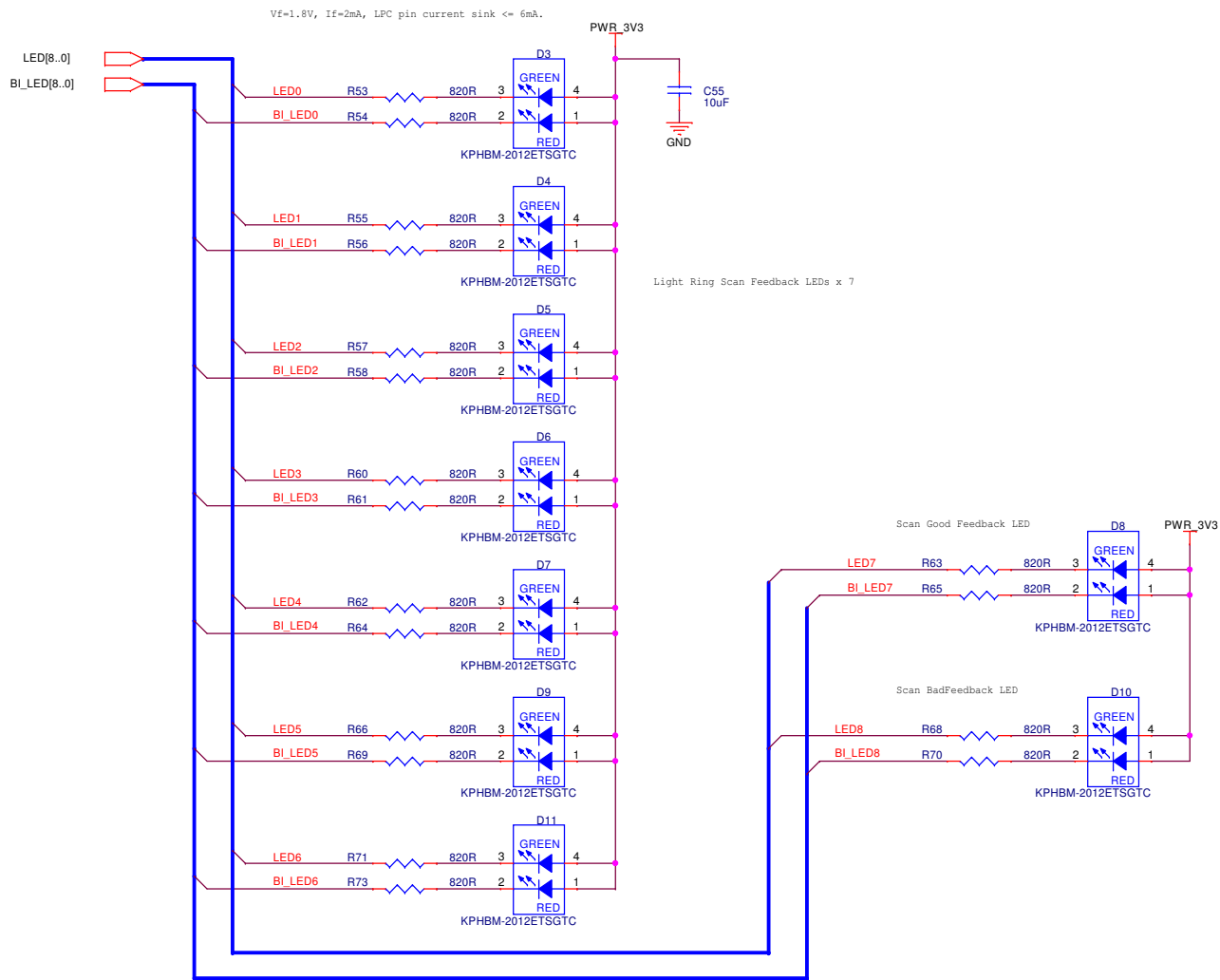
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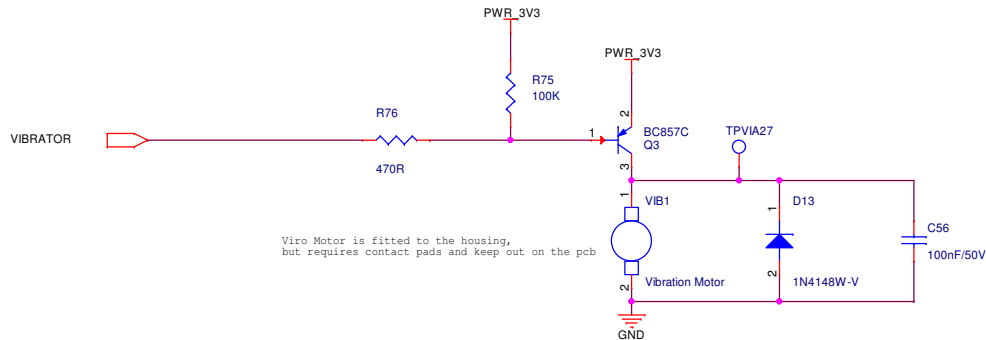
Camera Interface for future variants








Debounce performed by processor?



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