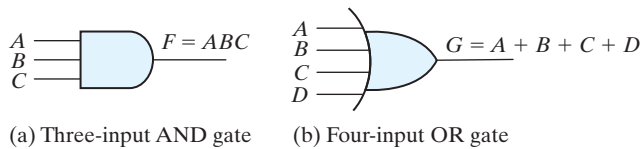


**FIGURE 1.5**  
Input–output signals for gates



**FIGURE 1.6**  
Gates with multiple inputs

AND and OR gates may have more than two inputs. An AND gate with three inputs and an OR gate with four inputs are shown in Fig. 1.6. The three-input AND gate responds with logic 1 output if all three inputs are logic 1. The output produces logic 0 if any input is logic 0. The four-input OR gate responds with logic 1 if any input is logic 1; its output becomes logic 0 only when all inputs are logic 0.

## PROBLEMS

(Answers to problems marked with \* appear at the end of the text.)

- 1.1** List the octal and hexadecimal numbers from 16 to 32. Using A and B for the last two digits, list the numbers from 8 to 28 in base 12.
- 1.2\*** What is the exact number of bytes in a system that contains (a) 32K bytes, (b) 64M bytes, and (c) 6.4G bytes?
- 1.3** Convert the following numbers with the indicated bases to decimal:
 

(a)\*  $(4310)_5$   
 (c)  $(435)_8$

(b)\*  $(198)_{12}$   
 (d)  $(345)_6$
- 1.4** What is the largest binary number that can be expressed with 16 bits? What are the equivalent decimal and hexadecimal numbers?
- 1.5\*** Determine the base of the numbers in each case for the following operations to be correct:
 

(a)  $14/2 = 5$

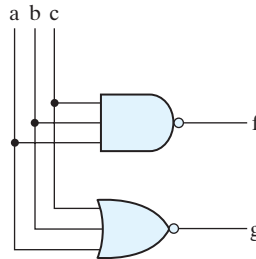
(b)  $54/4 = 13$

(c)  $24 + 17 = 40$
- 1.6\*** The solutions to the quadratic equation  $x^2 - 11x + 22 = 0$  are  $x = 3$  and  $x = 6$ . What is the base of the numbers?

- 1.7\*** Convert the hexadecimal number 64CD to binary, and then convert it from binary to octal.
- 1.8** Convert the decimal number 431 to binary in two ways: (a) convert directly to binary; (b) convert first to hexadecimal and then from hexadecimal to binary. Which method is faster?
- 1.9** Express the following numbers in decimal:
- (a)\*  $(10110.0101)_2$  (b)\*  $(16.5)_{16}$   
 (c)\*  $(26.24)_8$  (d)  $(DADA.B)_{16}$   
 (e)  $(1010.1101)_2$
- 1.10** Convert the following binary numbers to hexadecimal and to decimal: (a) 1.10010, (b) 110.010. Explain why the decimal answer in (b) is 4 times that in (a).
- 1.11** Perform the following division in binary:  $111011 \div 101$ .
- 1.12\*** Add and multiply the following numbers without converting them to decimal.
- (a) Binary numbers 1011 and 101.  
 (b) Hexadecimal numbers 2E and 34.
- 1.13** Do the following conversion problems:
- (a) Convert decimal 27.315 to binary.  
 (b) Calculate the binary equivalent of  $2/3$  out to eight places. Then convert from binary to decimal. How close is the result to  $2/3$ ?  
 (c) Convert the binary result in (b) into hexadecimal. Then convert the result to decimal. Is the answer the same?
- 1.14** Obtain the 1's and 2's complements of the following binary numbers:
- (a) 00010000 (b) 00000000  
 (c) 11011010 (d) 10101010  
 (e) 10000101 (f) 11111111.
- 1.15** Find the 9's and the 10's complement of the following decimal numbers:
- (a) 25,478,036 (b) 63,325,600  
 (c) 25,000,000 (d) 00,000,000.
- 1.16** (a) Find the 16's complement of C3DF.  
 (b) Convert C3DF to binary.  
 (c) Find the 2's complement of the result in (b).  
 (d) Convert the answer in (c) to hexadecimal and compare with the answer in (a).
- 1.17** Perform subtraction on the given unsigned numbers using the 10's complement of the subtrahend. Where the result should be negative, find its 10's complement and affix a minus sign. Verify your answers.
- (a)  $4,637 - 2,579$  (b)  $125 - 1,800$   
 (c)  $2,043 - 4,361$  (d)  $1,631 - 745$
- 1.18** Perform subtraction on the given unsigned binary numbers using the 2's complement of the subtrahend. Where the result should be negative, find its 2's complement and affix a minus sign.
- (a)  $10011 - 10010$  (b)  $100010 - 100110$   
 (c)  $1001 - 110101$  (d)  $101000 - 10101$
- 1.19\*** The following decimal numbers are shown in sign-magnitude form: +9,286 and +801. Convert them to signed-10's-complement form and perform the following operations (note that the sum is +10,627 and requires five digits and a sign).
- (a)  $(+9,286) + (+801)$  (b)  $(+9,286) + (-801)$   
 (c)  $(-9,286) + (+801)$  (d)  $(-9,286) + (-801)$

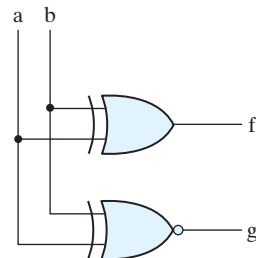
- 1.20** Convert decimal +49 and +29 to binary, using the signed-2's-complement representation and enough digits to accommodate the numbers. Then perform the binary equivalent of  $(+29) + (-49)$ ,  $(-29) + (+49)$ , and  $(-29) + (-49)$ . Convert the answers back to decimal and verify that they are correct.
- 1.21** If the numbers  $(+9,742)_{10}$  and  $(+641)_{10}$  are in signed magnitude format, their sum is  $(+10,383)_{10}$  and requires five digits and a sign. Convert the numbers to signed-10's-complement form and find the following sums:
- |                         |                         |
|-------------------------|-------------------------|
| (a) $(+9,742) + (+641)$ | (b) $(+9,742) + (-641)$ |
| (c) $(-9,742) + (+641)$ | (d) $(-9,742) + (-641)$ |
- 1.22** Convert decimal 6,514 to both BCD and ASCII codes. For ASCII, an even parity bit is to be appended at the left.
- 1.23** Represent the unsigned decimal numbers 791 and 658 in BCD, and then show the steps necessary to form their sum.
- 1.24** Formulate a weighted binary code for the decimal digits, using the following weights:
- |                 |                |
|-----------------|----------------|
| (a)* 6, 3, 1, 1 | (b) 6, 4, 2, 1 |
|-----------------|----------------|
- 1.25** Represent the decimal number 6,248 in (a) BCD, (b) excess-3 code, (c) 2421 code, and (d) a 6311 code.
- 1.26** Find the 9's complement of decimal 6,248 and express it in 2421 code. Show that the result is the 1's complement of the answer to (c) in CR\_PROBLEM 1.25. This demonstrates that the 2421 code is self-complementing.
- 1.27** Assign a binary code in some orderly manner to the 52 playing cards. Use the minimum number of bits.
- 1.28** Write the expression "G. Boole" in ASCII, using an eight-bit code. Include the period and the space. Treat the leftmost bit of each character as a parity bit. Each eight-bit code should have odd parity. (George Boole was a 19th-century mathematician. Boolean algebra, introduced in the next chapter, bears his name.)
- 1.29\*** Decode the following ASCII code:  
1010011 1110100 1100101 1110110 1100101 0100000 1001010 1101111 1100010 1110011.
- 1.30** The following is a string of ASCII characters whose bit patterns have been converted into hexadecimal for compactness: 73 F4 E5 76 E5 4A EF 62 73. Of the eight bits in each pair of digits, the leftmost is a parity bit. The remaining bits are the ASCII code.
- |  |   |
|--|---|
| (a) Convert the string to bit form and decode the ASCII. | (b) Determine the parity used: odd or even? |
|--|---|
- 1.31\*** How many printing characters are there in ASCII? How many of them are special characters (not letters or numerals)?
- 1.32\*** What bit must be complemented to change an ASCII letter from capital to lowercase and vice versa?
- 1.33\*** The state of a 12-bit register is 100010010111. What is its content if it represents
- |  |  |
|--|--|
| (a) Three decimal digits in BCD?             | (b) Three decimal digits in the excess-3 code? |
| (c) Three decimal digits in the 84-2-1 code? | (d) A binary number?                           |

- 1.34** List the ASCII code for the 10 decimal digits with an even parity bit in the leftmost position.
- 1.35** By means of a timing diagram similar to Fig. 1.5, show the signals of the outputs f and g in Fig. P1.35 as functions of the three inputs a, b, and c. Use all eight possible combinations of a, b, and c.



**FIGURE P1.35**

- 1.36** By means of a timing diagram similar to Fig. 1.5, show the signals of the outputs f and g in Fig. P1.36 as functions of the two inputs a and b. Use all four possible combinations of a and b.



**FIGURE P1.36**

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