

Ahmedabad
University

Embedded Systems and Design

Lab 2 - Report

Section 1

Submitted to

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Date of Submission: 27th October 2021

Student Details

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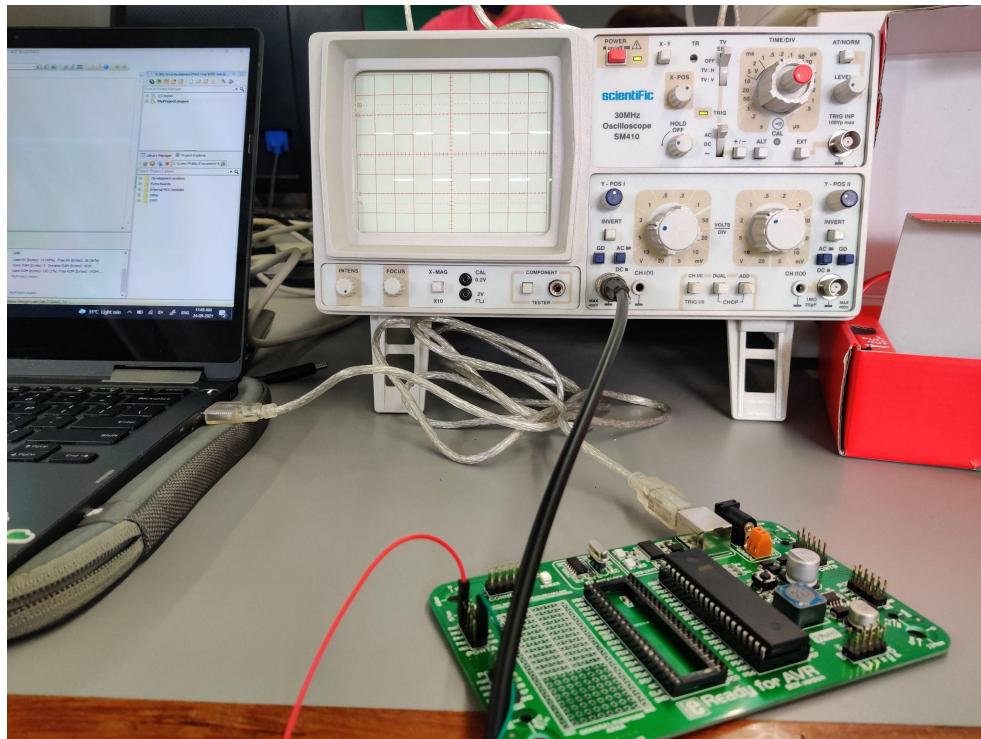
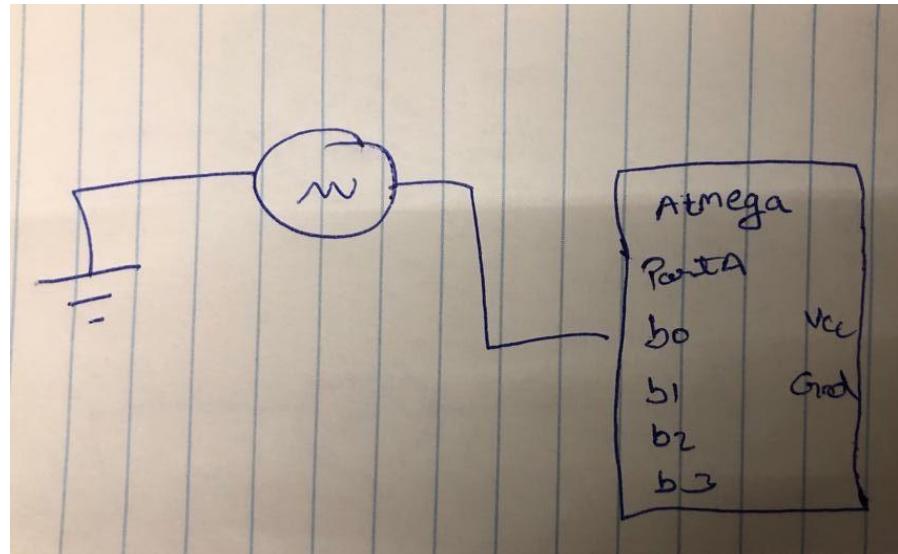
2021-2022 (Monsoon Semester)

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Question 1: Generate a square wave of 500 Hz (50% duty Cycle) by toggling the 0th pin of Port A. Observe this wave on CRO. (Hint: Use `delay_ms` in-build function to create appropriate delay, toggle the port at 1ms (frequency = 500 Hz. So, the time delay for the whole wave is 2 ms).)

[Video](#) | [Code](#)





```
1 void main() {
2     DDRA=0x01;
3     while(1){
4         PORTA=0x01;
5         Delay_ms(1);
6         PORTA=0x00;
7         Delay_ms(1);
8     }
9 }
```

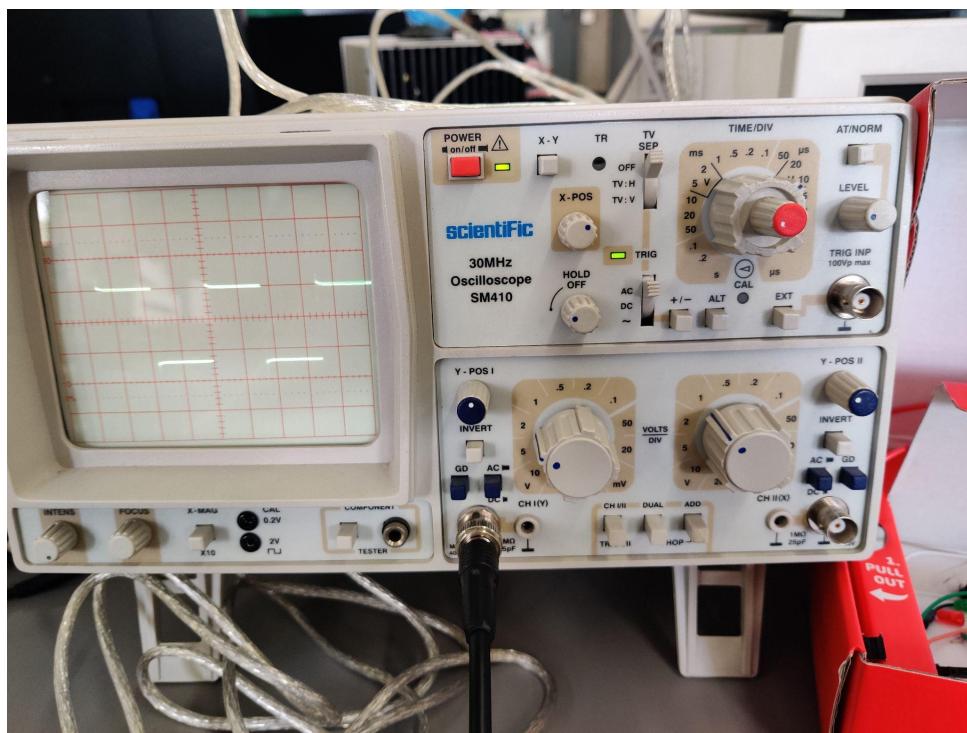
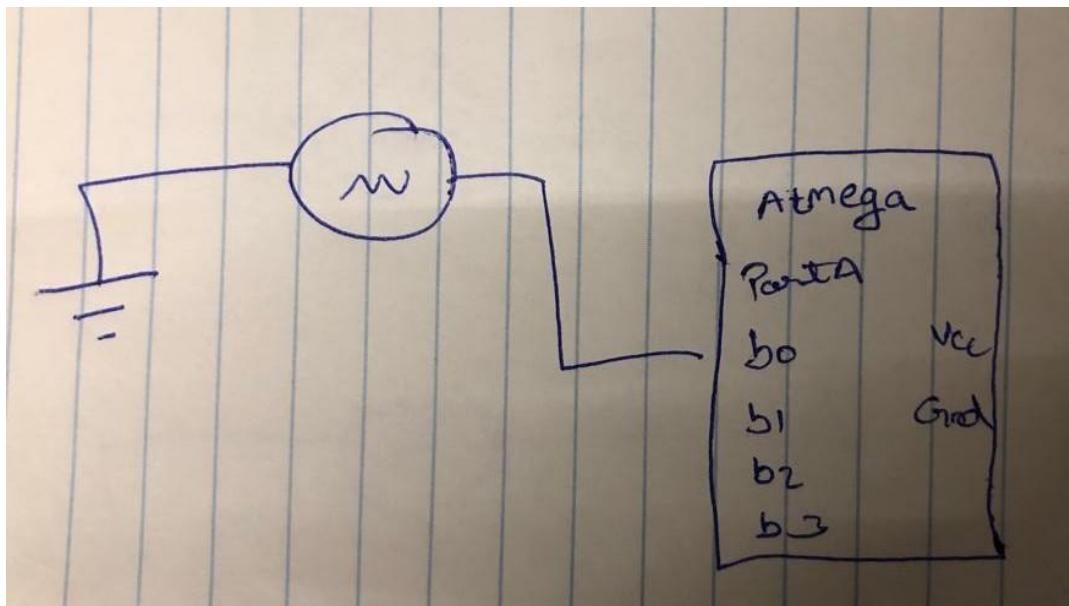
In the first question, we Generate a square wave of 500 Hz (50% duty Cycle) by toggling the 0th pin of Port A and then observe this wave in CRO. We use the delay_ms in-build function to create appropriate delay, which is of 1 ms

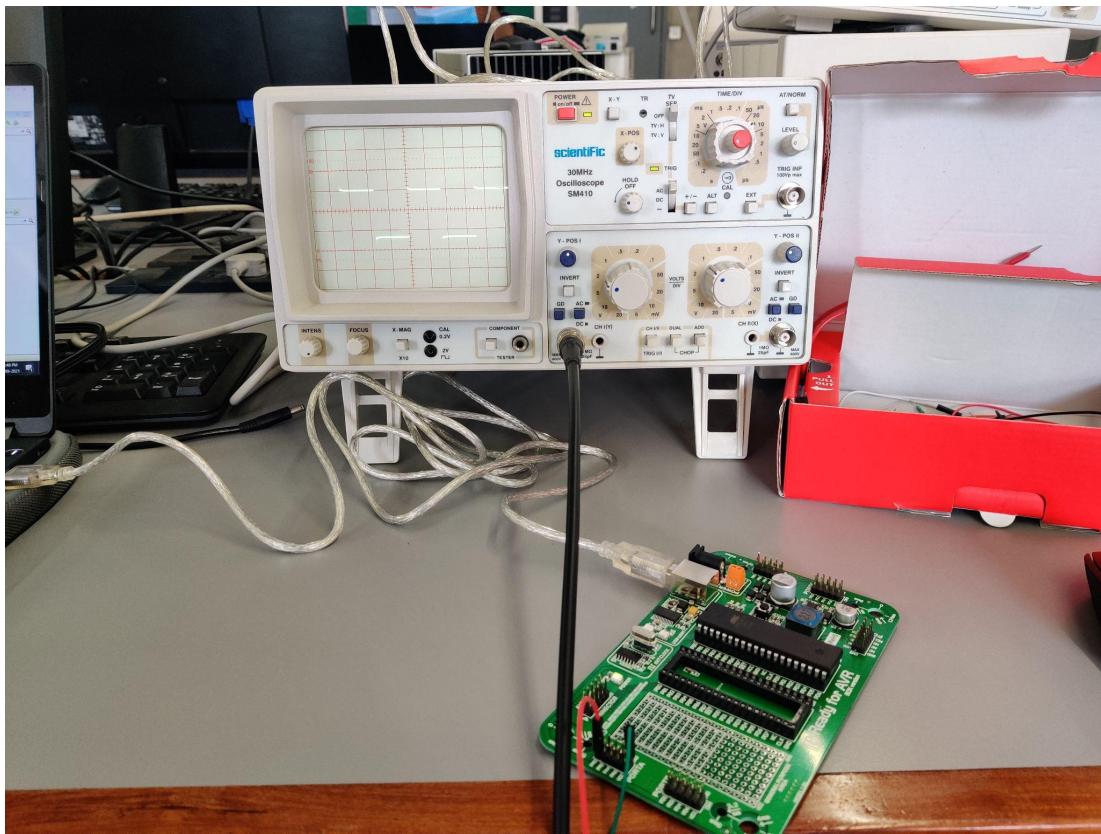
Question 2: Toggle a port pin upon stated conditions:

Calculate and verify the frequency for each case. You may write the values of these frequencies at the start of your program (in comment) for submission.

(A). Timer 0 Normal mode

[Video](#) | [Code](#)





D for AVR v.7.0.1-G:\My Drive\Academics\Third Year\Fifth Sem\ECE302 - Embedded Systems Design\Labs\Set 2\Question 2_2\MyProject.mcav - NOT REGISTERED

File Project Build Run Tools Help

Start Page NormalModeNoPrescaler.c

```

void T0Delay();
void main() {
    DDRA=0x01;
    while(1) {
        PORTA=0x01;
        T0Delay();
        PORTA=0x00;
        T0Delay();
    }
}
void T0Delay(){
    TCNT0=0x00; // No initial delay is provided
    TCCR0=0x01; // Normal mode, no prescaler
    while((TIFR&0x01)==0); // Check until TIFR overflows
    TIFR=0x01; // Reset the TIFR
}

// As it is 3.2 units at 20 microsecs, we get 20*3.2 or 64 microsecs
// This is correct as our input clk is 8Mhz with 0 delay and called twice
// Thus, 2*256*(1/8) is 64 microsecs

```

Quick Converter

Warnings Hints

Message No.	Message Text	Unit
1144	Used RX (bytes): 11 (34%) Free RX (bytes): 21 (66%)	Used RX (bytes): 11 (34%) Free RX (bytes): 21 (66%)
1144	Static RAM (bytes): 0 Dynamic RAM (bytes): 1024	Static RAM (bytes): 0 Dynamic RAM (bytes): 1024
1144	Used ROM (bytes): 146 (1%) Free ROM (bytes): 14190 (99%)	Used ROM (bytes): 146 (1%) Free ROM (bytes): 14190 ...
125	Project Linked Successfully	MyProject.mcav

```

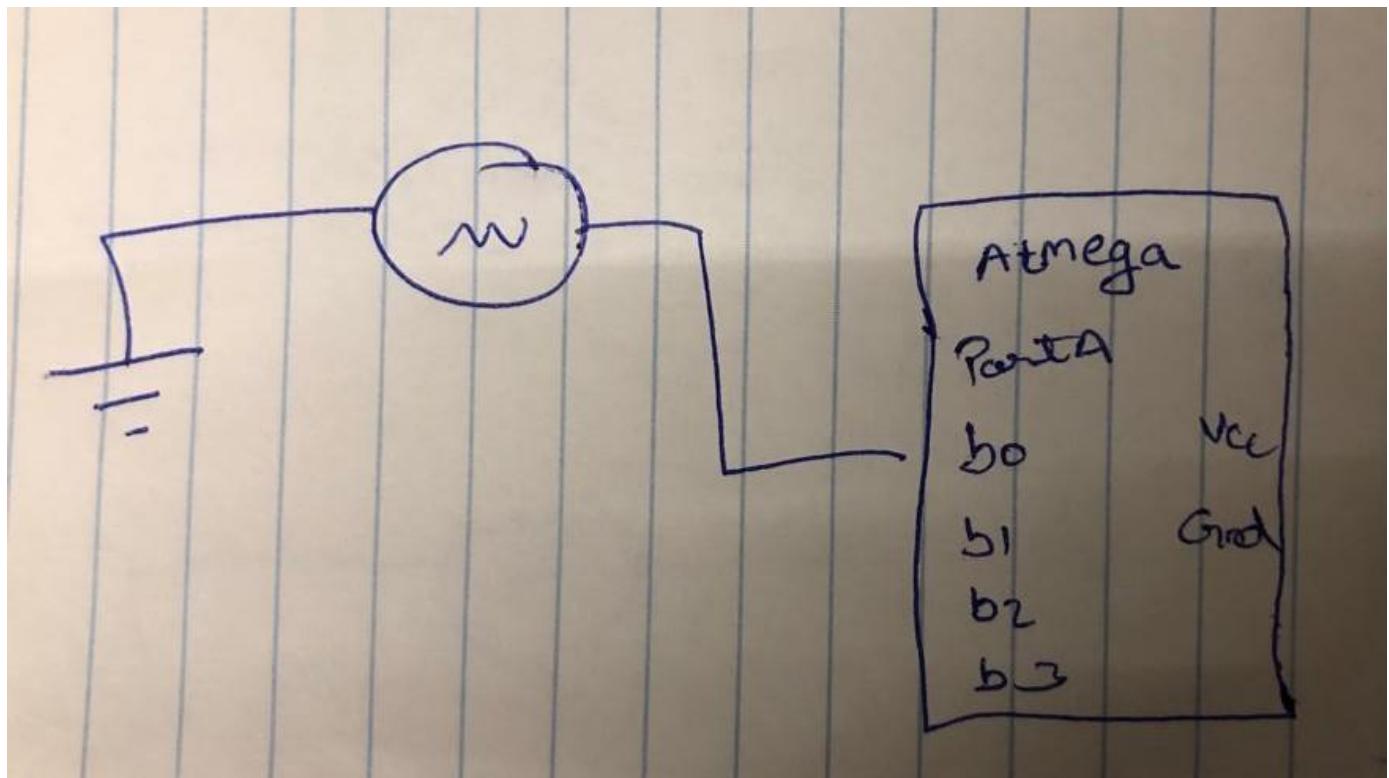
1 void T0Delay();
2 void main() {
3     DDRA=0x01;
4     while(1){
5         PORTA=0x01;
6         T0Delay();
7         PORTA=0x00;
8         T0Delay();
9     }
10 }
11 void T0Delay(){
12     TCNT0=0x00; // No initial delay is provided
13     TCCR0=0x01; // Normal mode, no prescaler
14     while((TIFR&0x01)==0); // Check until TIFR overflows
15     TIFR=0x01; // Reset the TIFR
16 }
17
18 /*
19 As it is 3.2 units at 20 microsecs, we get 20*3.2 or 64 microsecs.
20 This is correct as our input clk is 8Mh with 0 delay and called twice
21 Thus, 2*256*(1/8) is 64 microsecs
22 */

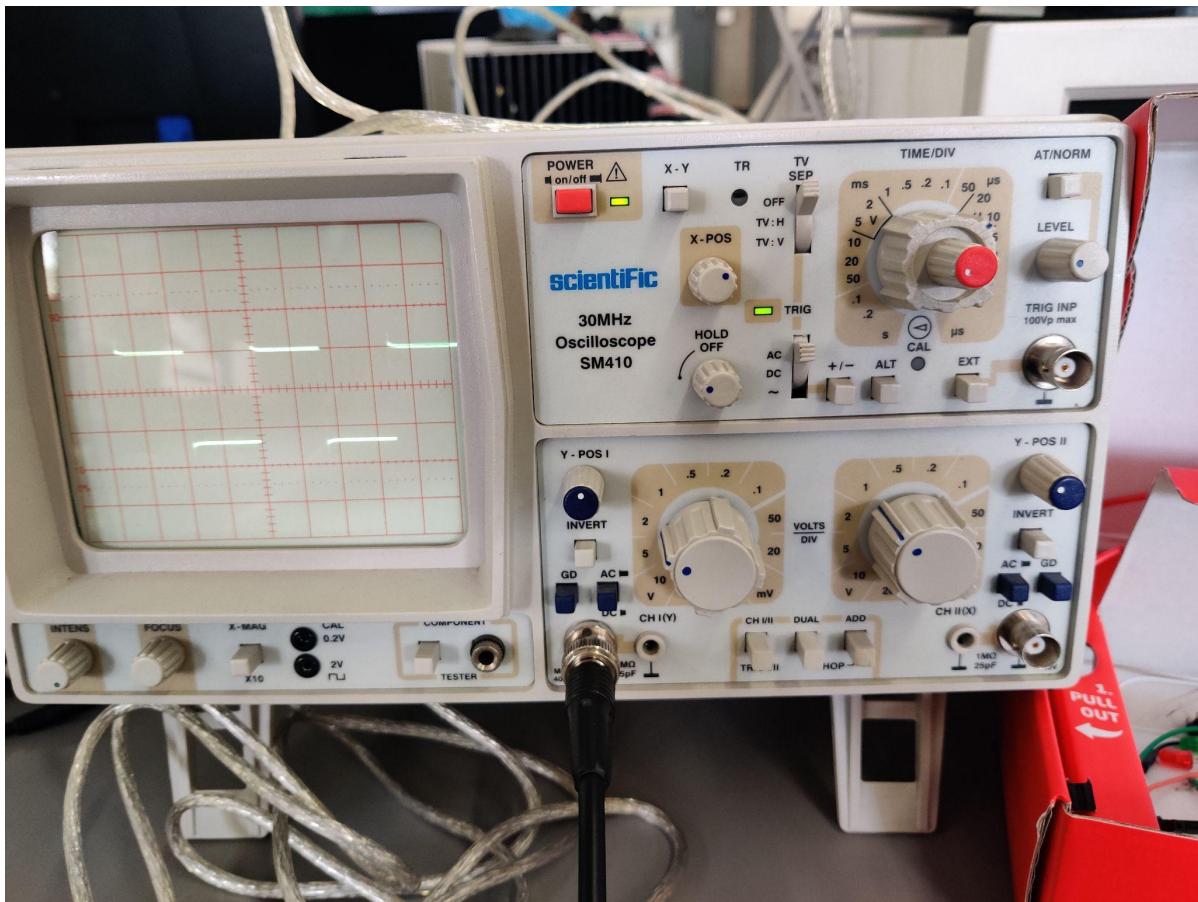
```

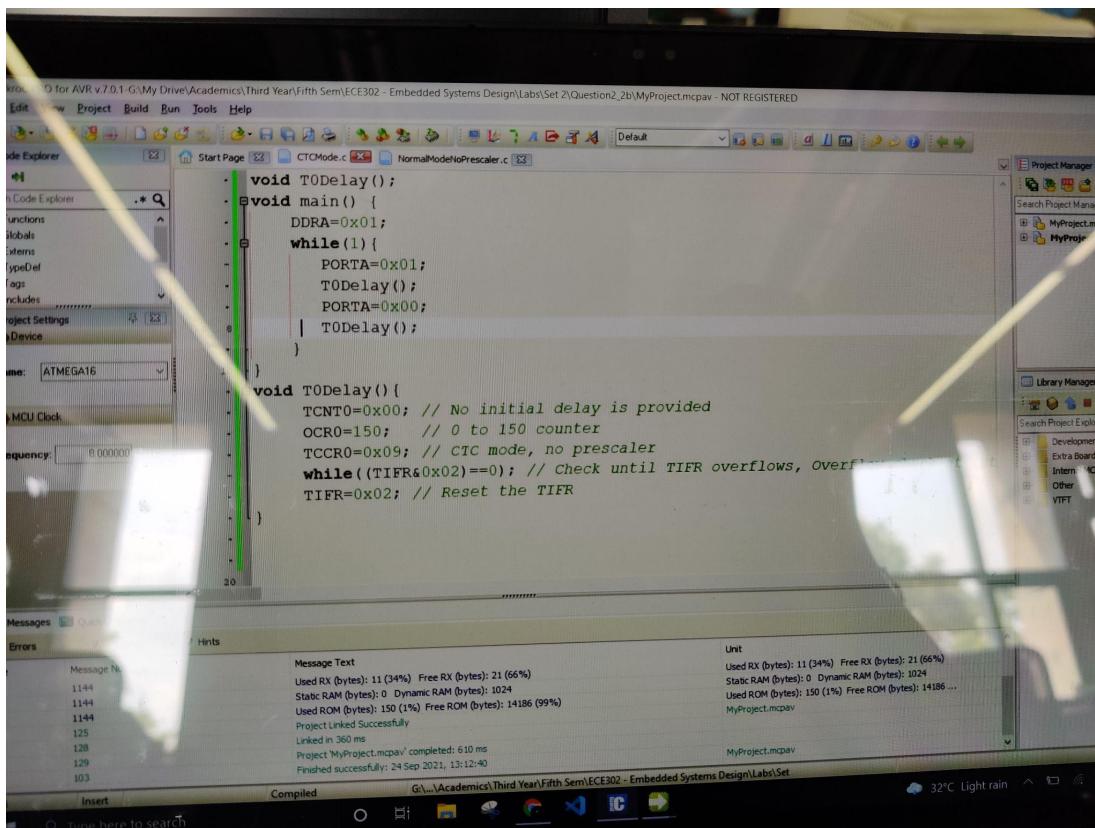
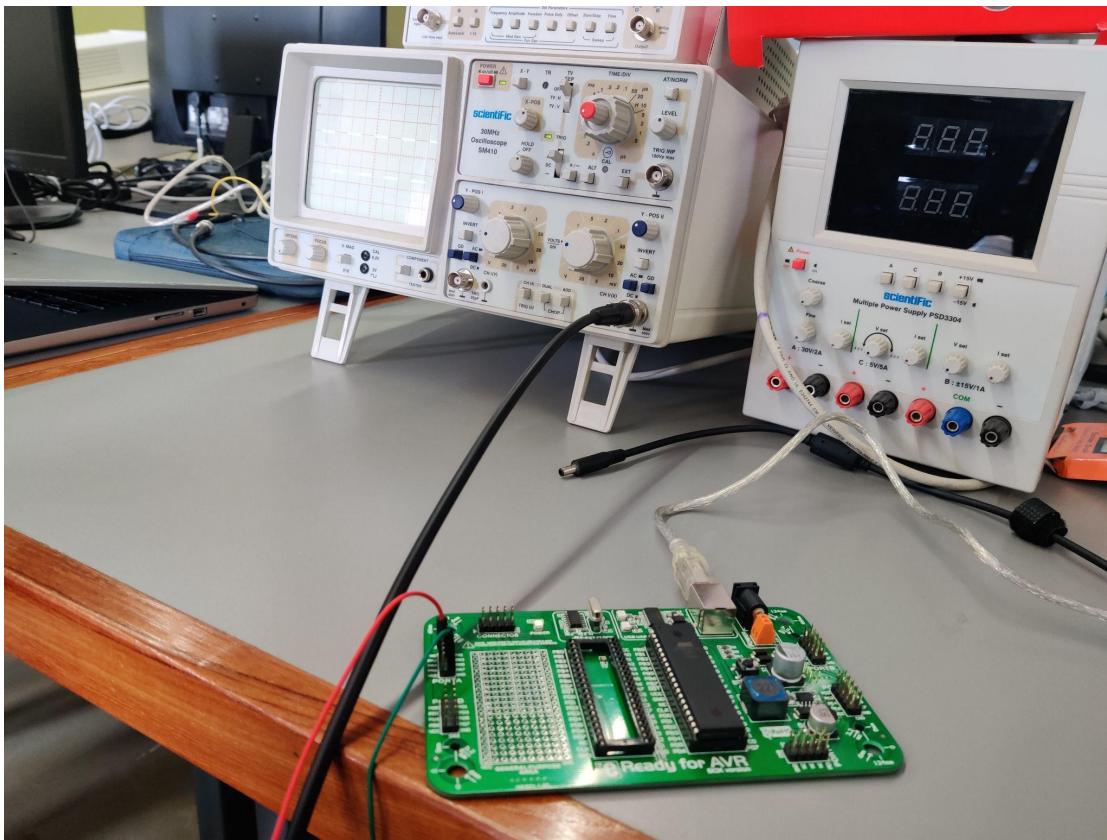
Here, we needed to generate a square wave by toggling PORTA.b0 using the timer0 circuit in normal mode. The connections are similar to the previous question. Furthermore, as we can see in the code below, it is evident that the timer counter register runs for a full cycle i.e from 0 to 255, and rolls over at 256. This means that the delay is 64 microseconds. This means that the theoretical frequency is 15.625 kHz. Now as we can see in the image above, the time delay for 2 blocks in CRO is approximately 20 microseconds each which means 40 microseconds. This means the time delay for the whole square wave is 80 microseconds. This means that the calculated frequency is 12.5 kHz.

(B). Timer 0 CTC mode

[Video](#) | [Code](#)







```

● ● ●

1 void T0Delay();
2 void main() {
3     DDRA=0x01;
4     while(1){
5         PORTA=0x01;
6         T0Delay();
7         PORTA=0x00;
8         T0Delay();
9     }
10 }
11 void T0Delay(){
12     TCNT0=0x00; // No initial delay is provided
13     OCR0=150; // 0 to 150 counter
14     TCCR0=0x09; // CTC mode, no prescaler
15     while((TIFR&0x02)==0); // Check until TIFR overflows
16                         //Overflow check at 1st bit and not 0th
17     TIFR=0x02; // Reset the TIFR
18 }
19 /*
20 * 2*150*(1/8) is theoretical = 37.5 microsecs
21 Real is 2.1*20 microsecs or 42 microsecs
22 Real freq is 1/42 or 0.0238 Mhz or 23.8 KHz and theoretical is 0.0266 Mhz or 26.67 KHz
23 */

```

Here the timer0 counter runs from 0 to 150 and rolls over. For this, the theoretical time delay is 37.5 microseconds and the theoretical frequency is 26.67 kHz.

As visible in the photo, 1 block of square wave has a 20 seconds delay and thus the whole wave has a 40 seconds delay. Thus, the calculated frequency becomes 25 kHz.

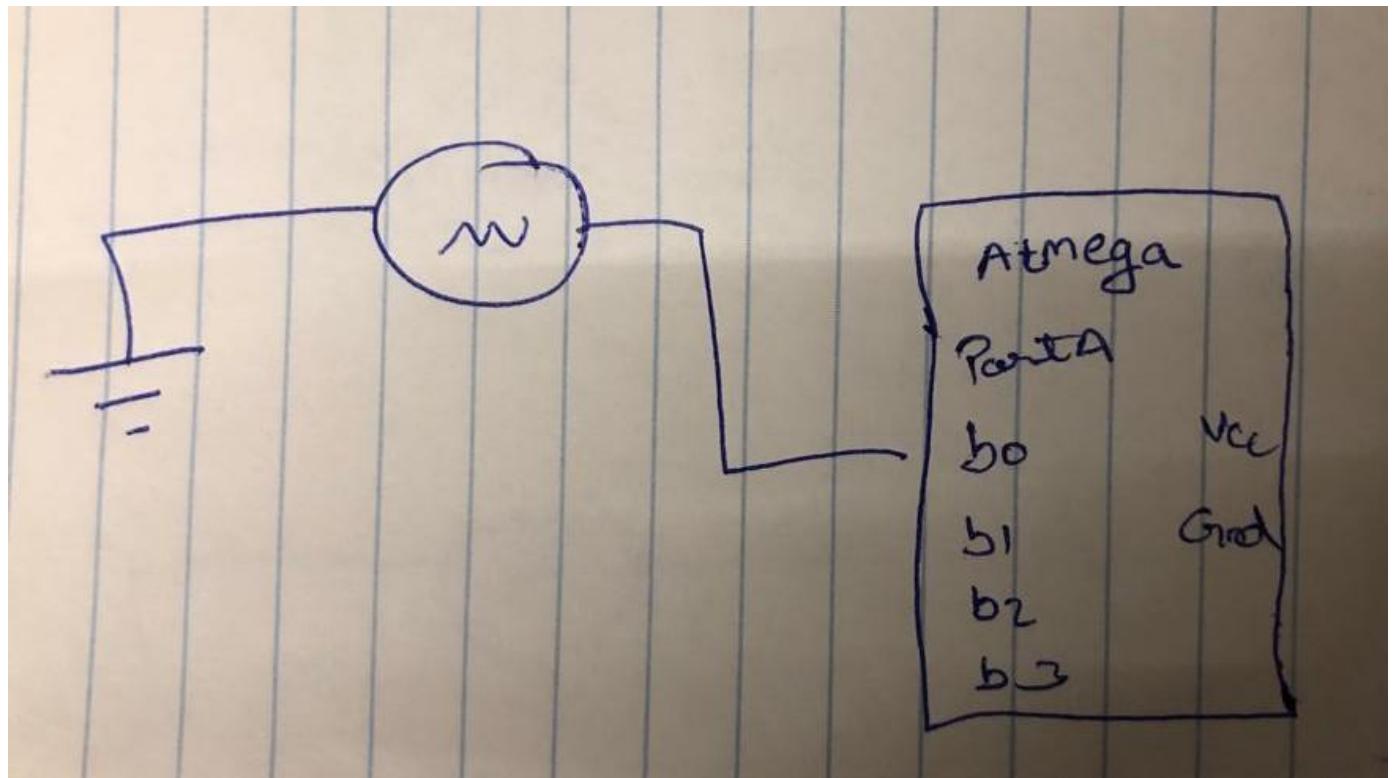
Question 3: Generate and verify square wave of the following frequency at pin

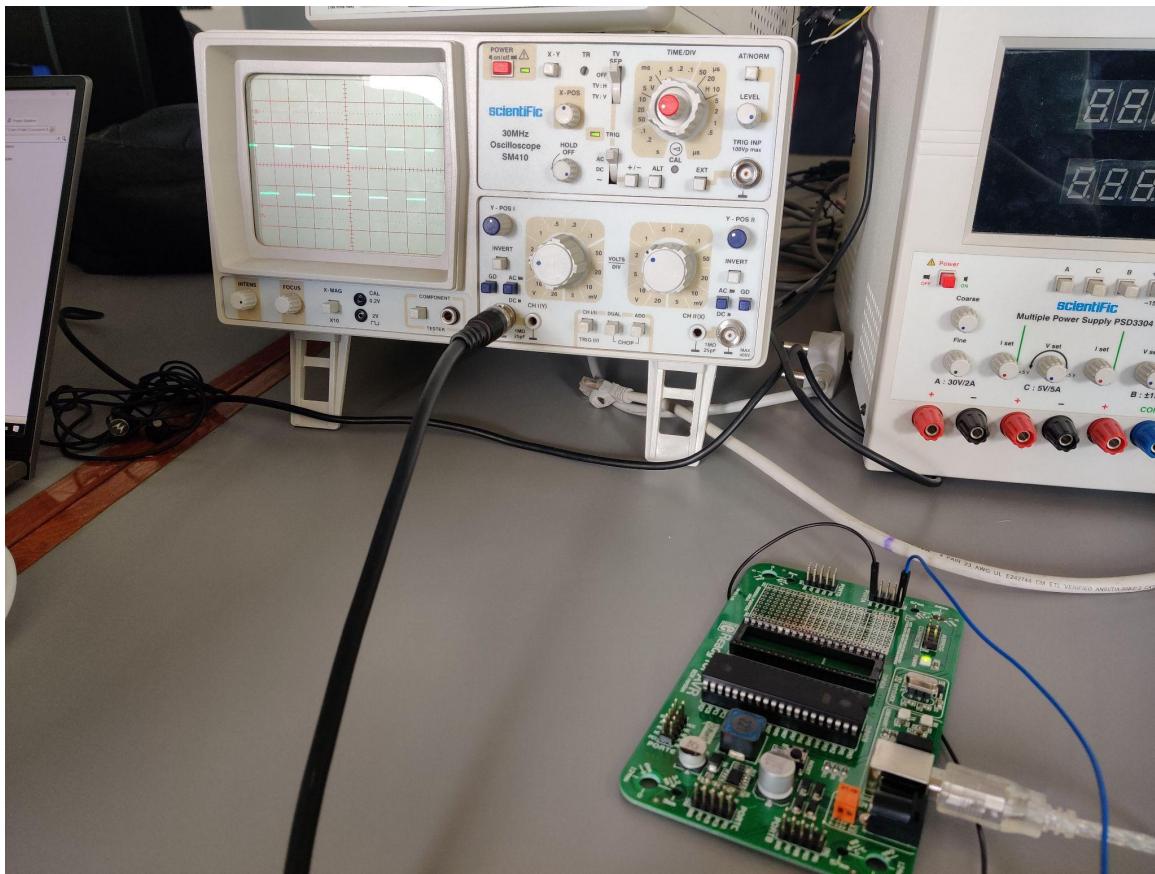
PORTA.B0:

(A) 1 kHz

(Hint: Setup Pre-scaler)

[Video](#) | [Code](#)





milkoC PRO for AVR v.7.0.1-G:\My Drive\Academics\Third Year\Fifth Sem\ECE302 - Embedded Systems Design\Labs\Set 2\Question 2_3a\Question2_3a.mcav - NOT REGISTERED

```

File Edit View Project Build Run Tools Help
Code Explorer Start Page Question2_3a.c Default
Project Settings Device
Name: ATMEGA16
MCU Clock
Frequency: 8.000000 MHz
void T0Delay();
void main() {
    DDRA=0x01;
    while(1){
        PORTA=0x01;
        T0Delay();
        PORTA=0x00;
        T0Delay();
    }
}
void T0Delay(){
    TCCR0=0x03; //64 prescaler is calculated
    TCNT0=194;
    while((TIFR & 0x01)==0); //Until it overflows
    TIFR=0x01; // Resetting the TIFR Flag;
}

```

Messages Quick Converter

Errors Warnings Hints

Line	Message No.	Message Text	Unit
0	1144	Used RX (bytes): 11 (34%) Free RX (bytes): 21 (66%)	Used RX (bytes): 11 (34%) Free RX (bytes): 21 (66%)
0	1144	Static RAM (bytes): 0 Dynamic RAM (bytes): 1024	Static RAM (bytes): 0 Dynamic RAM (bytes): 1024
0	1144	Used ROM (bytes): 146 (1%) Free ROM (bytes): 14190 (99%)	Used ROM (bytes): 146 (1%) Free ROM (bytes): 14190 ...
0	125	Project Linked Successfully	Question2_3a.mcav
0	128	Linked in 1.72 ms	
0	129	Project 'Question2_3a.mcav' completed: 1563 ms	
0	103	Finished successfully: 01 Oct 2021, 12:15:12	Question2_3a.mcav

15:44 Insert Compiled G:\...\Third Year\Fifth Sem\ECE302 - Embedded Systems Design\Labs\Set 2\Question 2_3a\Question2_3a.c

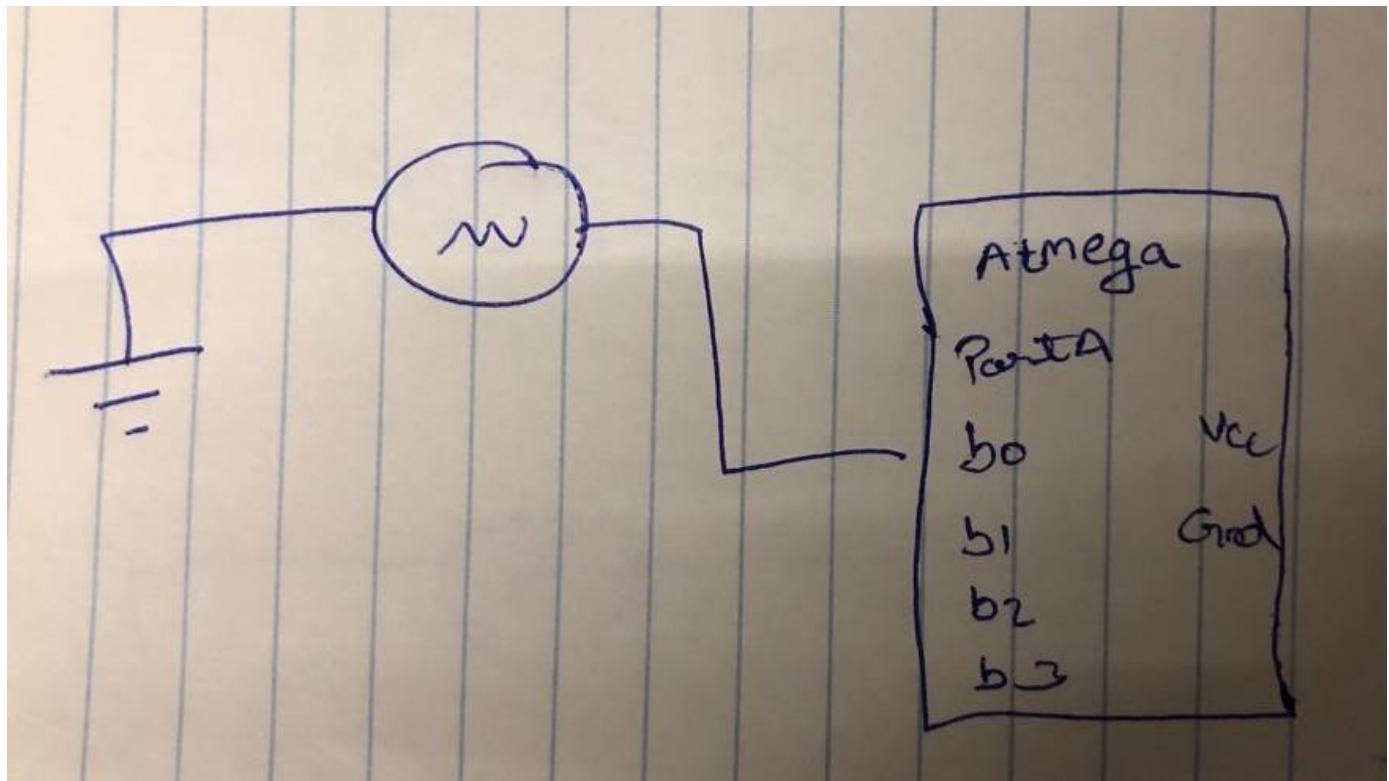
```
1 void T0Delay();
2 void main() {
3     DDRA=0x01;
4     while(1){
5         PORTA=0x01;
6         T0Delay();
7         PORTA=0x00;
8         T0Delay();
9     }
10 }
11 void T0Delay(){
12     TCCR0=0x03; //64 prescaler is calculated
13     TCNT0=194;
14     while((TIFR & 0x01)==0); //Until it overflows
15     TIFR=0x01; // Resetting the TIFR Flag
16 }
```

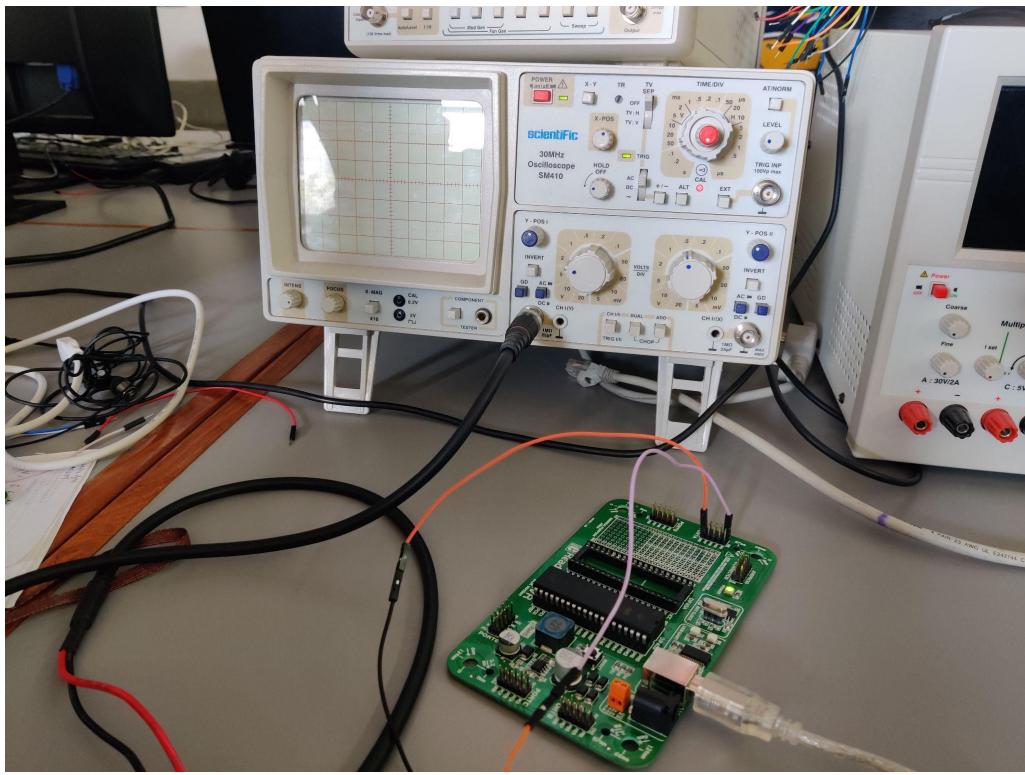
In this experiment, we had to use a timer0 circuit with an appropriate prescaler. Creating a square wave of frequency 1Khz lies beyond the range of no prescaler, thus we had to use CLK/64 prescaler. When the program was loaded into the microcontroller, it showed the result stated above. As seen in the image above, the delay for half part of the square wave is 0.5ms and thus the delay for the whole wave becomes 1ms which is the required delay.

(B)10 Hz

(Hint: If Pre-Scaler is not sufficient to generate the required delay, one could call the same delay multiple times)

[Video](#) | [Code](#)





mikroC PRO for AVR v.7.0.1-G\My Drive\Academics\Third Year\Fifth Sem\ECE302 - Embedded Systems Design\Labs\Set 2\Question 2_3b\Question2_3a.mcav - NOT REGISTERED

```

File Edit View Project Build Run Tools Help
Code Explorer Start Page Question2_3a.c Question2_3b.c
Project Settings Device
Name: ATMEGA16
MCU Clock 16 MHz
void T0Delay();
int i;
void main() {
    DDRA=0x01;
    while(1){
        PORTA^=0x01;
        T0Delay();
        T0Delay();
    }
}
void T0Delay(){
    TCCR0=0x05;
    TCNT0=61;
    while(TIFR & 0x01)==0; //Until it overflows
    TIFR=0x01; // Resetting the TIFR Flag
}

```

Message No. Message Text Unit

1144 Used RX (bytes): 11 (34%) Free RX (bytes): 21 (66%) Used RX (bytes): 11 (34%) Free RX (bytes): 21 (66%)
1144 Static RAM (bytes): 0 Dynamic RAM (bytes): 1024 Static RAM (bytes): 0 Dynamic RAM (bytes): 1024
1144 Used ROM (bytes): 146 (1%) Free ROM (bytes): 14190 (99%) Used ROM (bytes): 146 (1%) Free ROM (bytes): 14190 (99%)
0 Project Linked Successfully Project Linked Successfully
0 Linked in 36 ms Linked in 36 ms
0 Project 'Question2_3a.mcav' completed: 594 ms Project 'Question2_3a.mcav' completed: 594 ms
0 103 Finished successfully: 01 Oct 2021, 13:30:02 Finished successfully: 01 Oct 2021, 13:30:02

Question2_3a.mcav

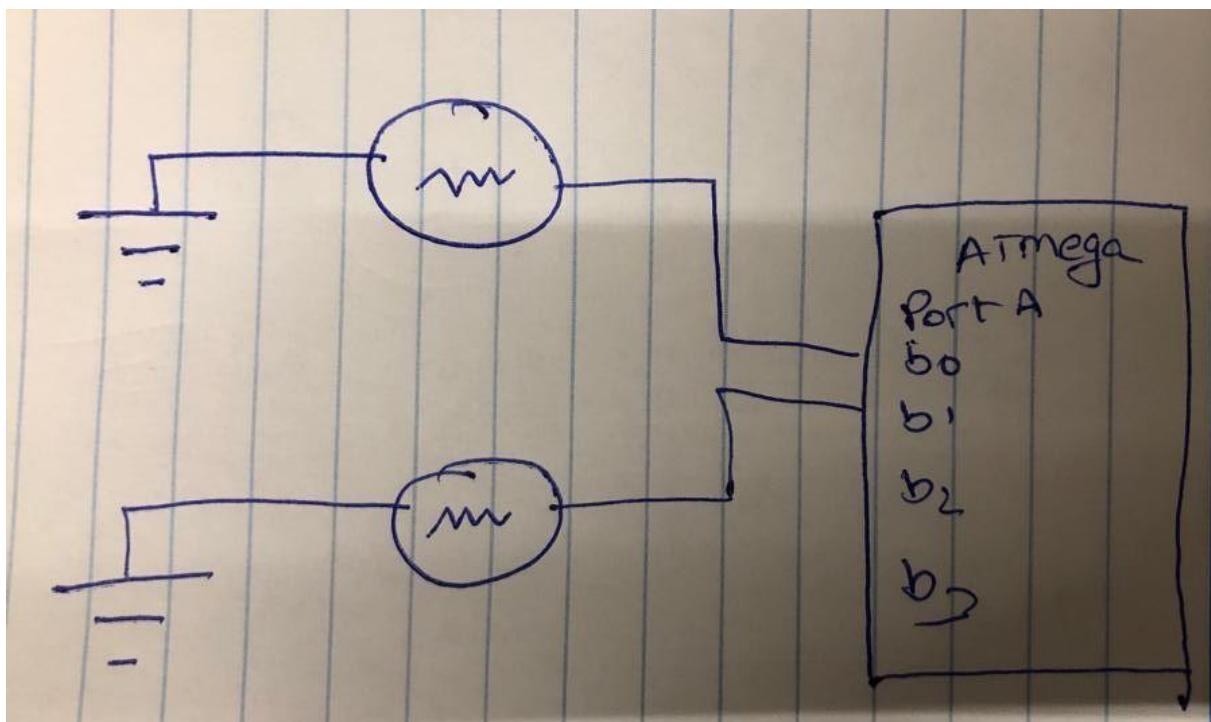
8:30 Insert Compiled G:\...\Third Year\Fifth Sem\ECE302 - Embedded Systems Design\Labs\Set 2\Question 2_3b\Question2_3b.c 32°C Light rain

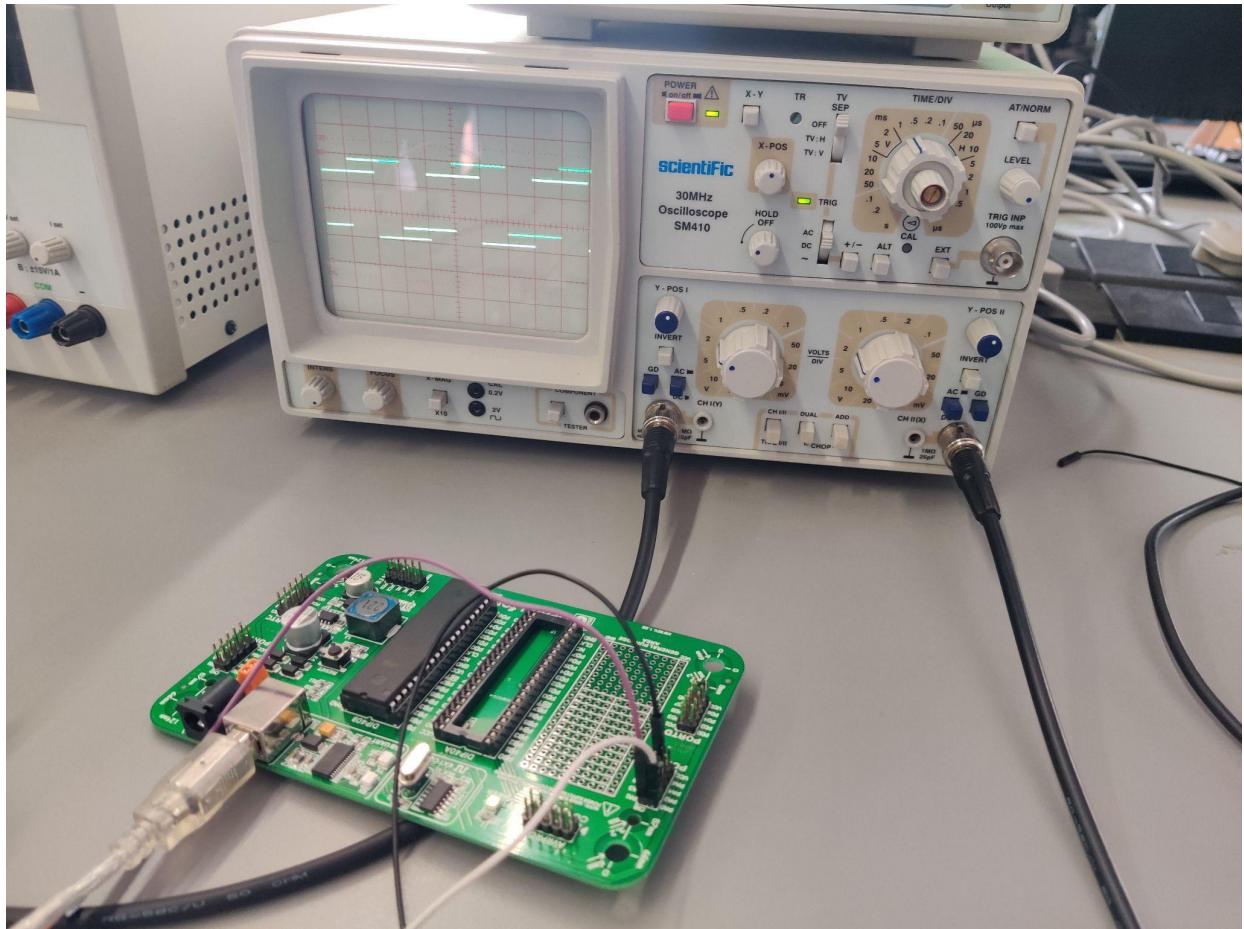
```
1 void T0Delay();
2 int i;
3 void main() {
4     DDRA=0x01;
5     while(1){
6         PORTA^=0x01;
7         T0Delay();
8         T0Delay();
9     }
10 }
11 void T0Delay(){
12     TCCR0=0x05;
13     TCNT0=61;
14     while((TIFR & 0x01)==0); //Until it overflows
15     TIFR=0x01; // Resetting the TIFR Flag
16 }
```

Here, we are asked to generate a square wave of 10Hz frequency which implies a delay of 0.1 seconds to the whole wave and 0.05 seconds to the half-wave. For this, even if we used the highest prescaler of CLK/1024, it was not possible to obtain the required time delay. Thus, we had to use multiple time delays. So we obtained the time delay of 0.025 seconds using CLK/1024 prescaler and applied it 2 times making the overall delay for half-wave to be 0.05 seconds. Even though the image is not clear we can see that the delay for half-wave is actually 0.5 seconds.

Question 4: Generate two square waves with a frequency of 1 kHz that are 90 phases shifted.

[Video](#) | [Code](#)





```

1 // Laboratory 1
2 // Question 4
3 // Parth Shah AU1940065
4
5 #include <avr/io.h>
6
7 // Declaring global variables
8 int i, j;
9 void main()
10 {
11     DDRA = 0xF0;
12     while (1)
13     {
14         // Checks if the b4 pin is pressed
15         if (PIN.A.b4 == 1)
16         {
17             // Loop from 0 to 15 Binary Display
18             for (i = 0; i < 16; i++)
19             {
20                 PORTA = i;
21                 Delay_ms(1111);
22                 if (PIN.A.b4 == 0)
23                 {
24                     // Resetting PORTA to 0
25                     PORTA = 0x00;
26                     break;
27                 }
28             }
29         }
30     }
31 }

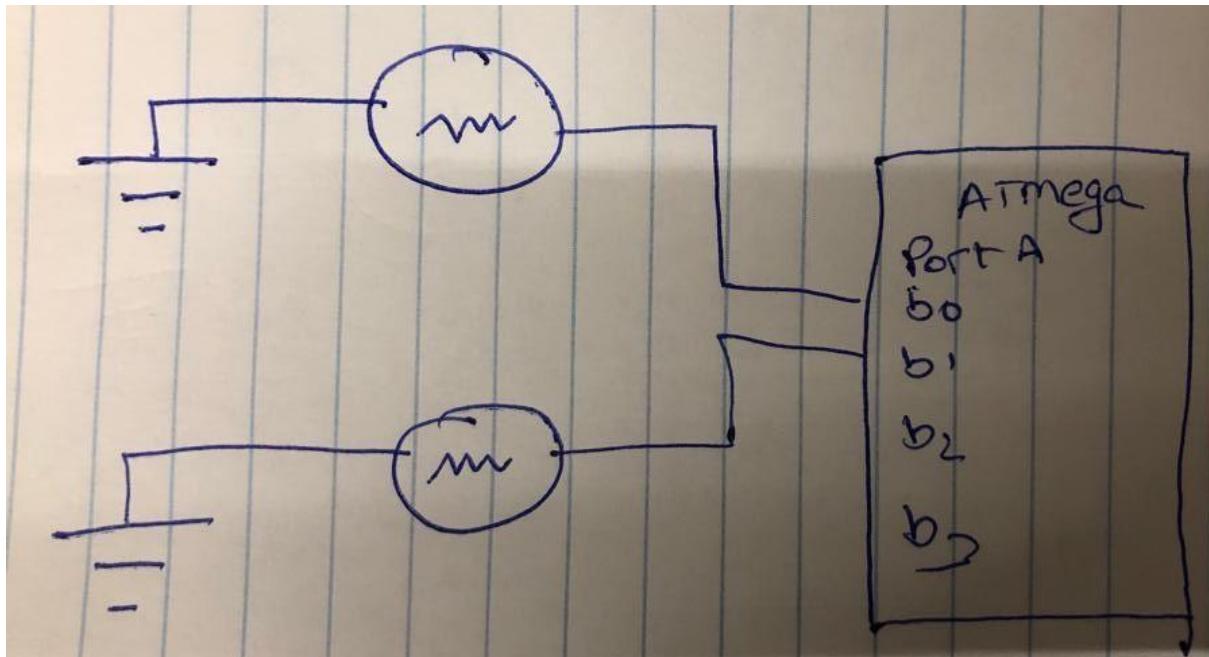
```

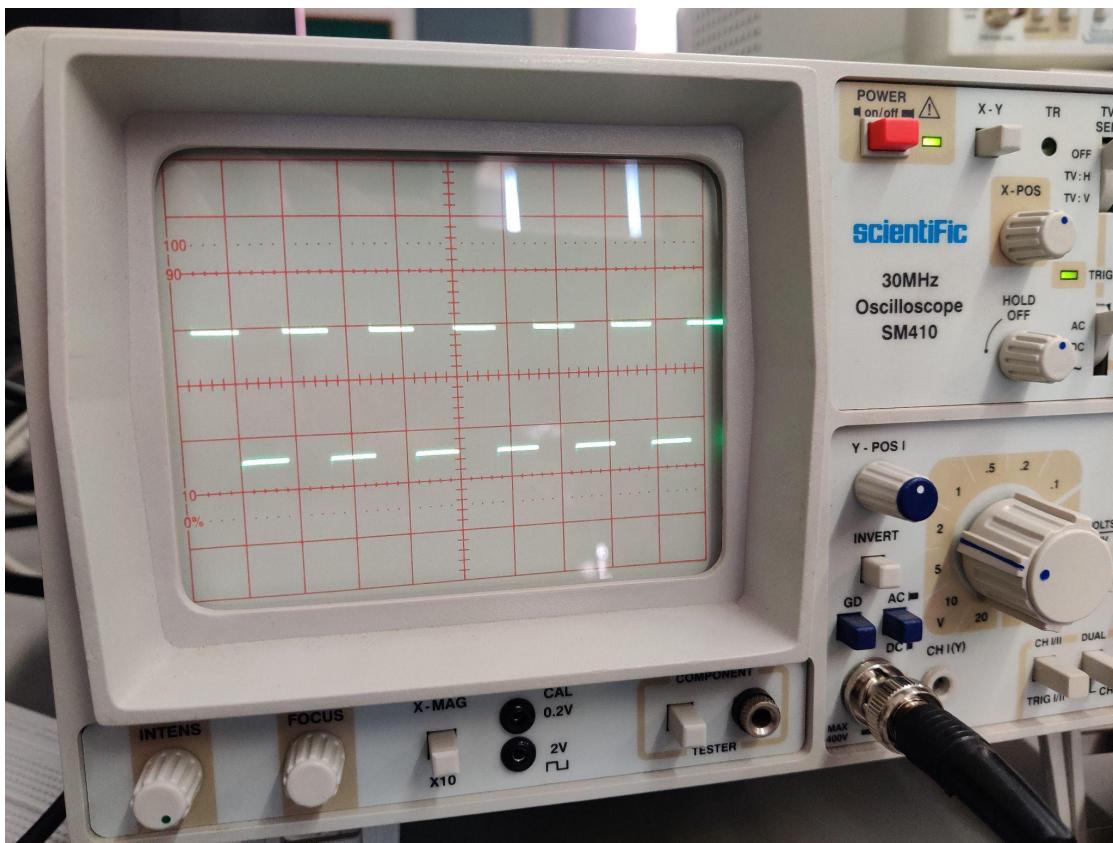
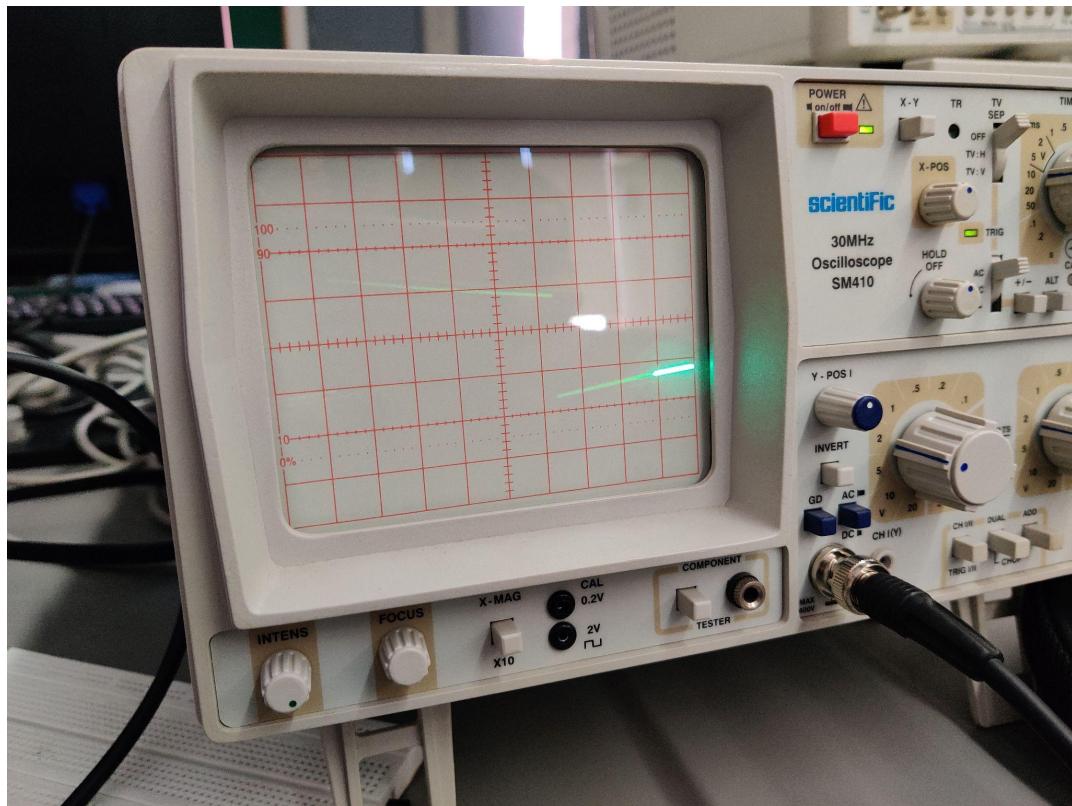
The question required to generate two square waves of frequency 1Khz that are shifted by 90 degrees. For this, we added a delay before toggling the PORTA.b0. Thus, it gave a proper 90 degrees shift. The time delay function remains the same as that in experiment 3a. The connections are quite simple with a modification that we need two CRO cables and operate the CRO in dual mode to get the desired result. As it can be seen in the image above, the 90 degrees phase shifting is clear.

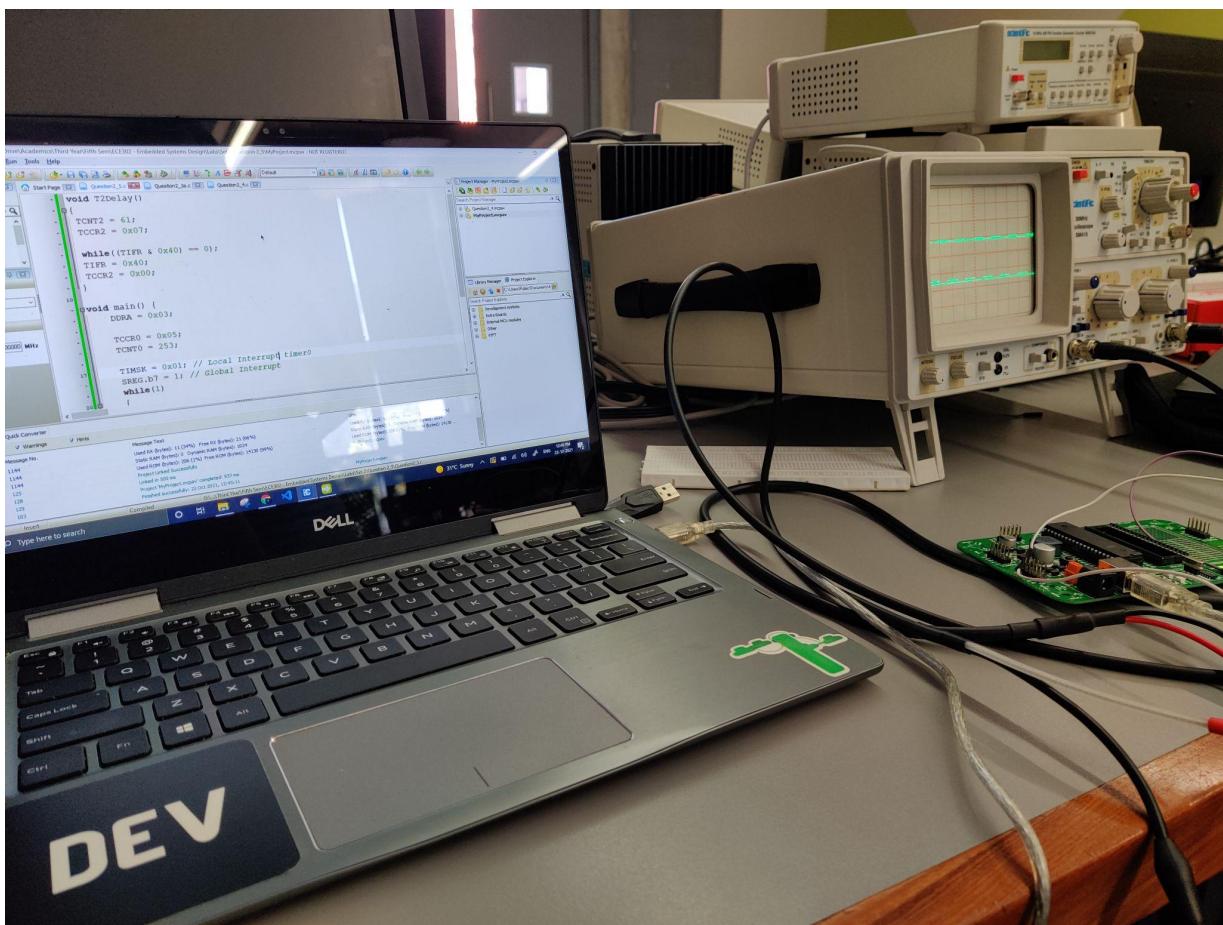
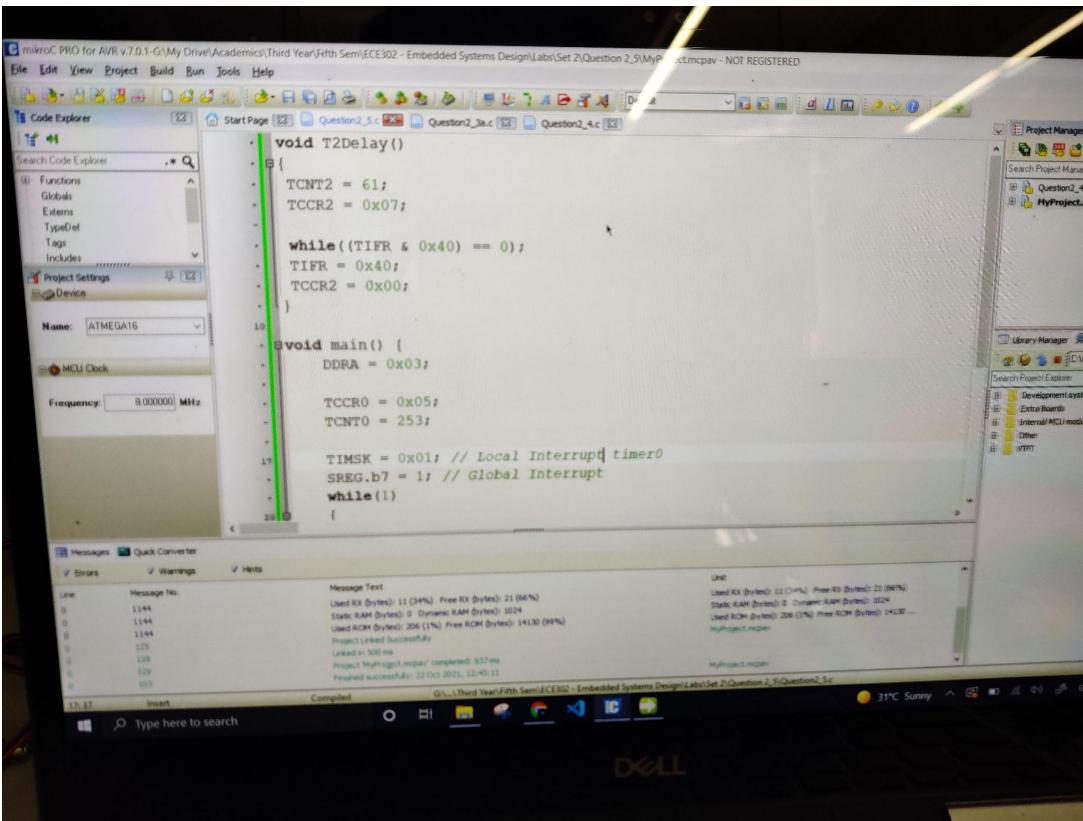
Question 5: (Extra Problem)

Generate both square waves of problem-3 simultaneously at two different port pins.
(Hint: You may use Timer 0 and Timer 2 together)

[Video](#) | [Code](#)







```

1 void T2Delay()
2 {
3     TCNT2 = 61;
4     TCCR2 = 0x07;
5
6     while((TIFR & 0x40) == 0);
7     TIFR = 0x40;
8     TCCR2 = 0x00;
9 }
10
11 void main() {
12     DDRA = 0x03;
13
14     TCCR0 = 0x05;
15     TCNT0 = 253;
16
17     TIMSK = 0x01; // Local Interrupt timer0
18     SREG.b7 = 1; // Global Interrupt
19     while(1)
20     {
21         PORTA ^= 0x01;
22         T2Delay();
23         T2Delay();
24     }
25 }
26
27 void timer0_ovf_isr(void) org 0x012
28 {
29     PORTA ^= 0x02;
30     TCNT0=253;
31 }

```

For the above experiment, we just had to replicate the results that we obtained in question 3 simultaneously at two different port pins. Thus we used PORTA.b0 and PORTA.b1. Furthermore, it was required to use an interrupt because two delays are to be applied using timer0 and timer2. Thus, timer0 is used in interrupt mode and timer2 is used in regular mode. Thus, we obtain the above result which is not clear because the time delay is very large.