GATE EE-2016-36

Sameer kendal

February 20, 2024

Question. The current state Q_A and Q_B of a two JK flip-flop is 00. Assume that the clock rise-time is much smaller than the delay of JK flip-flop. The next state of the system is   (GATE-EC2016,36)
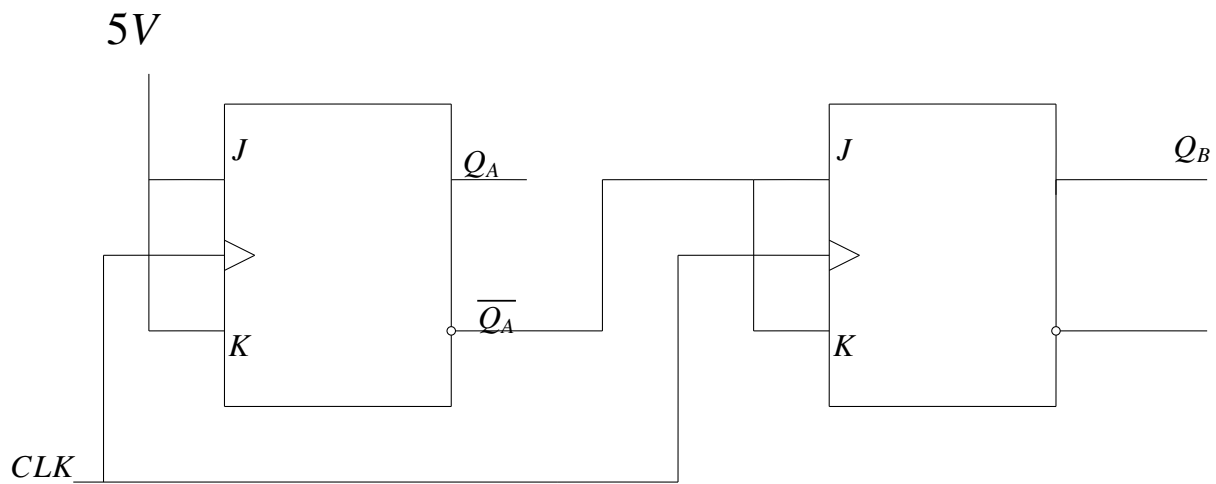

Figure 1: circuit diagram

(A) 00

(B) 01

(C) 11

(D) 10