

We started exercise 2 by using what we created in lab 4 and created 6 cases to create a second section. First, we had to find the address for 2^{17} which was 20000 in hexadecimal. The first write section (S_WRITE_CYCLE) starts from address 1FFFF (128k -1) and decreases to 0. Then (S_READ_CYCLE) reads everything and compares them to the expected values. Once it is done reading, the second section (S_WRITE_CYCLE2) starts from address 1FFFF to 3FFFF and then (S_READ_CYCLE2) is ran. We added delays in multiple stages to manually adjust the mismatch. This was the same method we used to adjust the mismatch in experiment 4. Lastly, we made sure to redirect the states at the bottom of each case. This was done to ensure that it followed the order we intended to