



East West University

Department of CSE

PROJECT REPORT

Course Code and Name: CSE325 (Operating System)

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Introduction:

WSClock Page Replacement Algorithm: An improved algorithm, which is based on the clock algorithm but also uses the working set information is called WSClock page replacement algorithm. In WSClock, when a page fault occurs, the algorithm not only checks the reference bit but also considers the time since the page was last referenced. If a page's reference bit is 1 and it falls within the working set time window, the page is considered recently used and is not a candidate for replacement. Otherwise, if the page is not in the working set, it may be a candidate for replacement.

Theoretical background:

Implementing the WSClock algorithm involves maintaining a circular list of page frames, tracking the reference bit for each page, and considering the temporal locality of page references through the working set information. Below is a theoretical step-by-step explanation of how we can implement the WSClock algorithm:

Data Structures:

Maintain a circular list of page frames to represent the available memory pages. Each page frame should have associated information:

- Reference bit: A flag indicating whether the page has been referenced recently (1 if referenced, 0 if not).
- Last referenced time: A timestamp indicating when the page was last referenced.
- Other necessary information, such as the page number.

Working Set Parameters:

- Working set time window (W): The time interval within which a page is considered part of the working set.
- Clock hand: A pointer to the current position in the circular list.

Page Fault Handling:

When a page fault occurs follow these steps:

- i. Start at the page pointed to by the clock hand in the circular list.
- ii. Check the reference bit of the page: If the reference bit is 0, indicating the page hasn't been referenced recently:
 - Check if the last referenced time is within the working set time window (W).
 - If within the working set, update the reference bit and last referenced time, and move the clock hand to the next page.
 - If not within the working set, replace the page with the new page, updating the necessary information.

If the reference bit is 1:

- Check if the last referenced time is within the working set time window (W).
- If within the working set, update the reference bit and last referenced time, and move the clock hand to the next page.
- If not within the working set, continue checking other pages until a suitable candidate is found.

Reference Bit and Clock Hand Updates:

- Update the reference bit and last referenced time for each page when it is referenced.
- Move the clock hand to the next page in the circular list after each page reference.

Timekeeping:

- Maintain a global clock or timestamp to record the current time.
- Update the last referenced time for each page whenever it is referenced.

Working Set Dynamics:

- The working set time window (W) determines the temporal locality of page references. Adjust this parameter based on the characteristics of the system and the workload.

Initialization:

- Initialize the circular list, reference bits, last referenced times, and other necessary data structures.

Implementation Details:

The C++ language used for implementation in this algorithm.

- The code includes necessary C++ standard libraries (iostream, cstdlib, ctime) for input/output operations, random number generation, and time functions.
- Macros are defined for maximum pages (MAX_PAGE), maximum RAM size (MAX_RAM), simulation time (TIME), time interval of working set (TAU), and time to reset reference bits (T_MES).

Data Structures:

1. Page Struct:

- **id:** Identifier for the page.
- **Ref_bit:** Reference bit indicating whether the page has been accessed.
- **time:** Timestamp indicating the last access time.

2. Node Struct:

- Represents a node in a doubly-linked list. Each node contains a **Page** and pointers to the next and previous nodes.

3. **LinkedList Struct:**

- Keeps track of the size and the front node of a doubly-linked list.

Functions:

1. **initializeList:**

- Initializes a linked list with a dummy node (head).

2. **getSize:**

- Returns the size of the linked list.

3. **isEmpty:**

- Checks if the linked list is empty.

4. **searchPage:**

- Searches for a page with a given ID in the linked list.

5. **insertPage:**

- Inserts a new page after a specified position in the linked list.

6. **removePage:**

- Removes a page from the linked list.

7. **printList:**

- Prints the contents of the linked list.

8. **randomPage:**

- Generates a random page with a random ID, reference bit set to 1, and a timestamp.

9. **resetRef_bit:**

- Resets the reference bits of all pages in the linked list to 0.

10. **replacePage:**

- Finds a page to substitute based on the Working Set Clock algorithm. It looks for pages with a reference bit of 0 and a timestamp older than a threshold (**TAU**).

11. **workingSetClock:**

- Simulates the working set clock algorithm over a specified time (**TIME**). It generates random pages and performs page replacement as necessary.

12. main:

- Initializes a linked list and runs the **workingSetClock** simulation.

Simulation Details:

- The main function initializes a linked list and calls **workingSetClock** to simulate the Working Set Clock algorithm over a specified time.
- The algorithm randomly generates pages and simulates page hits.
- When a page is accessed:
 - If the page is in memory, its reference bit is set to 1, and the timestamp is updated.
 - If the page is not in memory: If there is space in memory, the page is inserted. If memory is full, a page is selected for replacement based on the Working Set Clock algorithm.

Outputs:

```
n- 1      Inserted_id: 30      clock: 1
n- 2      Inserted_id: 97      clock: 2
n- 3      Inserted_id: 23      clock: 3
n- 4      Inserted_id: 55      clock: 4
n- 5      Inserted_id: 52      clock: 5
n- 6      Inserted_id: 94      clock: 6
n- 7      Inserted_id: 9 clock: 7
n- 8      Inserted_id: 74      clock: 8
n- 9      Inserted_id: 25      clock: 9
n- 10     Inserted_id: 95      clock: 10
n- 11     Inserted_id: 22      clock: 11
n- 12     Inserted_id: 83      clock: 12
n- 13     Inserted_id: 53      clock: 13
n- 14     Inserted_id: 24      clock: 14
n- 15     Inserted_id: 36      clock: 15
n- 16     Inserted_id: 32      clock: 16
n- 17     Inserted_id: 98      clock: 17
n- 18     Inserted_id: 29      clock: 18
n- 19     Inserted_id: 26      clock: 19
n- 20     Inserted_id: 15      clock: 20
n- 21     Inserted_id: 61      clock: 21
n- 22     Inserted_id: 62      clock: 22
n- 23     Inserted_id: 48      clock: 24
n- 24     Inserted_id: 31      clock: 26
n- 25     Inserted_id: 67      clock: 27
n- 26     Inserted_id: 54      clock: 28
n- 27     Inserted_id: 57      clock: 29
n- 28     Inserted_id: 64      clock: 30
n- 29     Inserted_id: 0 clock: 31
n- 30     Inserted_id: 65      clock: 32
```



```
id_page: 30      page_Ref_bit: 0 time_access: 25
id_page: 23      page_Ref_bit: 0 time_access: 39
id_page: 94      page_Ref_bit: 0 time_access: 40
id_page: 24      page_Ref_bit: 0 time_access: 43
id_page: 36      page_Ref_bit: 0 time_access: 50
id_page: 32      page_Ref_bit: 0 time_access: 35
id_page: 98      page_Ref_bit: 0 time_access: 30
id_page: 15      page_Ref_bit: 0 time_access: 31
id_page: 61      page_Ref_bit: 0 time_access: 47
id_page: 54      page_Ref_bit: 0 time_access: 50
id_page: 77      page_Ref_bit: 0 time_access: 37
id_page: 87      page_Ref_bit: 0 time_access: 34
id_page: 3       page_Ref_bit: 0 time_access: 44
id_page: 82      page_Ref_bit: 0 time_access: 42
id_page: 50      page_Ref_bit: 0 time_access: 44
id_page: 58      page_Ref_bit: 0 time_access: 47
id_page: 91      page_Ref_bit: 0 time_access: 38
id_page: 44      page_Ref_bit: 0 time_access: 29
id_page: 70      page_Ref_bit: 0 time_access: 43
id_page: 40      page_Ref_bit: 0 time_access: 41
id_page: 90      page_Ref_bit: 0 time_access: 30
id_page: 25      page_Ref_bit: 0 time_access: 31
id_page: 56      page_Ref_bit: 0 time_access: 32
id_page: 7       page_Ref_bit: 0 time_access: 45
id_page: 38      page_Ref_bit: 0 time_access: 48
id_page: 55      page_Ref_bit: 0 time_access: 43
id_page: 27      page_Ref_bit: 0 time_access: 41
id_page: 84      page_Ref_bit: 0 time_access: 31
id_page: 76      page_Ref_bit: 0 time_access: 49
id_page: 4       page_Ref_bit: 0 time_access: 39
id_page: 63      page_Ref_bit: 0 time_access: 25
id_page: 21      page_Ref_bit: 0 time_access: 48
id_page: 96      page_Ref_bit: 0 time_access: 33
id_page: 85      page_Ref_bit: 0 time_access: 34
id_page: 17      page_Ref_bit: 0 time_access: 39
id_page: 0       page_Ref_bit: 0 time_access: 41
id_page: 48      page_Ref_bit: 0 time_access: 46
id_page: 66      page_Ref_bit: 0 time_access: 49
id_page: 29      page_Ref_bit: 0 time_access: 50
id_page: 92      page_Ref_bit: 0 time_access: 33
id_page: 93      page_Ref_bit: 0 time_access: 29
```

The code outputs information about inserted and replaced pages at each clock tick, as well as statistics on the number of page hits. Then finally print the list of pages remains. By tracing outputs we see it works according to theoretical process and implement the WSClock page replacement successfully.

Conclusion:

In conclusion, the program provides a simple implementation of a page replacement algorithm using the working set clock policy. It randomly generates page accesses, manages a fixed-size RAM, and simulates the replacement of pages based on the Working Set Clock algorithm.