# Power Log, Peak Detection, and Histogram Accumulator IP

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# 1. **Problem Statement:** Design an integrated HLS IP core

- 1. Computes power or log-power per FFT bin
- 2. Detects peaks that cross a dynamic threshold
- 3. Accumulates histograms to analyze frequency occupancy over time

## 2. Workflow:-

- **1. Design:** The IP core is implemented in **Vitis HLS** C++ (power.h and compute.cpp). It streams complex FFT samples, computes the power in dBm using a fixed-point log approximation, and compares the result to configurable thresholds:
- **Upper threshold(max\_thresh)** detects unusually strong signals.
- Lower threshold(min\_thresh) detects signals that drop below a noise floor or expected minimum.

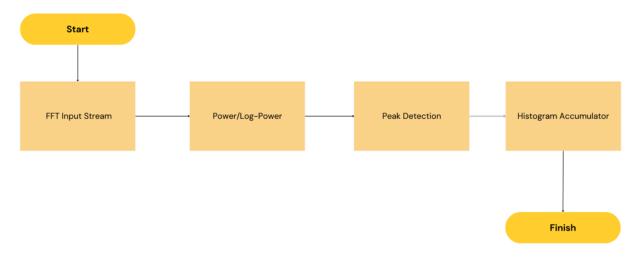
If the computed power crosses either threshold, the sample is flagged as a peak, and the corresponding frequency bin's histogram counter is incremented.

- 2. **Testbench:**The testbench (compute\_tb.cpp) verifies the IP core by:
- Reading FFT samples (real, imag, bin\_index) from 3input.csv.
- Resetting the histogram to clear previous counts.
- Streaming all samples through the IP to compute power, detect peaks, and update the histogram.
- Printing debug information for the first few samples in each bin to check the peak detection logic.
- Saving the final histogram to histogram\_output.csv for visualization.

The thresholds min\_thresh and max\_thresh are configurable and control whether a sample is flagged as a peak. The output verifies that the frequency bins correctly accumulate the number of detected peaks over time.

**3. Visualization:** The histogram output is plotted in Google Colab using Python. The CSV is loaded, frequency bins are labeled in MHz, and a bar graph shows peak counts per bin. The plot verifies how the spectrum is occupied over time.

# 3. Flow Chart:-



# 4. Design Specification:-

import numpy as np

<u>Parameter</u>	<u>Description</u>	
Input Data Format	16-bit signed integer (real & imag)	
Throughput	1 sample/cycle (II=1)	
Platform	Vitis HLS Software	
Pragma	Pipelining & Unrolling	

# 5. <u>Codes:-</u>

# 1. Google colab for the input generation in a csv file [1]

```
import pandas as pd
from scipy.fft import fft
from math import pi

# === Parameters ===
Fs = 100e6 # Sampling frequency
N = 65536 # Number of samples
num_bins = N // 2 # Positive frequency bins = 32768

# === Generate 65536 random samples using (r, theta) ===
r_min = 0.0219
r_max = 0.7096
```

```
# Random magnitudes and phases
r = np.random.uniform(r min, r max, N)
theta = np.random.uniform(0, 2 * pi, N)
# Convert to complex
real time = r * np.cos(theta)
imag time = r * np.sin(theta)
samples = real time + 1j * imag time
# === Perform FFT ===
fft result = fft(samples)
fft positive = fft result[:num bins] # Take only first 32768 positive bins
# === Scale and quantize ===
scale factor = 2**10 # Scale float to int16 range for HLS
real fixed = np.round(np.real(fft positive) * scale factor).astype(np.int16)
imag fixed = np.round(np.imag(fft positive) * scale factor).astype(np.int16)
fft bin number = np.arange(num bins, dtype=np.uint16) # 0 to 32767
# === Create CSV dataframe ===
df = pd.DataFrame({
  "real": real fixed,
  "imag": imag fixed,
  "fft_bin_number": fft_bin_number
})
# === Save to CSV ===
df.to csv("fft input for hls.csv", index=False)
print(" CSV saved: fft input for hls.csv")
# === Optional: download in Google Colab ===
try:
  from google.colab import files
  files.download("fft input for hls.csv")
except:
  pass
```

#### 2. power.h

```
#ifndef POWER_H
#define POWER_H
```

```
#include <ap_int.h>
#include <ap fixed.h>
#include <hls stream.h>
#define N SAMPLES 32768
#define N BINS 16
#define BIN FREQ RES 1525
                                            // \approx 100 \text{MHz} / 65536 \text{ (FFT bin)}
spacing)
#define MAX FREQ 50000000
                                        // Nyquist frequency: 50 MHz
#define FREQ BIN WIDTH (MAX FREQ / N BINS) // Each bin \approx 3.125 MHz
// Type definitions
typedef ap int<16> data t;
typedef ap uint<15> bin_index_t;
typedef ap uint<4>
                    bin t;
typedef ap uint<10> hist t;
typedef ap_fixed<16, 8> dbm_t; // Q8.8 fixed-point type for dBm
// Input sample format
struct sample t {
  data t real;
  data t imag;
  bin_index_t bin_index;
};
// Function to approximate 10*log10(power) using fixed-point math
dbm t log10 approx(ap uint<32> power);
// Top-level IP function for power computation and histogram update
void geeth power(
  hls::stream<sample t>& in stream,
  hist t histogram[N_BINS],
  bool reset histogram,
  dbm t min thresh,
  dbm t max thresh
);
#endif
```

## 3. compute.cpp

```
#include "power.h"
// Accurate fixed-point 10*log10(x) approximation using log2(x)
// Q8.8 format \rightarrow multiply by 256
dbm t log10 approx(ap uint<32> power) {
#pragma HLS inline
   // Avoid log(0)
   if (power == 0) power = 1;
   // Count leading zeros to estimate log2
   ap uint<6> lz = builtin clz(power);
   ap uint<6> log2 int = 31 - lz;
   // Normalize: shift power left so MSB is at bit 31
   ap uint<32> norm = power << lz;</pre>
   // Extract fractional part from high bits
   ap uint<8> frac = (norm >> 23) & 0xFF; // Top 8 bits after MSB
   // log2 fixed = int part << 8 + frac (Q8.8)
   ap uint<16> log2 fixed = (log2 int << 8) | frac;</pre>
   // \log 10(x) \approx \log 2(x) * \log 10(2) \rightarrow \log 10(2) \approx 0.30103
   // So 10*log10(x) \approx log2(x) * 10 * 0.30103 \approx log2(x) * 77 in Q8.8
   dbm t result = (log2 fixed * 77) >> 8;
   return result;
void geeth power(
  hls::stream<sample t>& in stream,
  hist t histogram[N BINS],
  bool reset_histogram,
  dbm_t min_thresh,
  dbm t max thresh
#pragma HLS INTERFACE axis port=in_stream
```

```
#pragma HLS INTERFACE s axilite port=histogram bundle=CTRL
#pragma HLS INTERFACE s_axilite port=reset_histogram bundle=CTRL
#pragma HLS INTERFACE s axilite port=min thresh bundle=CTRL
#pragma HLS INTERFACE s axilite port=max thresh bundle=CTRL
#pragma HLS INTERFACE s axilite port=return bundle=CTRL
#pragma HLS PIPELINE II=1
   static hist t hist local[N BINS] = {0};
#pragma HLS ARRAY PARTITION variable=hist local complete dim=1
   if (reset histogram) {
       for (int i = 0; i < N BINS; i++) {</pre>
#pragma HLS UNROLL
           hist local[i] = 0;
   }
  // Read one sample per call
  sample_t s = in_stream.read();
   // Shift input to remove LSB noise
  data t a = s.real >> 6;
   data t b = s.imag >> 6;
   // Power = a^2 + b^2
   ap uint<32> power = a * a + b * b;
   // Compute 10*log10(power) in Q8.8
   dbm t dbm = log10 approx(power);
   // Determine if it's a peak
   bool peak = (dbm > max thresh || dbm < min thresh);</pre>
  // Get frequency bin from FFT index
  int freq hz = s.bin index * BIN FREQ RES;
  bin_t bin = freq hz / FREQ BIN_WIDTH;
   if (bin >= N BINS) bin = N BINS - 1;
   // Accumulate peak count
   if (peak && !reset_histogram) {
```

```
hist_local[bin]++;
}

// Update output histogram
for (int i = 0; i < N_BINS; i++) {
#pragma HLS UNROLL
    histogram[i] = hist_local[i];
}
</pre>
```

## 4. compute tb.cpp

```
#include "power.h"
#include <fstream>
#include <iostream>
#include <sstream>
#include <string>
int main() {
  hls::stream<sample_t> in_stream;
  hist t histogram[N BINS] = {0};
  int debug count[N BINS] = {0};
  std::ifstream infile("3input.csv");
   if (!infile.is open()) {
       std::cerr << "X ERROR: Could not open 3input.csv\n";</pre>
      return 1;
   }
   std::string line;
  getline(infile, line); // skip header
  const dbm t min thresh = -30;
  const dbm t max thresh = 20;
  // Step 1: Reset histogram
   geeth_power(in_stream, histogram, true, min_thresh, max_thresh);
   // Step 2: Stream all input samples
```

```
while (getline(infile, line)) {
       sample_t s;
                 sscanf(line.c_str(), "%hd,%hd,%hu", &s.real, &s.imag,
&s.bin index);
       in stream.write(s);
      geeth_power(in_stream, histogram, false, min_thresh, max_thresh);
  infile.close();
  // Step 3: Print debug info (first 5 samples per bin)
  std::ifstream infile debug("3input.csv");
  getline(infile debug, line); // skip header again
  std::cout << "\n Debug Info (First 5 Samples Per Bin):\n";
  while (getline(infile debug, line)) {
       sample_t s;
                sscanf(line.c str(), "%hd,%hd,%hu", &s.real,
                                                                   &s.imag,
&s.bin_index);
      data t a = s.real >> 6;
      data t b = s.imag >> 6;
      ap uint<32> power = a * a + b * b;
      dbm_t dbm = log10_approx(power);
      bool peak = (dbm > max thresh || dbm < min thresh);</pre>
      int freq hz = s.bin index * BIN FREQ RES;
      bin t bin = freq hz / FREQ BIN WIDTH;
      if (bin >= N BINS) bin = N BINS - 1;
      if (debug count[bin] < 5) {</pre>
           std::cout << " Bin " << (int)bin</pre>
                     << " | real = " << s.real
                     << ", imag = " << s.imag
                     << ", power = " << (float)dbm
                     << " dBm, peak = " << (peak ? "YES" : "NO") << "\n";
           debug count[bin]++;
```

```
infile_debug.close();
  // Step 4: Save histogram to CSV
   std::ofstream out csv("histogram output.csv");
   if (!out_csv.is_open()) {
       std::cerr << "X ERROR: Could not write to histogram_output.csv\n";</pre>
       return 1;
   }
   out csv << "Bin,Freq Start Hz,Freq End Hz,Peak Count\n";</pre>
   std::cout << "\n Final Peak Histogram (Frequency Bins):\n";</pre>
   for (int i = 0; i < N BINS; i++) {</pre>
       int f_start = i * FREQ_BIN_WIDTH;
       int f_end = (i + 1) * FREQ_BIN_WIDTH - 1;
        std::cout << " Bin " << i << " [" << f start << " Hz - " << f end
<< " Hz] \rightarrow Peaks: "
                 << histogram[i] << "\n";</pre>
           out csv << i << "," << f start << "," << f end << "," <<
histogram[i] << "\n";
   out csv.close();
        std::cout << "\nV histogram_output.csv has been
                                                                       saved
successfully!\n";
  return 0;
```

#### 5. Google colab for histogram plotting

```
import pandas as pd
import matplotlib.pyplot as plt
# Read histogram CSV
```

```
df = pd.read csv("histogram output.csv")
# Convert frequency to MHz with decimal precision
df["Freq Range"] = df.apply(
    lambda row: f"{row['Freq_Start_Hz'] / 1e6:.3f}-{row['Freq_End_Hz'] /
1e6:.3f} MHz", axis=1
)
# Plot
plt.figure(figsize=(14, 6))
edgecolor='black')
plt.xticks(rotation=45, ha='right')
plt.xlabel("Frequency Range (MHz)")
plt.ylabel("Peak Count")
plt.title("Histogram of Peaks Detected per Frequency Bin")
plt.grid(axis='y', linestyle='--', alpha=0.6)
plt.tight layout()
plt.savefig("histogram_plot_mhz.png")
plt.show()
  6. Outputs
Bin, Freq Start Hz, Freq End Hz, Peak Count
0,0,3124999,285
1,3125000,6249999,332
2,6250000,9374999,315
3,9375000,12499999,342
4,12500000,15624999,321
5,15625000,18749999,322
6,18750000,21874999,351
7,21875000,24999999,312
8,25000000,28124999,341
9,28125000,31249999,352
10,31250000,34374999,322
11,34375000,37499999,333
12,37500000,40624999,298
13,40625000,43749999,376
14,43750000,46874999,319
15,46875000,49999999,314
```

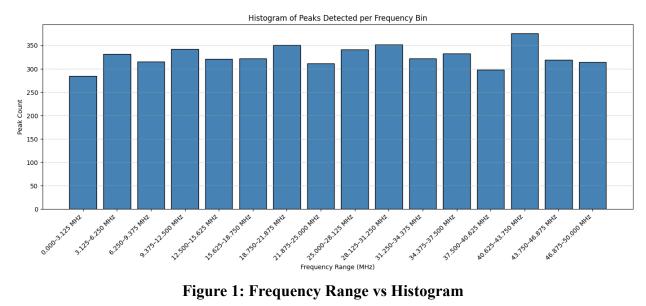


Figure 1: Frequency Range vs Histogram

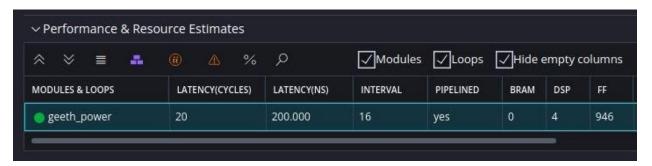


Figure 2: Performance of the Code

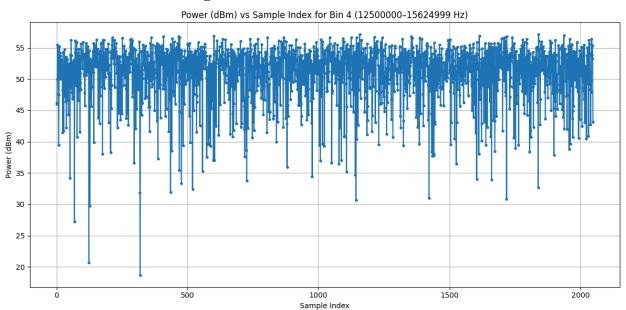


Figure 3: Sample index vs power (dbm)

# 6. Challenges Faced:-

- Debugging stream interfaces and ensuring II=1 pipelining.
- Designing an accurate fixed-point log10 approximation[2].

#### 7. Conclusion:

In this project, an integrated Power Log, Peak Detection, and Histogram Accumulator IP was successfully designed, simulated, and verified. FFT samples were processed in real-time to compute power or log-power, detect peaks above or below dynamic thresholds, and accumulate frequency bin histograms.

# 8. Weekly Report:-

- Week 1&2: Learned basics of RTL and HDL Bits
- Week 3: Given Problem Statement[3]
- Week 4: Did the functionality of the problem statement in vitis[4]
- Week 5: Input Generation in Google Colab[5]
- Week 6: Integrating the Google Colab and Vitis to get the histogram output

#### 9. References:-

- 1. <a href="https://docs.google.com/spreadsheets/d/1tYSwBJcjGEta9Bw-4-LUpVkdr5pXFV5g0q6Z">https://docs.google.com/spreadsheets/d/1tYSwBJcjGEta9Bw-4-LUpVkdr5pXFV5g0q6Z</a> <a href="https://docs.googl
- 2. https://coretechgroup.com/dbm-calculator/
- 3. <a href="https://www.xilinx.com/support/documents/sw\_manuals/xilinx2022\_2/ug1399-vitis-hls.p">https://www.xilinx.com/support/documents/sw\_manuals/xilinx2022\_2/ug1399-vitis-hls.p</a> df
- 4. <a href="https://www.youtube.com/@nitinchandrachoodan6783/playlists">https://www.youtube.com/@nitinchandrachoodan6783/playlists</a>
- 5. <a href="https://www.youtube.com/watch?v=GlbJxX130iE">https://www.youtube.com/watch?v=GlbJxX130iE</a>