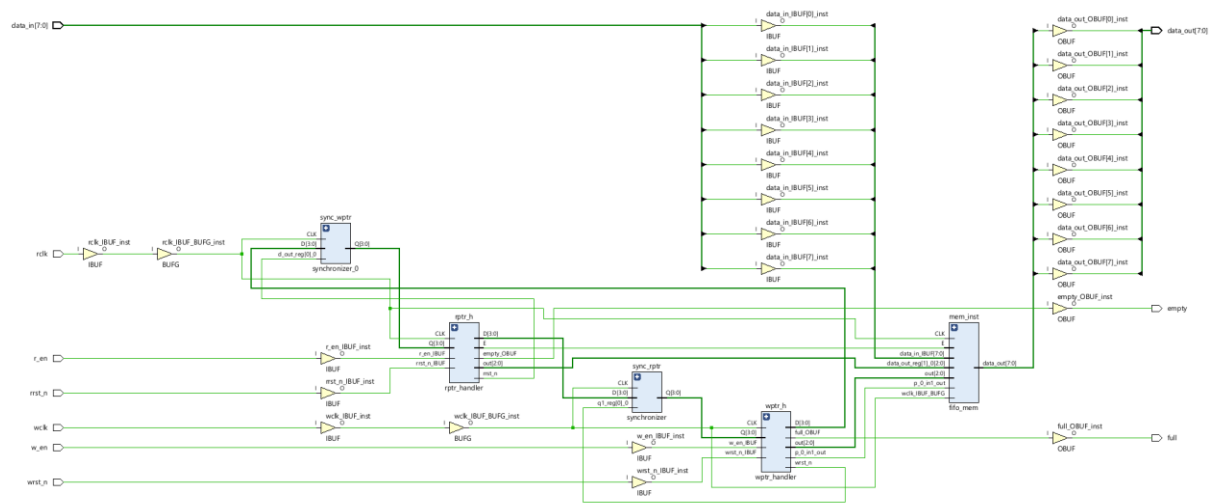


# Asynchronous FIFO Schematic and Simulation:

## Schematic:



## Simulation:

