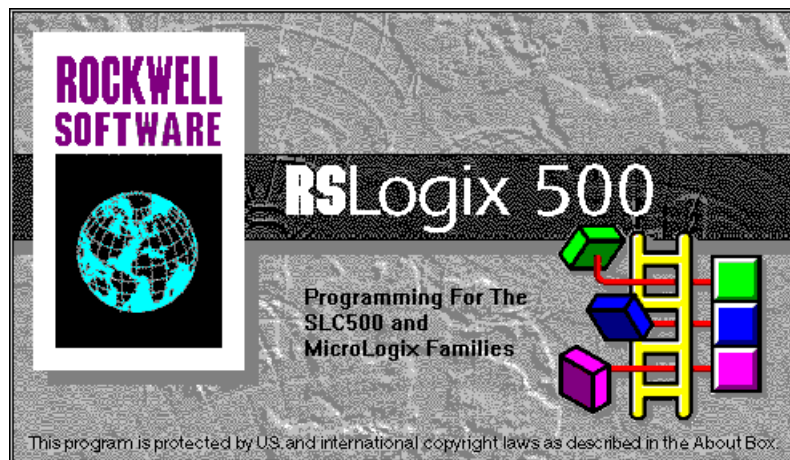


## RSLogix500 Project Report



Processor Information

---

Processor Type: 1747-L524      5/02 CPU - 4K Mem.

Processor Name: SLC05

Total Memory Used: 342 Instruction Words Used - 249 Data Table Words Used

Total Memory Left: 3625 Instruction Words Left

Program Files: 7

Data Files: 10

Program ID: d4bf

## I/O Configuration

0	1747-L524	5/02 CPU - 4K Mem.
1	1746-BAS-5/01	BASIC Module - 500 - 5/01
2		
3		
4	1746-OW16	16-Output (RLY) 240 VAC
5	1746-NIO4I	Analog 2 Ch In/2 Ch Current Out
6	1746-IV16	16-Input (SOURCE) 24 VDC

Channel Configuration

---

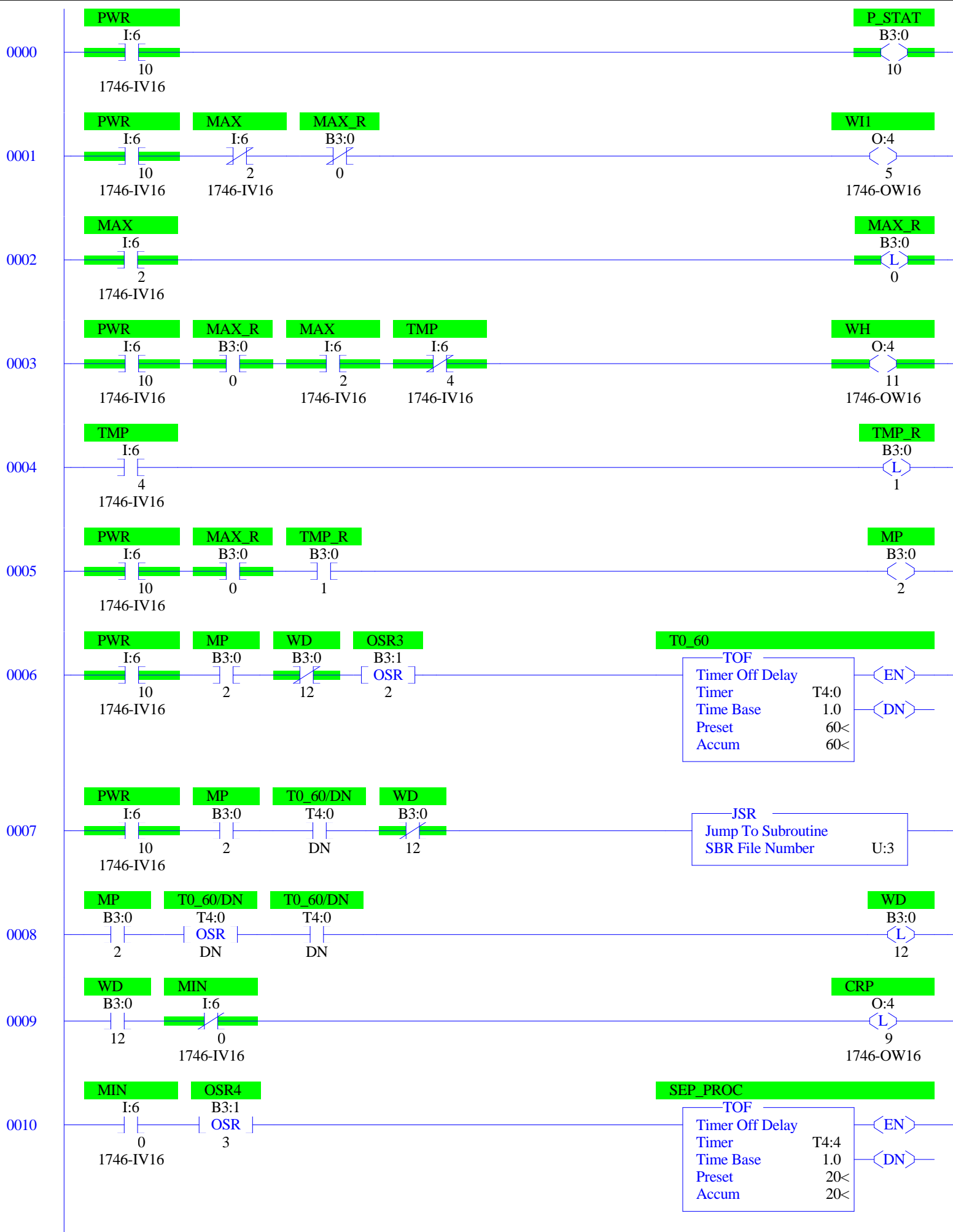
Node : 2 (decimal)  
Baud: 19200

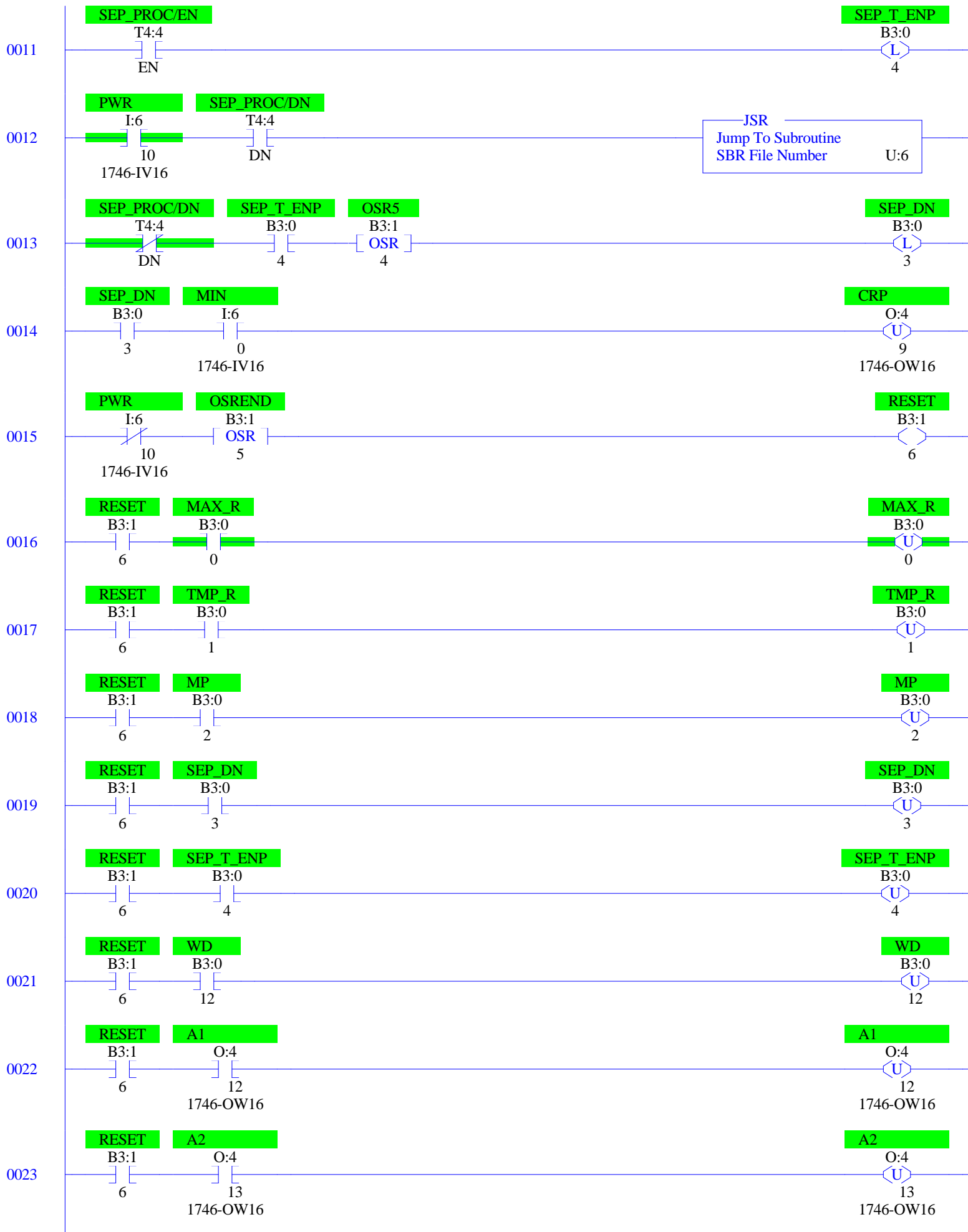
## Program File List

Name	Number	Type	Rungs	Debug	Bytes
[SYSTEM]	0	SYS	0	No	0
	1	SYS	0	No	0
MAIN	2	LADDER	27	No	643
WAHING	3	LADDER	10	No	176
RL_W	4	LADDER	9	No	177
RR_W	5	LADDER	8	No	174
SEP	6	LADDER	9	No	177

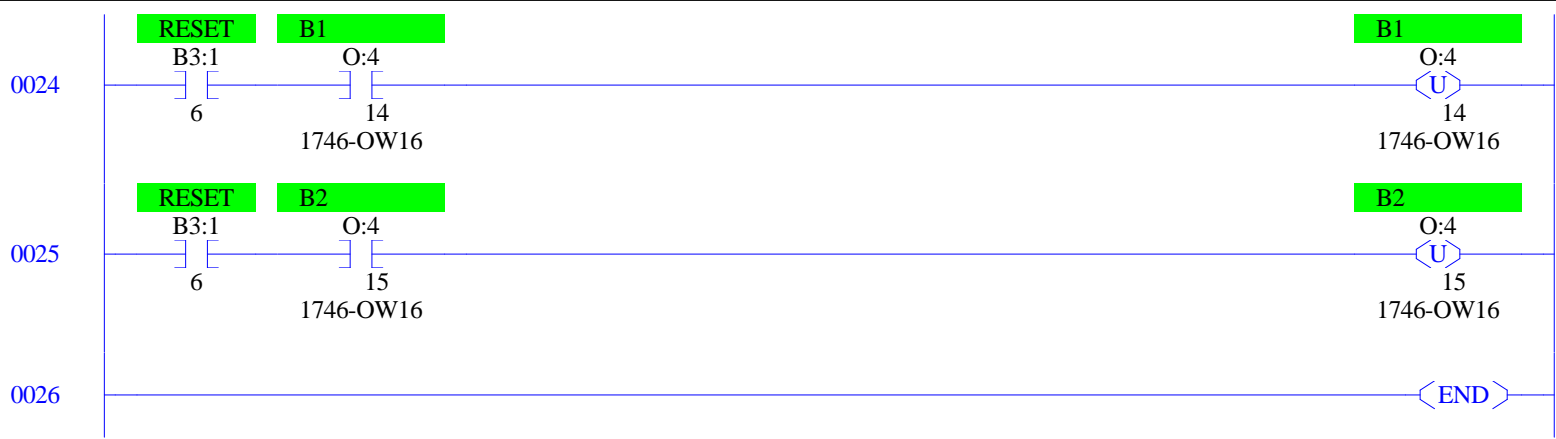
## Data File List

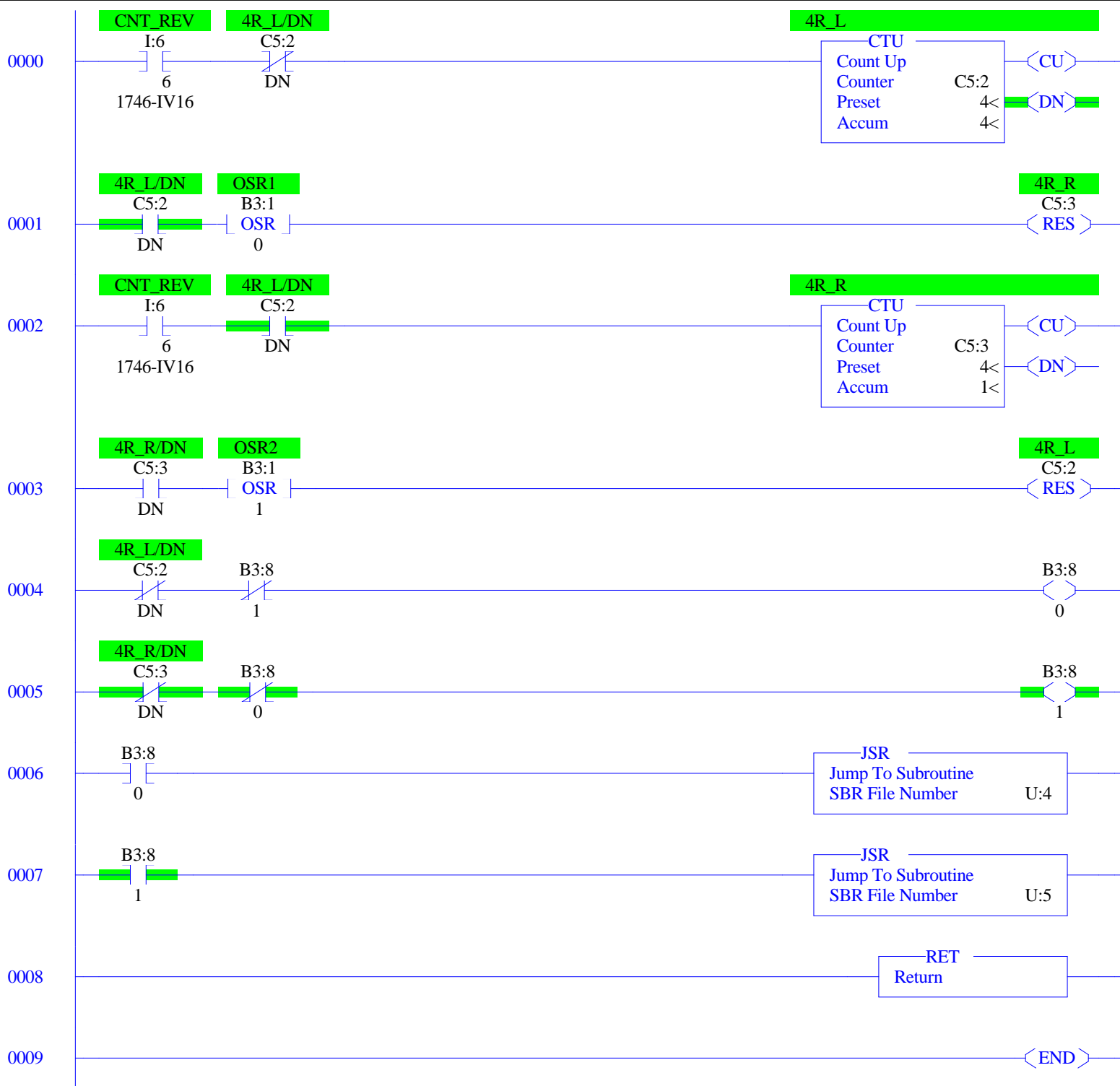
Name	Number	Type	Scope	Debug	Words	Elements	Last
OUTPUT	0	O	Global	No	33	11	O:10
INPUT	1	I	Global	No	33	11	I:10
STATUS	2	S	Global	No	0	33	S:32
BINARY	3	B	Global	No	9	9	B3:8
TIMER	4	T	Global	No	90	30	T4:29
COUNTER	5	C	Global	No	18	6	C5:5
CONTROL	6	R	Global	No	39	13	R6:12
INTEGER	7	N	Global	No	24	24	N7:23
RESERVED	8	r	Global	No	0	0	
	9	T	Global	No	3	1	T9:0

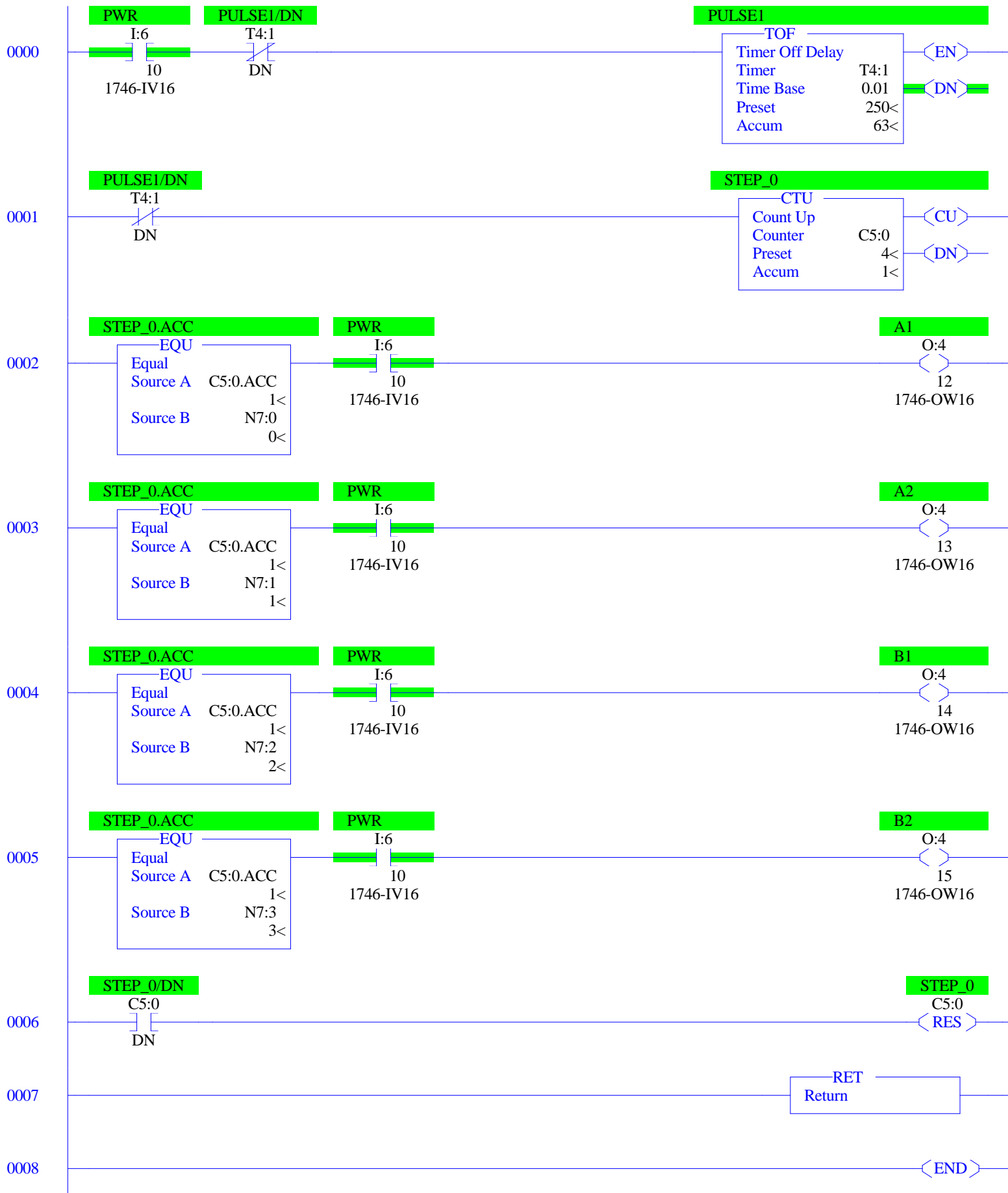


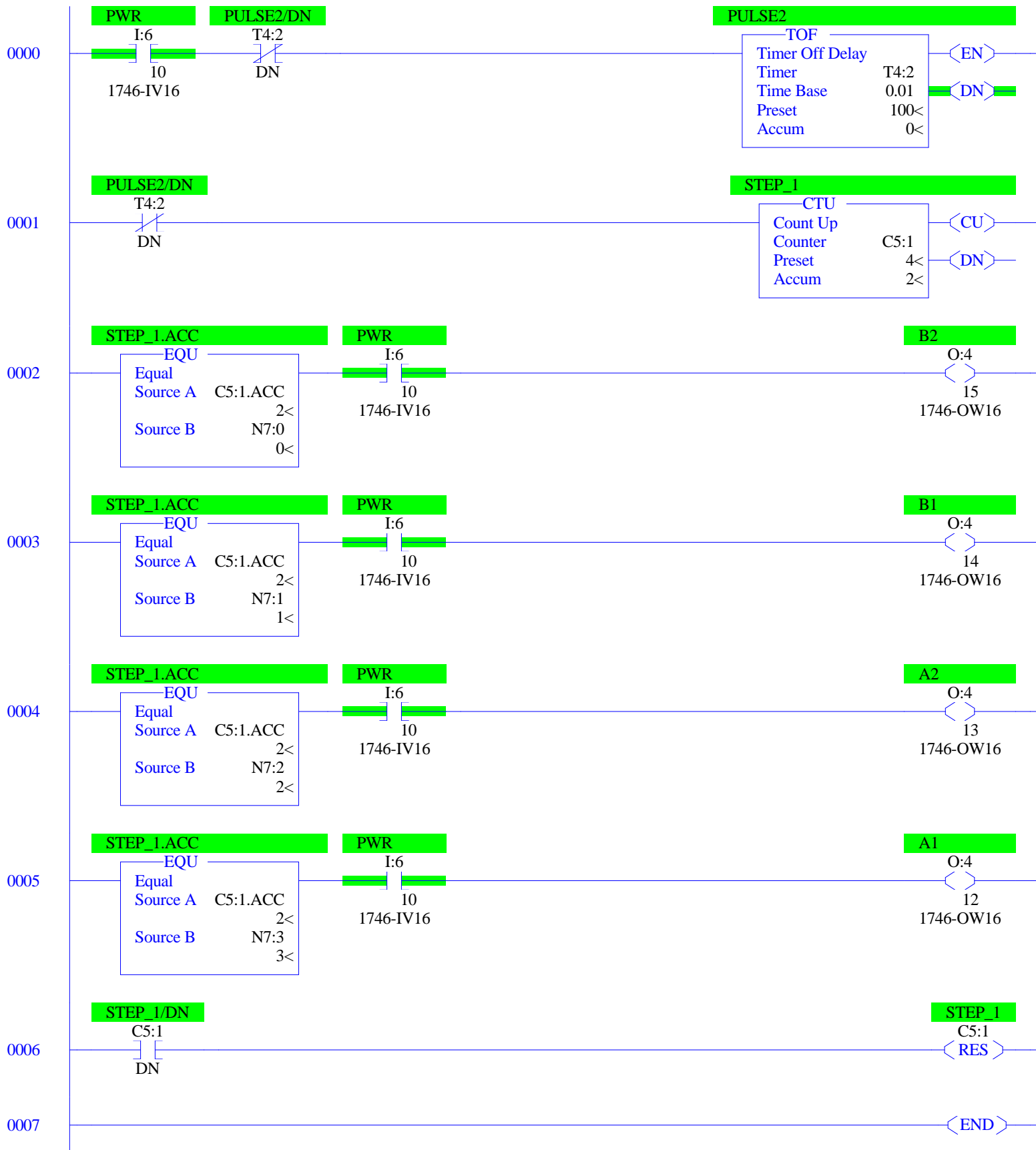


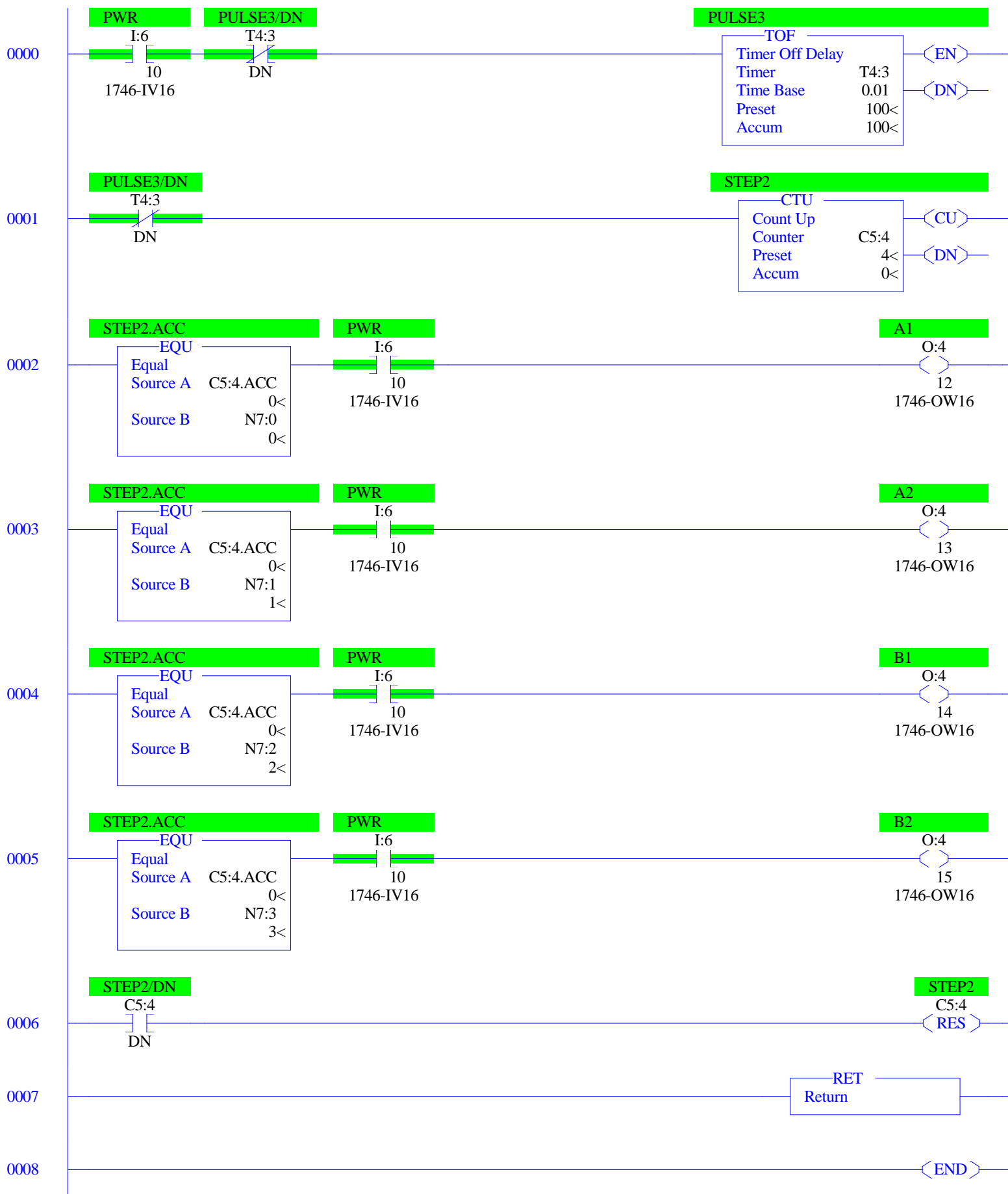












Offset	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
I:1.0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:1.7	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-BAS-5/01 - BASIC Module - 500 - 5/01
I:5.0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1746-NIO4I - Analog 2 Ch In/2 Ch Current O
I:5.1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1746-NIO4I - Analog 2 Ch In/2 Ch Current O
I:6.0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	1746-IV16 - 16-Input (SOURCE) 24 VDC

---

Address (Symbol) = Value [Description]

Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV
B3:0/0	MAX_R	Global				
B3:0/1	TMP_R	Global				
B3:0/2	MP	Global				
B3:0/3	SEP_DN	Global				
B3:0/4	SEP_T_ENP	Global				
B3:0/10	P_STAT	Global				
B3:0/12	WD	Global				
B3:0/13						
B3:1/0	OSR1	Global				
B3:1/1	OSR2	Global				
B3:1/2	OSR3	Global				
B3:1/3	OSR4	Global				
B3:1/4	OSR5	Global				
B3:1/5	OSREND	Global				
B3:1/6	RESET	Global				
B3:2/0	A1_PREV	Global				
B3:2/1	A2_PREV	Global				
B3:2/2	B1_PREV	Global				
B3:2/3	B2_PREV	Global				
C5:0	STEP_0	Global				
C5:0/DN						
C5:0/CU						
C5:1	STEP_1	Global				
C5:2	4R_L	Global				
C5:2/DN						
C5:3	4R_R	Global				
C5:4	STEP2	Global				
C5:4.ACC						
I:6/0	MIN	Global				
I:6/2	MAX	Global				
I:6/4	TMP	Global				
I:6/6	CNT_REV	Global				
I:6/10	PWR	Global				
O:0/1						
O:4/5	WI1	Global				
O:4/7	WI2	Global				
O:4/9	CRP	Global				
O:4/11	WH	Global				
O:4/12	A1	Global				
O:4/13	A2	Global				
O:4/14	B1	Global				
O:4/15	B2	Global				
S:0			Arithmetic Flags			
S:0/0			Processor Arithmetic Carry Flag			
S:0/1			Processor Arithmetic Underflow/ Overflow Flag			
S:0/2			Processor Arithmetic Zero Flag			
S:0/3			Processor Arithmetic Sign Flag			
S:1			Processor Mode Status/ Control			
S:1/0			Processor Mode Bit 0			
S:1/1			Processor Mode Bit 1			
S:1/2			Processor Mode Bit 2			
S:1/3			Processor Mode Bit 3			
S:1/4			Processor Mode Bit 4			
S:1/5			Forces Enabled			
S:1/6			Forces Present			
S:1/7			Comms Active			
S:1/8			Fault Override at Powerup			
S:1/9			Startup Protection Fault			
S:1/10			Load Memory Module on Memory Error			
S:1/11			Load Memory Module Always			
S:1/12			Load Memory Module and RUN			
S:1/13			Major Error Halted			
S:1/14			Access Denied			
S:1/15			First Pass			
S:2/0			STI Pending			
S:2/1			STI Enabled			
S:2/2			STI Executing			
S:2/3			Index Addressing File Range			
S:2/4			Saved with Debug Single Step			
S:2/5			DH-485 Incoming Command Pending			
S:2/6			DH-485 Message Reply Pending			
S:2/7			DH-485 Outgoing Message Command Pending			
S:2/15			Comms Servicing Selection			
S:3			Current Scan Time/ Watchdog Scan Time			
S:4			Time Base			
S:5/0			Overflow Trap			
S:5/2			Control Register Error			
S:5/3			Major Err Detected Executing UserFault Routine			
S:5/4			M0-M1 Referenced on Disabled Slot			
S:5/8			Memory Module Boot			
S:5/9			Memory Module Password Mismatch			
S:5/10			STI Overflow			
S:5/11			Battery Low			
S:6			Major Error Fault Code			
S:7			Suspend Code			
S:8			Suspend File			
S:9			Active Nodes			
S:10			Active Nodes			
S:11			I/O Slot Enables			
S:12			I/O Slot Enables			



## Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV
S:13			Math Register			
S:14			Math Register			
S:15			Node Address/ Baud Rate			
S:16			Debug Single Step Rung			
S:17			Debug Single Step File			
S:18			Debug Single Step Breakpoint Rung			
S:19			Debug Single Step Breakpoint File			
S:20			Debug Fault/ Powerdown Rung			
S:21			Debug Fault/ Powerdown File			
S:22			Maximum Observed Scan Time			
S:23			Average Scan Time			
S:24			Index Register			
S:25			I/O Interrupt Pending			
S:26			I/O Interrupt Pending			
S:27			I/O Interrupt Enabled			
S:28			I/O Interrupt Enabled			
S:29			User Fault Routine File Number			
S:30			STI Setpoint			
S:31			STI File Number			
S:32			I/O Interrupt Executing			
S:33			Extended Proc Status Control Word			
S:33/0			Incoming Command Pending			
S:33/1			Message Reply Pending			
S:33/2			Outgoing Message Command Pending			
S:33/3			Selection Status User/DF1			
S:33/4			Communicat Active			
S:33/5			Communicat Servicing Selection			
S:33/6			Message Servicing Selection Channel 0			
S:33/7			Message Servicing Selection Channel 1			
S:33/8			Interrupt Latency Control Flag			
S:33/9			Scan Toggle Flag			
S:33/10			Discrete Input Interrupt Reconfigur Flag			
S:33/11			Online Edit Status			
S:33/12			Online Edit Status			
S:33/13			Scan Time Timebase Selection			
S:33/14			DTR Control Bit			
S:33/15			DTR Force Bit			
S:34			Pass-thru Disabled			
S:34/0			Pass-Thru Disabled Flag			
S:34/1			DH+ Active Node Table Enable Flag			
S:34/2			Floating Point Math Flag Disable,Fl			
S:35			Last 1 ms Scan Time			
S:36			Extended Minor Error Bits			
S:36/8			DII Lost			
S:36/9			STI Lost			
S:36/10			Memory Module Data File Overwrite Protection			
S:37			Clock Calendar Year			
S:38			Clock Calendar Month			
S:39			Clock Calendar Day			
S:40			Clock Calendar Hours			
S:41			Clock Calendar Minutes			
S:42			Clock Calendar Seconds			
S:43			STI Interrupt Time			
S:44			I/O Event Interrupt Time			
S:45			DII Interrupt Time			
S:46			Discrete Input Interrupt- File Number			
S:47			Discrete Input Interrupt- Slot Number			
S:48			Discrete Input Interrupt- Bit Mask			
S:49			Discrete Input Interrupt- Compare Value			
S:50			Processor Catalog Number			
S:51			Discrete Input Interrupt- Return Number			
S:52			Discrete Input Interrupt- Accumulat			
S:53			Reserved/ Clock Calendar Day of the Week			
S:55			Last DII Scan Time			
S:56			Maximum Observed DII Scan Time			
S:57			Operating System Catalog Number			
S:58			Operating System Series			
S:59			Operating System FRN			
S:61			Processor Series			
S:62			Processor Revision			
S:63			User Program Type			
S:64			User Program Functional Index			
S:65			User RAM Size			
S:66			Flash EEPROM Size			
S:67			Channel 0 Active Nodes			
S:68			Channel 0 Active Nodes			
S:69			Channel 0 Active Nodes			
S:70			Channel 0 Active Nodes			
S:71			Channel 0 Active Nodes			
S:72			Channel 0 Active Nodes			
S:73			Channel 0 Active Nodes			
S:74			Channel 0 Active Nodes			
S:75			Channel 0 Active Nodes			
S:76			Channel 0 Active Nodes			
S:77			Channel 0 Active Nodes			
S:78			Channel 0 Active Nodes			
S:79			Channel 0 Active Nodes			
S:80			Channel 0 Active Nodes			
S:81			Channel 0 Active Nodes			
S:82			Channel 0 Active Nodes			

## Address/Symbol Database

Address	Symbol	Scope	Description	Sym Group	Dev. Code	ABV
S:83			DH+ Active Nodes			
S:84			DH+ Active Nodes			
S:85			DH+ Active Nodes			
S:86			DH+ Active Nodes			
T4:0	T0_60	Global				
T4:1	PULSE1	Global				
T4:1/TT	PULSE_1	Global				
T4:2	PULSE2	Global				
T4:3	PULSE3	Global				
T4:4	SEP_PROC	Global				
U:3						
U:4						
U:5						

Address	Instruction	Description
---------	-------------	-------------

---

Group_Name	Description
------------	-------------