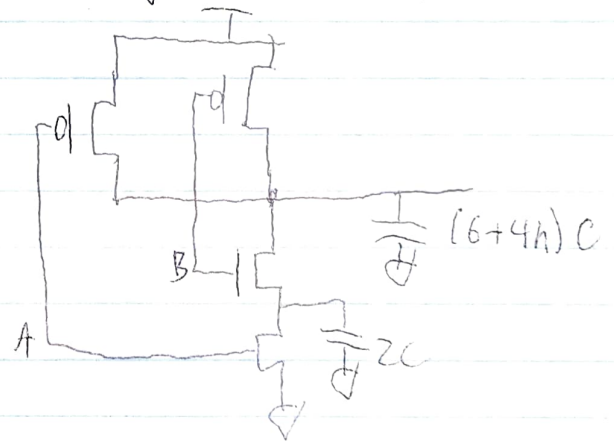
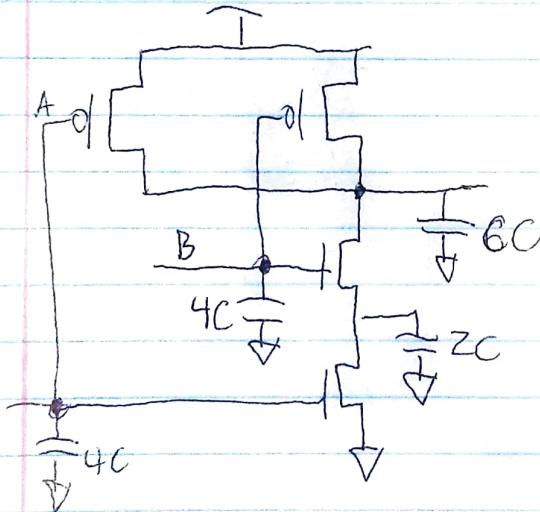
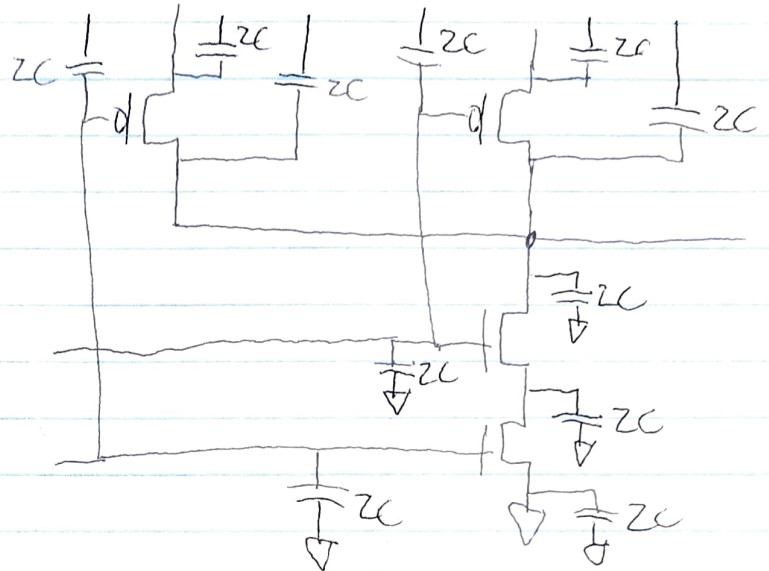
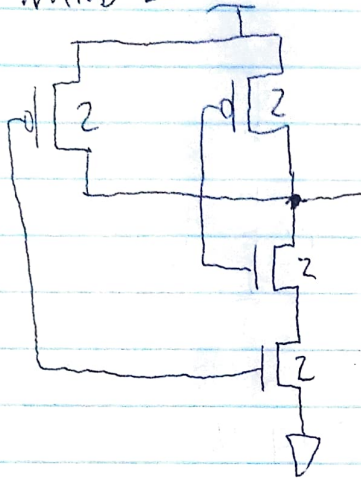


# Sam Sylvester

## VLSI Design I

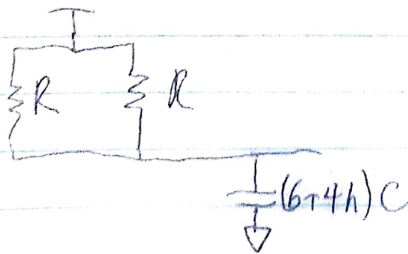
NAND 2



Case AB

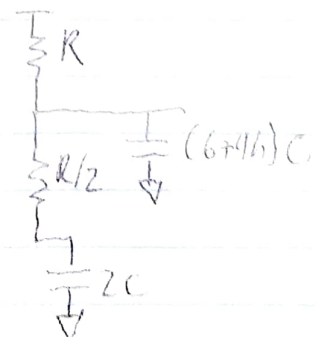
Case 00

Case 01



$$t = \frac{1}{2} (6+4h) RC$$

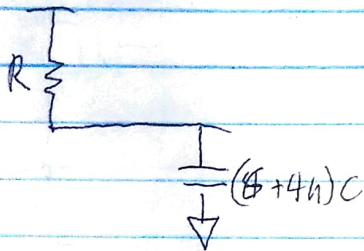
$$= (3+2h) RC$$



$$t = R (6+4h) C + R \cdot \frac{1}{2} C$$

$$= (8+4h) RC$$

Case 10



$$t = (6+4h)RC$$

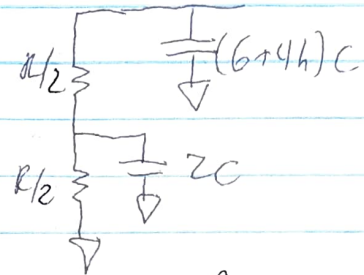
$$t_{pdr} = (8+4h)RC$$

$$t_{pdf} = (7+4h)RC$$

$$t_{cdr} = (3+2h)RC$$

$$t_{cdf} = (7+4h)RC$$

Case 11



$$t = R(6+4h)C + \frac{R}{2}(2C)$$

$$= (7+4h)RC$$

Using  $h=0$ ,  $R=10\text{LSI} \cdot \mu\text{m}$ ,  $C=2\text{fF}/\mu\text{m}$

$$3RC = 60\text{ps}$$

$$RC = 20\text{ps}$$

$$t_{pdr} = 160\text{ps}$$

$$t_{pdf} = 140\text{ps}$$

$$t_{cdr} = 60\text{ps}$$

$$t_{cdf} = 140\text{ps}$$