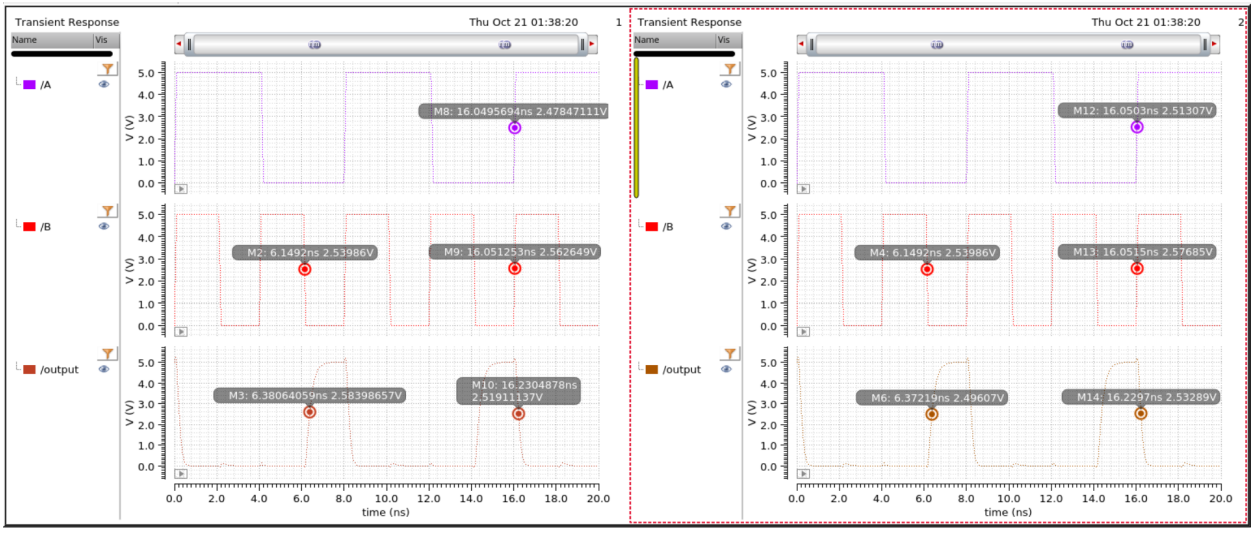


$t_{fall}$ = 179.25 ps
$t_{rise}$ = 231.44 ps

Again, these values don't match very well to the paper results, however they are close enough for the fact that the paper results are a first degree estimation.

| | NAND | NOR |
|---|---|---|
| $t_{pdr}$ (paper) | 160 ps | 100 ps |
| $t_{pdf}$ (paper) | 140 ps | 240 ps |
| $t_{cdr}$ (paper) | 60 ps | 100 ps |
| $t_{cdf}$ (paper) | 140 ps | 60 ps |
| $t_{fall}$ (sim) | 255 ps | 179 ps |
| $t_{rise}$ (sim) | 234 ps | 231 ps |

My results from paper analysis show much lower delay values than my simulation results, but that is due to the simulation results being more accurate and accounting for more capacitances than is reasonable to do by hand.