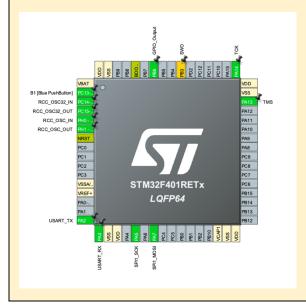
Mark	/11
------	-----

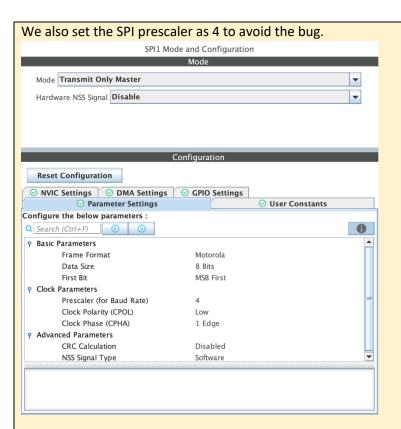
Team name:	A1			
Homework number:	HOMEWORK 09			
Due date:	26/11/24			
Contribution	NO	Partial	Full	
Piombo			х	
Fumagalli			х	
Pierfederici			х	
Zenoni			х	
Ferraro			х	
Notes:				

Project name	SPI – LED MATRIX			
Not done	Partially done (major problems)	Partially done (minor problems)	Completed	
			Х	

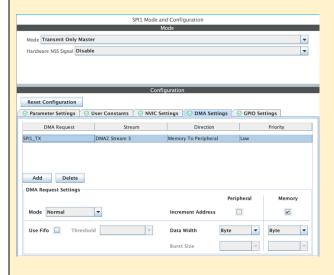
Part 1:

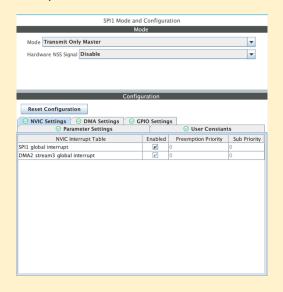
Starting from the ".ioc" we enabled the SPI1 in "Transmit Only Master" mode so that SCK (PA5) and MOSI (PA7) pins are automatically set. Then we set the SS (PB6) pin, which is connected to the RCLK of the shift registers that drive the LED matrix, as GPIO_Output.



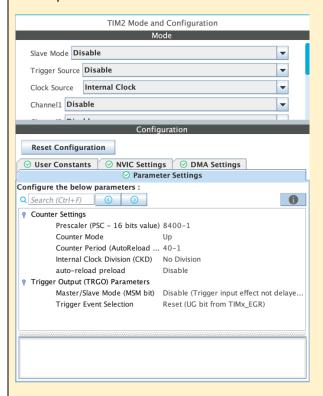


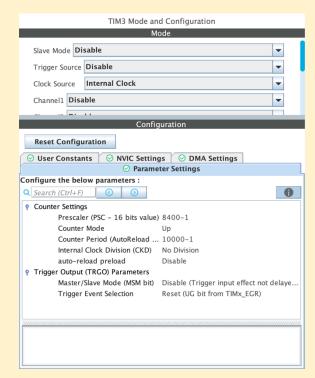
We set the SPI in DMA transmit mode and we enbled its interrupt.





In order to keep our ISR as simple as possible we decided to exploit two timers: TIM2 counts 4ms to refresh the matrix's columns and TIM3 counts 1s to change the letter displayed. We also enabled their interrupts.





Regarding the "main.c", we use the following defines and variables:

We used a single 3D array containing all the values needed to display the characters (S, T) in the LED matrix. The first index is used to choose the desired character.

Each character is saved as 5 pair of bytes to drive 5 columns. Each pair is saved as: {LED_row, LED_column} so that we transmit first the row data to the column shift register that then pass the data to the row shift register, thanks to the Daisy Chain configuration.

In the main function we started both timers in interrupt mode.

```
/* USER CODE BEGIN 2 */

153

__HAL_TIM_CLEAR_IT(&htim2, TIM_IT_UPDATE);
__HAL_TIM_CLEAR_IT(&htim3, TIM_IT_UPDATE);
__HAL_TIM_Base_Start_IT(&htim2);
__HAL_TIM_Base_Start_IT(&htim3);
__HAL_TIM_Base_S
```

These are our callbacks. Every 4ms TIM2 sends the two bytes used to drive one column of the LED matrix. Every second TIM3 changes the "char_index" to change the displayed letter.

When the transmission is over, we provide a positive edge to the RCLK pin to update the output.

```
89⊖/* Private user code --
90 /* USER CODE BEGIN 0 */
 92\odot void HAL_TIM_PeriodElapsedCallback (TIM_HandleTypeDef *htim) {
          i = 0;
//send correct char
 96
                   HAL_SPI_Transmit_DMA(&hspi1, &tx_char[char_index][i++], 2);
 98
 99
         }
100
101
          if (htim == &htim3) {    //timer 3 callback: 1 second elapsed -> change letter
               char_index++;
if (char_index == NUM_OF_CHAR) //char index rollback
    char_index = 0;
102
103
         }
105
106 }
107
108⊕ void HAL_SPI_TxCpltCallback(SPI_HandleTypeDef * hspi) {
                                                                           //SPI transmission completed
      if (hspi == &hspi1) {
   //provide a rising edge to RCLK in order to update the output shift registers
   HAL_GPIO_WritePin(RCLK, GPIO_PIN_SET);
   HAL_GPIO_WritePin(RCLK, GPIO_PIN_RESET);
}
109
110
111
112
113
114 }
116 /* USER CODE END 0 */
```