

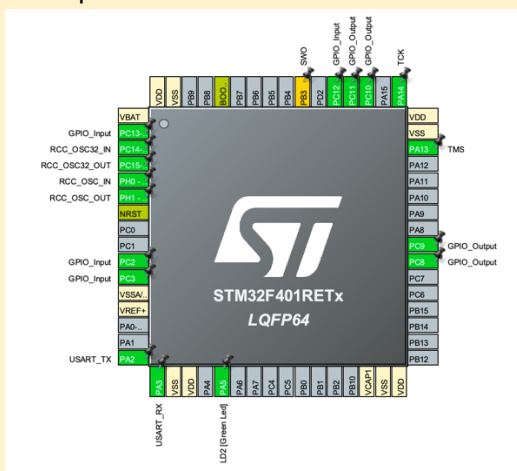
Mark	/11
-------------	------------

Team name:	A1		
Homework number:	HOMEWORK 10		
Due date:	03/12/24		
Contribution	NO	Partial	Full
Piombo			x
Fumagalli			x
Pierfederici			x
Zenoni			x
Ferraro			x
Notes:			

Project name	Keyboard + Encoder		
Not done	Partially done (major problems)	Partially done (minor problems)	Completed
			x

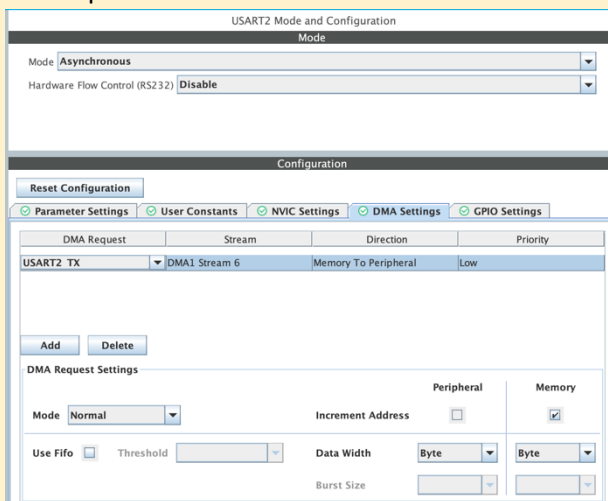
Part 1:

In the “.ioc” we set the I/O pins to use the keyboard and we checked the proper configuration of the UART pins.



We also set TIM2 to rise an interrupt every 5ms to change the keyboard's column we are reading from.

Additionally, we set the USART2 in DMA mode to send the characters to the PC, enabling also its interrupt.



Looking at the schematic we defined the keyboard pins:

```

32 /* Private define -----
33 /* USER CODE BEGIN PD */
34
35 #define ROW_0 GPIOC,GPIO_PIN_3
36 #define ROW_1 GPIOC,GPIO_PIN_2
37 #define ROW_2 GPIOC,GPIO_PIN_13
38 #define ROW_3 GPIOC,GPIO_PIN_12
39
40 #define COL_0 GPIOC,GPIO_PIN_11
41 #define COL_1 GPIOC,GPIO_PIN_10
42 #define COL_2 GPIOC,GPIO_PIN_9
43 #define COL_3 GPIOC,GPIO_PIN_8
44
45 #define NUM_OF_SAMPLES 2
46 #define ROW_LENGTH 4
47 #define COLUMN_LENGTH 4
48
49 /* USER CODE END PD */

```

We choose to sample the buttons only twice because it's enough to correctly debounce.

These are our variables:

```

62 /* USER CODE BEGIN PV */
63
64 uint8_t sample_index = 0; //index of the samples collected in order to debounce
65 uint8_t col_index = 0; //index of the number of the column
66 uint8_t row_index = 0; //index of the number of the row
67
68 uint8_t btn = 0; //button state (used also for debouncing) - if btn = 0 -> button is pressed
69
70 uint8_t i = 0;
71 uint8_t j = 0;
72 uint8_t k = 0;
73
74 uint8_t rx_data[NUM_OF_SAMPLES][COLUMN_LENGTH][ROW_LENGTH]; //buttons state - 3rd dimension stores the different samples to debounce
75
76 char out_matrix[COLUMN_LENGTH][ROW_LENGTH] = {
77     {'F', 'E', 'D', 'C'},
78     {'B', 'A', '9', '8'},
79     {'7', '6', '5', '4'},
80     {'3', '2', '1', '0'}
81 };
82
83 uint8_t old_btn[COLUMN_LENGTH][ROW_LENGTH] = { //previous state of the buttons
84     {1, 1, 1, 1},
85     {1, 1, 1, 1},
86     {1, 1, 1, 1},
87     {1, 1, 1, 1},
88 };
89
90 /* USER CODE END PV */
91

```

In the main function we initialized “rx_data” matrix with all ones (buttons not pressed). Then we set the first column and we started TIM2 in interrupt mode.

```
201  /* USER CODE BEGIN 2 */
202
203  for (i = 0; i < NUM_OF_SAMPLES; i++) {
204      for (j = 0; j < COLUMN_LENGTH; j++) {
205          for (k = 0; k < ROW_LENGTH; k++) {
206              rx_data[i][j][k] = 1;
207          }
208      }
209  }
210
211  HAL_GPIO_WritePin(COL_0, GPIO_PIN_SET); //drive the first column
212
213  __HAL_TIM_CLEAR_IT(&htim2, TIM_IT_UPDATE); //clear interrupt request BEFORE enabling tim interrupt
214  HAL_TIM_Base_Start_IT(&htim2); //start TIM2 in interrupt mode
215
216  /* USER CODE END 2 */
```

In the timer’s callback, for each row of the selected column we read the button’s state, we debounce it and we send the corresponding value in case the button is not kept pressed from the previous reading. Here’s an example for the first row:

```
102  /* Private user code -----*/
103  /* USER CODE BEGIN 0 */
104
105  void HAL_TIM_PeriodElapsedCallback (TIM_HandleTypeDef *htim) { //5ms elapsed
106      if (htim == &htim2) {
107          row_index = 0;
108
109          //ROW_0
110          rx_data[sample_index][row_index][col_index] = HAL_GPIO_ReadPin(ROW_0); //save the btn state
111          //debouncing: check if the btn is stably zero (check if many samples are = to 0)
112          for (i = 0, btn = 0; i < NUM_OF_SAMPLES; i++)
113              btn = btn || rx_data[i][row_index][col_index];
114
115          //check if button already pressed: print only if previously not pressed, now pressed
116          if (!btn && old_btn[row_index][col_index])
117              HAL_UART_Transmit_DMA(&huart2, &out_matrix[row_index][col_index], 1);
118
119          old_btn[row_index][col_index] = btn; //save the state for the next check
120      }
```

This is repeated for all rows incrementing “row_index”.

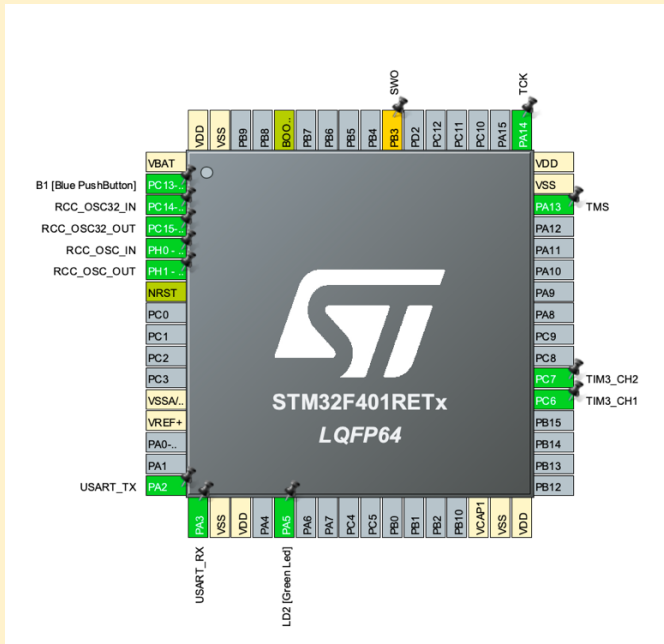
In the end, we increment “col_index” and if necessary “sample_index”.

Then we set the proper column and we reset the others for the next reading.

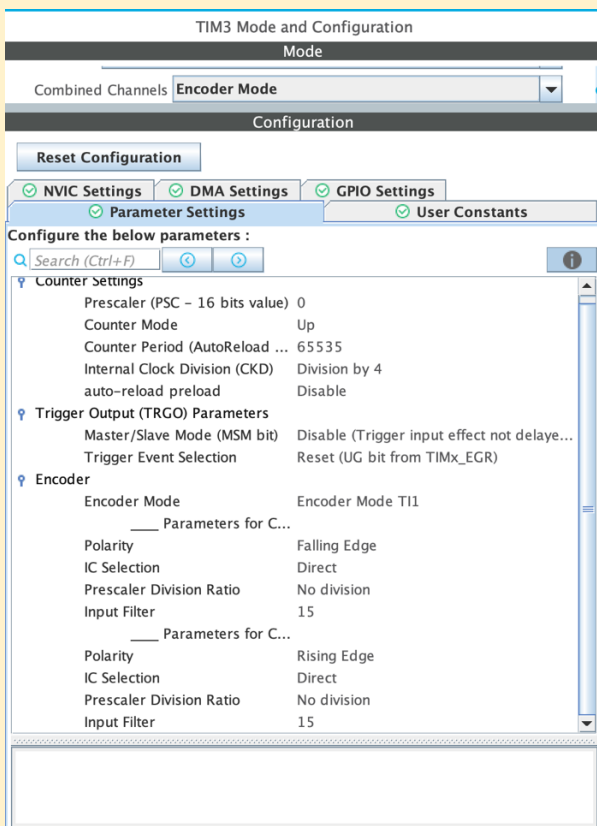
```
151      col_index++;
152      if (col_index == ROW_LENGTH) {
153          col_index = 0;
154          sample_index++; //need to acquire new sample
155          if (sample_index == NUM_OF_SAMPLES)
156              sample_index = 0;
157      }
158
159      //columns drive manager
160      HAL_GPIO_WritePin(COL_0, (col_index == 0) ? GPIO_PIN_SET : GPIO_PIN_RESET);
161      HAL_GPIO_WritePin(COL_1, (col_index == 1) ? GPIO_PIN_SET : GPIO_PIN_RESET);
162      HAL_GPIO_WritePin(COL_2, (col_index == 2) ? GPIO_PIN_SET : GPIO_PIN_RESET);
163      HAL_GPIO_WritePin(COL_3, (col_index == 3) ? GPIO_PIN_SET : GPIO_PIN_RESET);
164  }
165  }
166
167  /* USER CODE END 0 */
168
```

Part 2:

In the “.ioc” we set PC6 and PC7 to use TIM3 in Encoder mode and we checked the proper configuration of the UART pins.



Then we enabled TIM3 in Encoder mode TI1 with the proper polarity and setting the timer’s digital filter at 15.



TIM2 is configured in interrupt mode to enter in the ISR each second.

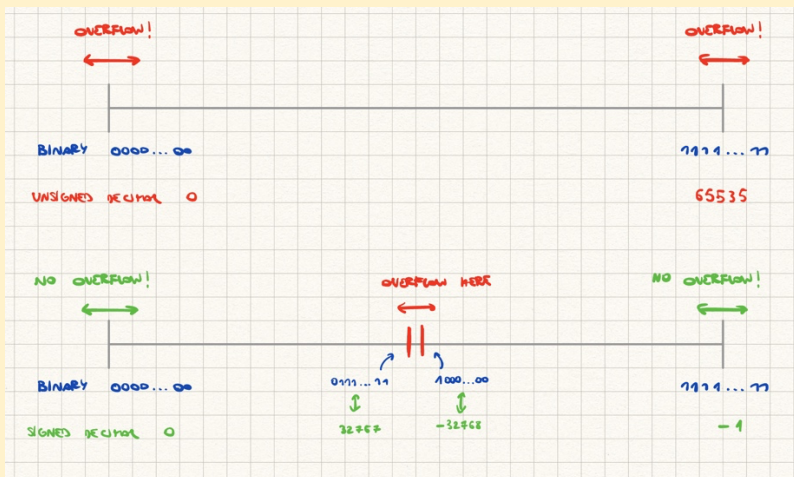
We enabled USART2 in DMA mode as in the previous project.

These are our variables and our defines:

```
35 /* Private define -----
36  /* USER CODE BEGIN PD */
37
38 #define STR_LEN 32
39 #define FULL_CYCLE_COUNT 24.0
40 #define SEC_TO_MIN 60
41
42 /* USER CODE END PD */
```

```
56 /* USER CODE BEGIN PV */
57
58 //signed to solve overflow/underflow problem of timer counter
59 int16_t counter = 0;
60 int16_t old_counter = 0;
61 int16_t delta = 0;
62
63 float rpm_value = 0;
64
65 int length = 0;
66 char string[STR_LEN];
67
68 /* USER CODE END PV */
```

We collect the values from the timer's counter as signed integer to solve the overflow/underflow problem.



As shown in the drawing: if we used unsigned integers we easily face an underflow problem when the delta is calculated around 0/65535, while using signed integers (cpl2 format) this problem is moved in the middle of the counter range, which is hardly reached in our application.

In the main function we start our timers in their corresponding modes.

```
134 /* USER CODE BEGIN 2 */
135
136 HAL_TIM_Encoder_Start(&htim3, TIM_CHANNEL_ALL); //start TIM3 in encoder mode
137 HAL_TIM_CLEAR_IT(&htim2, TIM_IT_UPDATE); //clear interrupt request BEFORE enabling tim interrupt
138 HAL_TIM_Base_Start_IT(&htim2); //start TIM2 in interrupt mode
139
140 /* USER CODE END 2 */
```

In the timer's ISR we acquire the new counter value and compute the difference with respect to the old one. Then we convert it into displacement dividing by "FULL_CYCLE_COUNT"; the displacement value corresponds to the rps one because we acquire each second ($\text{rps} = \text{displacement}/1\text{s}$).

Finally rpm are obtained by multiplying by 60, and the value is sent to the PC.

```
84= void HAL_TIM_PeriodElapsedCallback (TIM_HandleTypeDef *htim) { //timer 2 callback called every second
85
86     if (htim == &htim2) {
87
88         counter = __HAL_TIM_GET_COUNTER(&htim3);
89         delta = counter - old_counter;
90         old_counter = counter;
91
92         rpm_value = (delta/FULL_CYCLE_COUNT)*SEC_TO_MIN;    //convert in rpm
93
94         int length = snprintf(string, sizeof(string), "Speed: %.1frpm\n", rpm_value);
95         HAL_UART_Transmit_DMA(&huart2, string, length);
96     }
97 }
98
99 /* USER CODE END 0 */
```