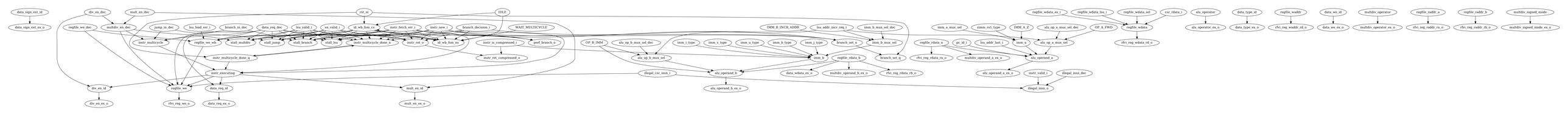
data_sign_ext_id
data_sign_ext_ex_o
div_en_dec
regfile_we_dec
mult_en_dec
multdiv_en_dec
rst_ni
IDLE
WAIT_MULTICYCLE
jump_in_dec
lsu_load_err_i
branch_in_dec
data_req_dec
lsu_valid_i
ex_valid_i
id_wb_fsm_cs
instr_fetch_err_i
instr_new_i
branch_decision_i
instr_multicycle
regfile_we_wb
stall_multdiv
stall_jump
stall_branch
stall_lsu
instr_multicycle_done_n
instr_ret_o
id_wb_fsm_ns
instr_is_compressed_i
perf_branch_o
instr_ret_compressed_o
instr_multicycle_done_q
instr_executing
div_en_id
div_en_ex_o
regfile_we
rfvi_reg_we_o
data_req_id
data_req_ex_o
mult_en_id
mult_en_ex_o
OP_B_IMM
alu_op_b_mux_sel_dec
alu_op_b_mux_sel
illegal_csr_insn_i
IMM_B_INCR_ADDR
lsu_addr_incr_req_i
imm_b_mux_sel_dec
imm_i_type
imm_s_type
imm_u_type
imm_b_type
imm_j_type
branch_set_n
imm_b_mux_sel
imm_b
branch_set_q
alu_operand_b
alu_operand_b_ex_o
imm_a_mux_sel
zimm_rs1_type
IMM_A_Z
alu_op_a_mux_sel_dec
OP_A_FWD
imm_a
alu_op_a_mux_sel
regfile_rdata_a
pc_id_i
lsu_addr_last_i
multdiv_operand_a_ex_o
alu_operand_a
alu_operand_a_ex_o
regfile_wdata_ex_i
regfile_wdata_lsu_i
regfile_wdata_sel
csr_rdata_i
regfile_wdata
rfvi_reg_wdata_rd_o
alu_operator
alu_operator_ex_o
data_type_id
data_type_ex_o
regfile_waddr
rfvi_reg_waddr_rd_o
data_we_id
data_we_ex_o
multdiv_operator
multdiv_operator_ex_o
regfile_raddr_a
rfvi_reg_raddr_ra_o
regfile_raddr_b
rfvi_reg_raddr_rb_o
multdiv_signed_mode
multdiv_signed_mode_ex_o
regfile_rdata_b
data_wdata_ex_o
multdiv_operand_b_ex_o
rfvi_reg_rdata_rb_o
rfvi_reg_rdata_ra_o
instr_valid_i
illegal_insn_dec
illegal_insn_o