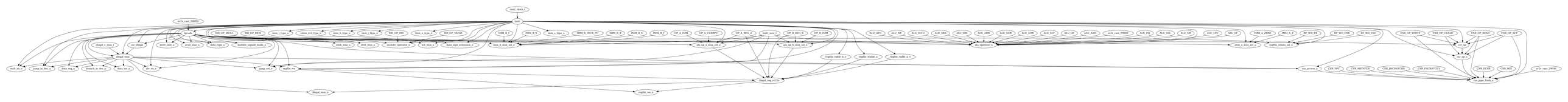

CSR_OP_CLEAR

csr_op

csr_op_o

csr_pipe_flush_o

sv2v_cast_F99D1

alu_operator_o

MD_OP_MULH

multdiv_operator_o

MD_OP_MULL IMM_B_INCR_PC

imm_b_mux_sel_o

instr

div_en_o

imm_s_type_o

ebrk_insn_o

jump_set_o

csr_illegal dret_insn_o wfi_insn_o data_sign_extension_o alu_op_a_mux_sel_o

regfile_waddr_o

alu_op_b_mux_sel_o

opcode

jump_in_dec_o regfile_we csr_access_o

imm_i_type_o zimm_rs1_type_o imm_b_type_o

regfile_raddr_a_o

mret_insn_o imm_a_mux_sel_o

illegal_insn

imm_j_type_o

ecall_insn_o data_type_o

imm_u_type_o

regfile_wdata_sel_o

regfile_raddr_b_o

multdiv_signed_mode_o

mult_en_o

illegal_reg_rv32e

data_req_o branch_in_dec_o data_we_o

illegal_insn_o regfile_we_o

MD_OP_REM IMM_B_I IMM_B_S IMM_B_B IMM_B_U IMM_B_J instr_new_i OP_B_REG_B ALU_GEU ALU_NE ALU_SLTU ALU_SRA ALU_SRL ALU_SLT ALU_GE ALU_EQ ALU_SLL ALU_OR ALU_LTU ALU_LT IMM_A_ZERO RF_WD_CSR CSR_OP_READ

CSR_DCSR CSR_MIE

sv2v_cast_DB892

ALU_ADD RF_WD_LSU

sv2v_cast_290A1

OP_A_IMM OP_A_CURRPC

CSR_DPC

ALU_SUB

illegal_c_insn_i

MD_OP_DIV OP_A_REG_A OP_B_IMM ALU_XOR ALU_AND IMM_A_Z RF_WD_EX CSR_OP_WRITE

instr_rdata_i

CSR_MSTATUS CSR_DSCRATCH0 CSR_DSCRATCH1

CSR_OP_SET