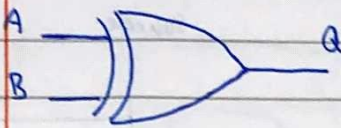


i) XOR

$\therefore A, B$ is input, Q is the output

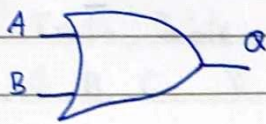


Symbol \nearrow

Truth Table

A	B	Q
0	0	0
0	1	1
1	0	1
1	1	0

ii) NOR

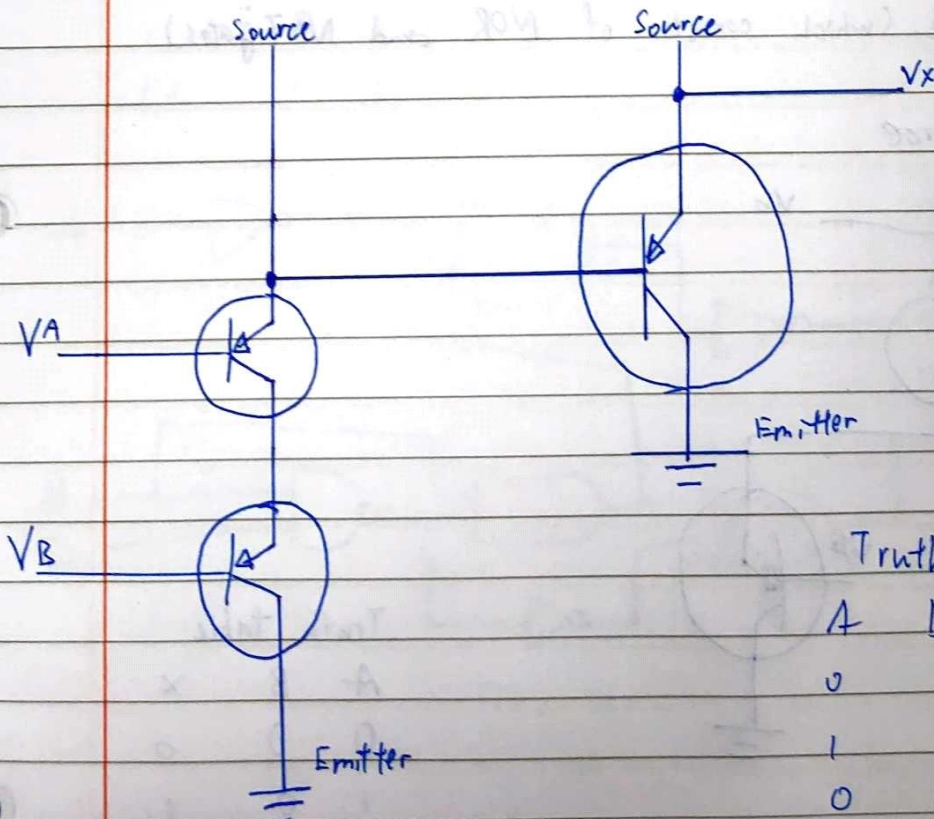


Symbol \nearrow

Truth Table

A	B	Q
0	0	1
0	1	0
1	0	0
1	1	0

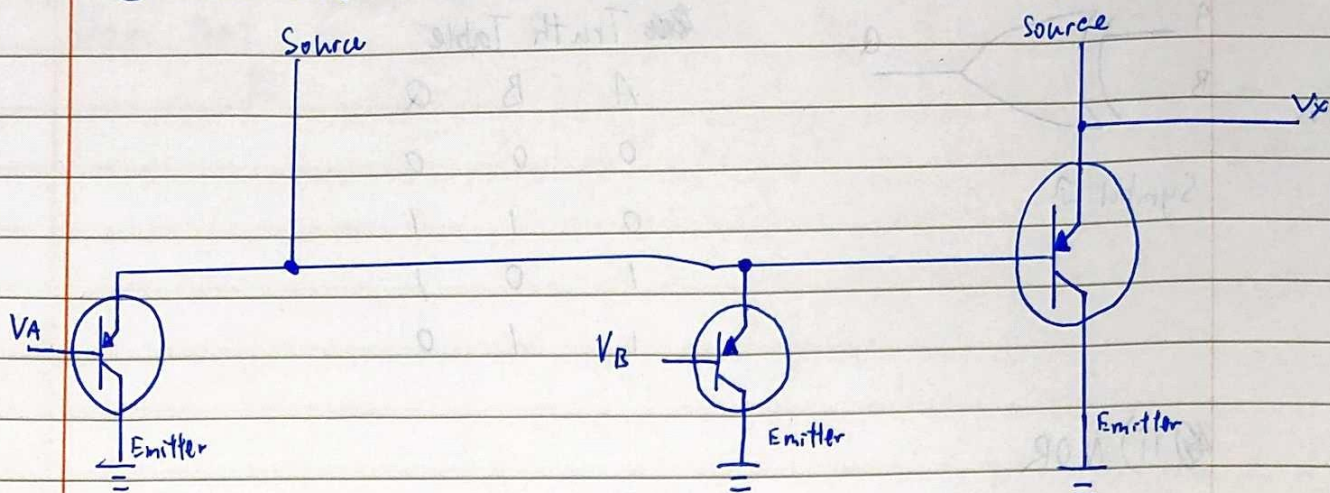
b) AND Gate (which consists of NAND and NOT gates)



Truth Table

A	B	X
0	0	0
1	0	0
0	1	0
1	1	1

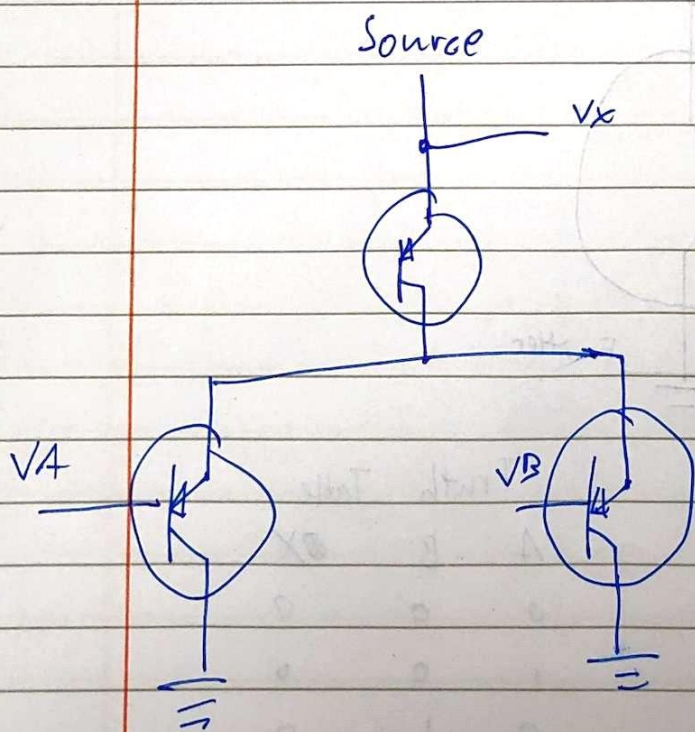
2.c) OR Gate (which consists of NOR and NOT gates)



Truth table

A	B	x
0	0	0
1	0	1
0	1	1
1	1	1

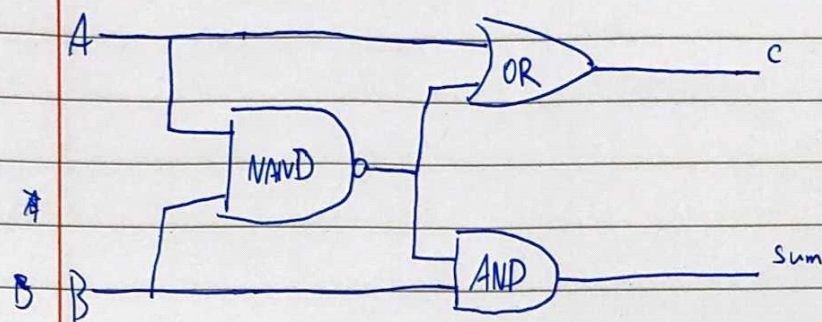
2.c) XOR Gate (which consists of NOR and NOT gates)



Truth table

A	B	x
0	0	0
1	0	1
0	1	1
1	1	0

2) Half Adder using NAND, OR and AND gate



Truth table

A	B	C	Sum
0	0	1	0
0	1	1	1
1	0	1	0
1	1	1	0

3) Truth Table

A	B	C	Y
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

