loops passing through in the same
loops passing through in the assumed current
direction minus the opposite direction
through in the opposite direction

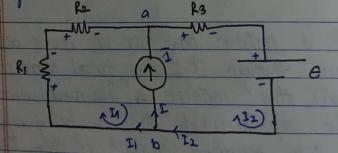
b) The pularity of a voltage source is unaffected by the direction of the assigned loop currents.

> Step 4: Solve the resulting simultaneous linear equations for the assumed loop currents.

< Num. No. 34 3 In numer | 35/36/37 = In num copy)

Subermenh:

If current source is between two-meth systems, it is called a cuper-meth.



For solving super-mesh system, the circuit is redrawn removing the ament source.

At node b, applying KCL, $I_2 = I + I_1$ or, $I = I_2 - I_1$ or, $I = -I_1 + I_2 - (i)$

For loop 1, $-I_1R_1 - I_2R_2 - I_2R_3 - E = 0$ $C + I_1R_1 + I_2R_2 + I_2R_3 = 0$

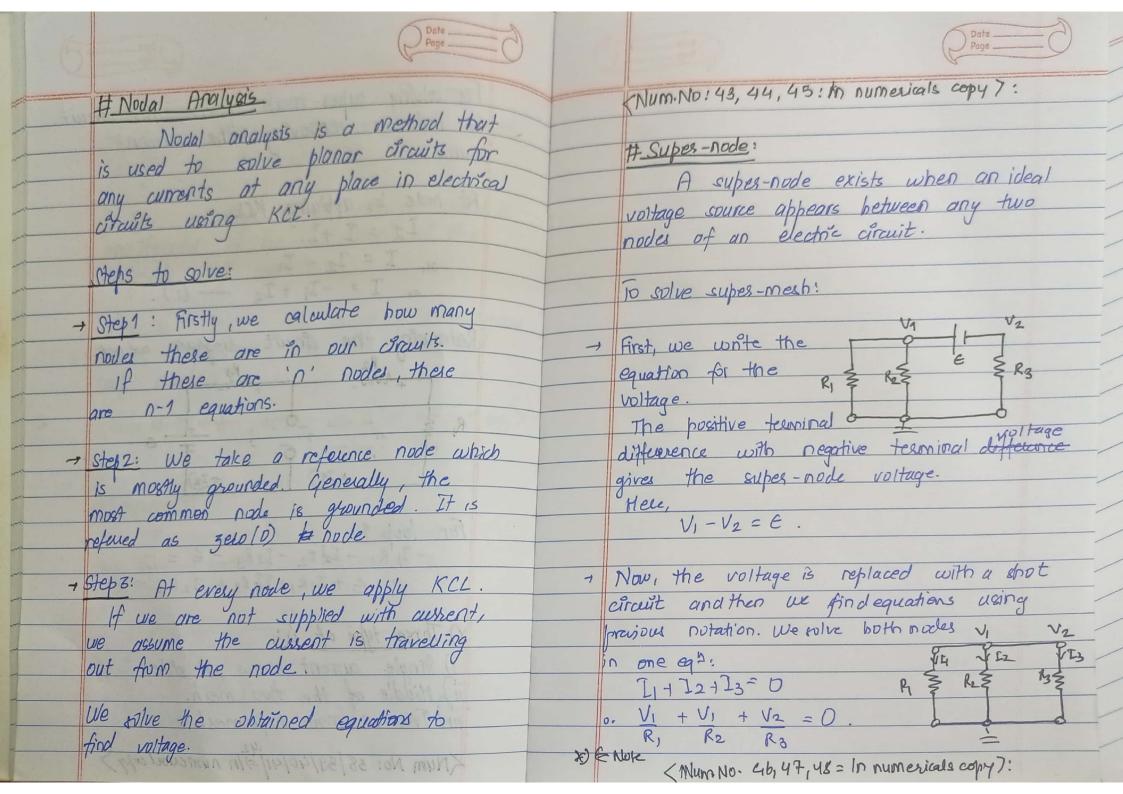
No Three type of Q:

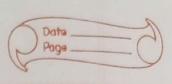
i) Single current source at side

ii) Middle of the two mesh.

iii) Two or more supermesh.

(Num No: 38/39/40/41/=)In numerical copy





(X)	Note: (Note copy content)
	when to use mesh analysis or
	node analysis
	1): If number of nudes = number of mesh,
	we can use any method
	(i) A de
	i) If number of nodes less than number
	of mesh, we use nodal analysis.
	ii) If number of nodes mesh less than
	number of nodes, we use mesh analysis.
	E & &
	ON STATE OF THE PROPERTY OF TH
	Land Control of the C
	(Cetroung the circuit)
	86.68 = 93.33