

EDIT distance using DP.

edit distance has overlapping subproblems, ~~use~~ use memoization.

DP $[N] \times [M] \Rightarrow$ $TC = (N \times M)$ no of states

Space = $O(N \times M)$ \leftarrow DP array
 $+ O(N+M)$ \leftarrow auxilliary stack space

int $f(\text{int } i, \text{int } j, \text{string } s_1, s_2)$ {
 if ($i < 0$) return $j+1$;
 if ($j < 0$) return $i+1$;
 // If either string gets exhausted, return other string length.

if ($s_1[i] == s_2[j]$) return $0 + f(i-1, j-1, s_1, s_2)$; character matches
 else no insert, del or update if matches

return $1 + \min(f(i-1, j, s_1, s_2), f(i, j-1, s_1, s_2), f(i-1, j-1, s_1, s_2))$

using DP

Memoization

$f(i, j, s_1, s_2, dp)$

{ if ($i < 0$) return $j+1$;
 if ($j < 0$) return $i+1$;
 if ($s_1[i] == s_2[j]$) return $0 + f(i-1, j-1, s_1, s_2, dp)$;
 else

return $dp[i][j] = 1 + \min$ {
 $f(i-1, j, s_1, s_2, dp)$ // delete
 $f(i, j-1, s_1, s_2, dp)$ // insert
 $f(i-1, j-1, s_1, s_2, dp)$ // update

$TC = N \times M$

tabulation

$n = s1.size()$
 $m = s2.size()$

Base cases

for ($i=0 \rightarrow N$) $dp[i][0] = i$;
 for ($j=0 \rightarrow M$) $dp[0][j] = j$;

for ($i=1 \rightarrow n$) {
 for ($j=1 \rightarrow m$) {
 if ($s1[i-1] == s2[j-1]$) $dp[i][j] = dp[i-1][j-1]$
 else $dp[i][j] = \min$ {
 $dp[i-1][j] + 1$
 $dp[i][j-1] + 1$
 $dp[i-1][j-1] + 1$

	0	1	2	3
0	0	1	2	3
1	1			
2	2			
3	3			
4	4			

Subset sum equals to target.

arr { ⁰1, ¹2, ²3, ³4 }
target = 4

$f(3, 4)$ means on the entire array, index 0 to 3, does there exist someone with a target = 4.

Base cases - $f(\text{index}, \text{target})$

- if (target == 0) return true
- if (index == 0) return true if $a[0] == \text{target}$;

Now explore all possibilities of the index.

bool notTake = $f(\text{index} - 1, \text{target})$
bool take = false

if ($a[\text{index}] \leq \text{target}$)

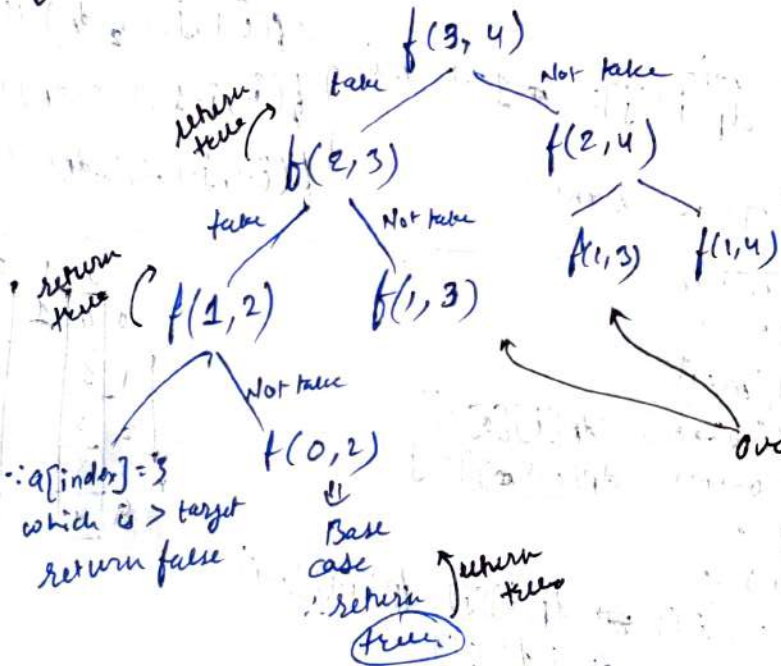
take = $f(\text{index} - 1, \text{target} - a[\text{index}])$

return (take) or (notTake);

If any of the possibilities gives true for the target to exist, return it.

main() { return $f(n-1, \text{target})$;

Eg:- arr = { ⁰2, ¹3, ²1, ³1 } target = 4



overlapping subproblems.

Recursion

• Time comp $\Rightarrow T(n) = 2T(n-1) + c$
 $= O(2^n)$

Then exist
subproblems

• Space comp $\Rightarrow O(N)$

Memorization
can be applied

① Figure out change in states
index and target changes here

② So, DP array = [index] x [target]

• Time comp = $O(N \times \text{target})$

• Space comp = $O(N \times \text{target}) + O(N)$

← Auxiliary space

To reduce
auxiliary
space

↓
Tabulation

① Figure out Base cases -

for $(i=0 \rightarrow n-1)$ $dp[i][0] = \text{true}$;

$dp[0][arr[0]] = \text{true}$;

② Form the nested loops.

index $\rightarrow (1 \text{ to } n-1)$

target $\rightarrow (1 \text{ to target})$

③ Copy the recursion code

		target			
		0	1	2	3
index	0	T			
	1	T			
	2	T			
	3	T			

• T.C = $O(N \times \text{target})$

• S.C = $O(N \times \text{target})$

Memorization code

bool f(index, target, arr, dp) {

Base cases { if (target == 0) return true;
if (index == 0) return (arr[0] == target);

Check dp table ← if (dp[index][target] != -1) return dp[index][target];

bool notTake = f(index-1, target, arr, dp);
bool take = false;

if (arr[index] <= target)

take = f(index-1, target - arr[index], arr, dp)

return dp[index][target] = take || notTake.

{ f(n-1, target, arr, dp); }

Tabulation

~~bool f (index, target)~~

~~Same code as memoization~~

~~bool subsetSumToK (n, target, arr)~~

~~bool subsetSumToK (int n, target k, vector<int> arr)~~

~~vector<vector<bool>> dp (n, vector<bool> (k+1, 0));~~

① Base case { for (int i = 0 → n) dp[i][0] = true;
dp[0][arr[0]] = true;

② for (int index = 1 to n-1) {
for (int target = 1 to → k) {

bool notTake = dp[index-1][target];

bool take = false;

if (arr[index] ≤ target)

take = dp[index-1][target - arr[index]]

dp[index][target] = take || notTake;

return dp[n-1][k];

Reduction

$P = \{ \text{Problems solvable in polynomial time} \}$

$NP = \{ \text{Decision problems solvable in non-deterministic polynomial time} \}$

Answer
is YES
or NO

can guess out of
polynomially many options,
if any guess leads to YES answer
then we get such a guess
in ~~poly time~~ $O(1)$ time.

* 3SAT: Given Boolean formula of the form
 $(x_1 \vee x_3 \vee \bar{x}_6) \wedge (\bar{x}_2 \vee x_3 \vee x_7) \wedge \dots$
clause

Decision Problem: Can you set the variables x_1, x_2, \dots such that formula come out to be True.

↳ This is a hard problem, we don't know polytime algo to solve this.

But we do have a polytime non-deterministic algo.
So, this problem is in NP.

3SAT ∈ NP: guess $x_1 = \text{True or False}$ } All takes poly time.
guess $x_2 = \text{True or False}$

check formula $\begin{cases} \text{If True: set YES} \\ \text{If False: return No.} \end{cases}$

This is a polytime verification algo that check if solⁿ is valid. You can check it in polynomial time.

NP Complete: A problem is NP-complete if

1. $x \in NP$
2. $A \leq_P x$ for some known NP-complete prob. A

x is NP complete if $x \in NP$ and x is NP-hard

x is NP-hard if every problem $y \in NP$ reduces to x .

x is NPC if

Every $y \in NP \leq_P A \leq_P x$
A is a known NP complete

So every y reduces to x .

All NP \rightarrow SAT $\rightarrow x$
 \therefore All NP $\rightarrow x$

Reductions:- From Problem A \rightarrow Problem B

\Rightarrow Polytime algorithm converting A inputs to equivalent B inputs

* NP complete:-

A problem X is NP-complete if:

(1) $X \in NP$

(2) $A \leq_p X$ for some known NP-C Problem A

\Rightarrow To prove B is NP-complete:

- ✓ Reduce known NP-C problem to B
- ✗ Never reduce B to known problem

To Prove B is NP-complete

1. $B \in NP$ (\because A solⁿ can be verified in poly time)

2. Let A be known NP-complete problem

3. We construct a polytime reduction $A \rightarrow B$

4. We show A is satisfiable if and only iff B has a solⁿ

5. Hence B is NP-complete

- Identify known NPC Problem
- State reduction
- Construct mapping
- Proof correctness
- mention poly time

Cook-Levin Theorem:-

SAT is NP-complete, Therefore ^{every} problem in NP can be polynomially reduced to SAT

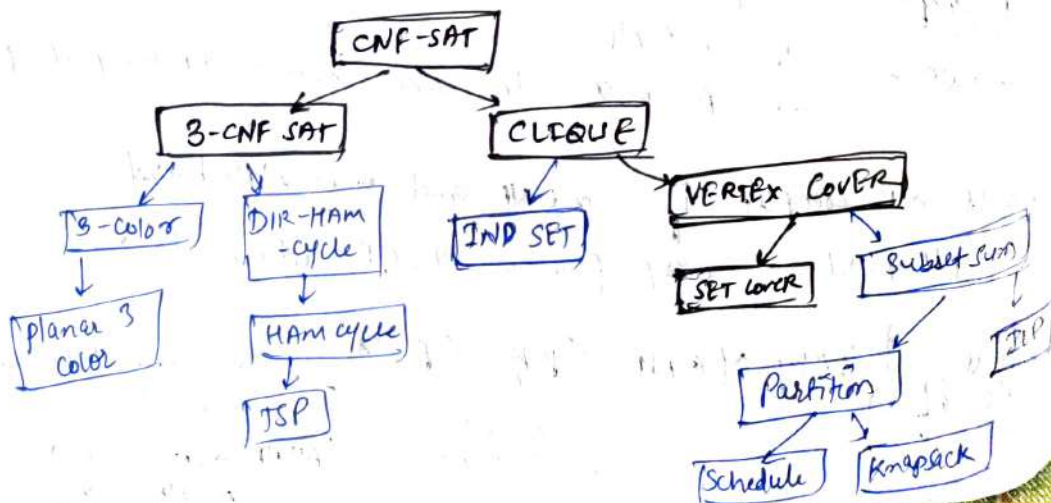
CKT SAT \downarrow SAT \rightarrow 3SAT \rightarrow Clique \rightarrow Indep. set \rightarrow vertex cover \rightarrow set cover

✓ Clique \leftrightarrow Indep. set (complement graph)

• Subset sum \rightarrow partition \rightarrow knapsack

• Ham-cycle \rightarrow TSP

4 main reduction chains



*Note:- To prove X is NP-complete, reduce a known NP-complete problem to X and show the reduction is polynomial and correctness preserving.

For example:-

To prove clique is NP-complete.

1. Verify clique is NP.
2. Reduce 3SAT \rightarrow clique (\because SAT is a known NP-C problem by Cook's theo.)
3. Construct a graph where each literal is a vertex, connect compatible ~~vertex~~ literals, seek k -clique.
4. Satisfiable \Leftrightarrow clique exist

-
- SAT \rightarrow General satisfiability
 - CNF \rightarrow Format (AND of OR's)
 - 3SAT \rightarrow SAT with 3 literals per clause
 - Circuit-SAT \rightarrow Boolean circuit satisfiability
-

Q1) Prove that $\text{Clique} \in \text{NPC}$.

Clique:- Subset of vertices $V' \subseteq V$ such that every two distinct vertex in V' are connected by edge E .

Decision problem

In a graph $G(V, E)$ and integer k , does G contain a clique of size k ?

* Proof for NP:- X : Instance of a graph $G(V, E)$ and k
 Y : Set $V' \subseteq V(G)$

$V(X, Y)$: Given a subset of vertices, V' then

$\left\{ \begin{array}{l} \text{i) If } |V'| \neq k \text{ return False} \\ \text{ii) If } |V'| = k \\ \quad \text{for } u \text{ in } V': \\ \quad \text{for } v \text{ in } V': \\ \quad \quad \text{If } u \neq v \text{ and } (u, v) \notin E \\ \quad \quad \text{return False} \end{array} \right.$ \leftarrow no. of vertex not equal to clique size

else return True

• so verifying clique we need $O(|V|^2) = O(k^2)$ [polynomial] time
 $\therefore \boxed{\text{Clique} \in \text{NP}}$

$\boxed{\text{CKTSAT} \rightarrow \text{SAT} \xrightarrow[\equiv_p]{\text{3CNF SAT}} \text{Indep Set} \leftrightarrow \text{Clique} \rightarrow \text{PCs} \rightarrow \text{ILP}}$

* Proof of NPC.

using Cook's Theorem we know circuit sat $\in \text{NPC}$, so we show $\text{CKTSAT} \leq_p \text{SAT} \leq_p \text{3CNF} \leq_p \text{Clique}$

① • Proof of $\text{CKTSAT} \leq_p \text{SAT}$.

- ① Assign variables x_i for each input of a circuit.
- ② Assign variable x_0 for output
- ③ Set up an if and only if formula for each gate. Let ϕ_k be the formula for k^{th} gate.

④ Let x_0 be final output $\Rightarrow x_0, f = \phi_1 \cdot \phi_2 \cdot \phi_3 \dots$
(CNF formula)

• Reduction for NOT Gate

$$\phi = x_1 \leftrightarrow \bar{x}_2$$

$$= (\bar{x}_1 + \bar{x}_2)(x_1 + x_2)$$

$$= (\bar{x}_1 + \bar{x}_2)(x_1 + x_2)$$



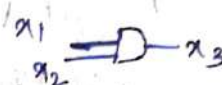
So NOT gate can be reduced to CNF in poly time.

• Reduction for AND Gate

$$\phi = x_3 \leftrightarrow x_1 \cdot x_2$$

$$= (x_3 + \bar{x}_1 \cdot \bar{x}_2)(\bar{x}_3 + x_1 \cdot x_2)$$

$$= (x_3 + \bar{x}_1 + \bar{x}_2)(\bar{x}_3 + x_1)(\bar{x}_3 + x_2)$$



Hence AND Gate can be reduced to CNF in polytime

• Reduction for OR Gate

$$\phi = x_3 \leftrightarrow x_1 + x_2$$

$$= (x_3 + \overline{(x_1 + x_2)})(\bar{x}_3 + x_1 + x_2)$$

$$= (x_3 + \bar{x}_1)(x_3 + \bar{x}_2)(\bar{x}_3 + x_1 + x_2)$$

So OR gate can also be reduced to CNF in polytime

Hence each gate can be reduced to CNF formula ϕ in poly time

$$\boxed{\text{Ckt SAT} \leq_p \text{SAT}}$$

⑤ Proving 3SAT \leq_p Clique

We reduce a known NP-complete problem 3SAT to clique

• Let the input formula be

$$\phi = (x_{11} + x_{12} + x_{13})(x_{21} + x_{22} + x_{23}) \dots (x_{n1} + x_{n2} + x_{n3})$$

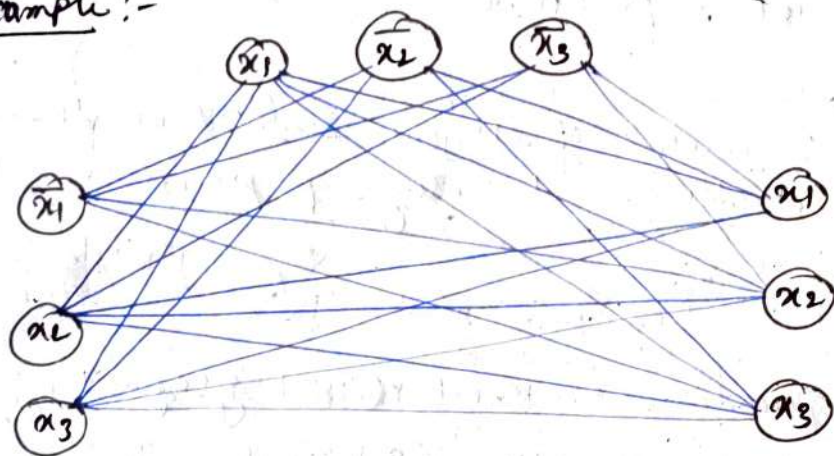
There are n clauses, each clause contains 3 literals.

* Construction of Graph G .

- ① Construct a graph G of k clusters with 3 nodes each
- ② Each cluster corresponds to a clause (each clause contributes 3 vertices)
- ③ For each literal in each clause, create a vertex.

- ④ Add an edge between two vertices if:
1. They come from different clauses, and
 2. add edge except for the pairs of the form (x_i, \bar{x}_i)

Example:-



⑤ Correctness

- If ϕ is satisfiable, then each clause has at least one true literal. Choosing one true literal from each clause gives k vertices, all pairwise compatible.

Hence they must form a clique of size $k(n)$

- If G has a clique of size k , it must select one vertex from each clause (\because no edges exist b/w same clause). These chosen literals cannot contradict, so they give a satisfying assignment for ϕ .

$\therefore \phi \text{ is satisfiable} \iff G \text{ has a clique of size } k.$

The construction is clearly polynomial

$\therefore 3SAT \leq_p \text{Clique}$

So, clique is NP-Hard.

⑥ conclusion

since 1. clique \in NP

2. $3SAT \leq_p \text{Clique}$ and $3SAT$ is NP-complete

clique is NP-complete

* Reductions and Decision Problems -

Q1) Prove that PCS is in class NP (10 marks)

(PCS = circuit SAT)

• You need to only show a solⁿ (input assignment) can be verified in poly time.

• Decision Problem:-

Is there a schedule of all jobs such that they all finish before deadline d using P processors.

• PCS : $V(X, Y)$

X : set of precedence constraints (Task graph T), containing set of jobs $J = \{J_1, J_2, J_3 \dots J_n\}$, No of processors P and Deadline D .

Y : $t_1, t_2 \dots t_n \leftarrow$ Processing time for jobs
 t_i = time at which job i is scheduled.

To show a problem is in NP, we must show
Given a proposed solⁿ, it can be verified in polytime.

Proof:- we are given a certificate (proposed solⁿ) consisting of an ordering of all jobs (schedule)
we verify this certificate in polytime by

① Check Precedence constraints

For every constraint $J_i < J_j$:

- verify that job J_i appears before J_j in schedule
 - This can be done by scanning job order
- takes atmost $O(n^2)$ time

② Check completion time.

• simulate the scheduling by adding processing times in order

- compute total finishing time
- verify that it is $\leq D$

→ This takes $O(n)$ time.

③ Time complexity :- Both checks are polynomial
 \therefore verifier runs in polytime
 $\therefore \boxed{PC8 \in NP}$

*Q) Prove Independent set is in NP.

Decision Problem:- Does G contain Indep. set of size k
 (k vertices with no edges b/w any pair)

Input:-
 • A graph $G(V, E)$
 • An integer k

Proof:- we show that proposed solⁿ can be verified in polytime
 Set of vertices $S = \{v_1, v_2, \dots, v_k\}$ claimed to be indep. set

- ① Check that $|S| = k \rightarrow O(1)$
- ② For every pair (v_i, v_j) with $i \neq j$ check
 $(v_i, v_j) \notin E \rightarrow O(k^2)$

Conclusion:- A certificate can be verified in polytime.

$\therefore \boxed{\text{Indep set} \in NP}$

*Q) Prove that Indep-set is NP-complete.

Step 1:- Indep set $\in NP \leftarrow$ already proved.

Step 2:- Independent set is NP-Hard.

we reduce a known NP-complete prob CLIQUE to Indep-set
 $\text{Clique} \leq_p \text{Indep set}$

* Construction

Instance of Clique:

- Graph $G(V, E)$
- Integer k
- Question: Does G have a clique of size k .

Construct a new Graph

Let \bar{G} be the complement of Graph G

- Same vertices: V
- Edge exist in \bar{G} if and only if it does not exist in G
- keep the same integer k

- Clique in G : Every pair is connected
- Indep. set in \bar{G} : No pair is connected.
- Since edges are flipped, these conditions are equivalent

* correctness of Reduction

$$(G, k) \in \text{CLIQUE} \Leftrightarrow (\bar{G}, k) \in \text{Independent set}$$

So solving Indep. set on \bar{G} solves Clique on G

* Poly time :-

Constructing complement graph takes $O(|V|^2)$ time

* conclusion:-

1. Independent set $\in \text{NP}$
2. $\text{Clique} \leq_p \text{Indep. set}$

Hence Indep. set is NP-C

* Q) Prove that ~~Indep~~ vertex cover is NP-complete.

$$(\text{Indep set} \leq_p \text{vertex cover})$$

known
decision
problem

Theorem :- Vertex cover decision problem is NP-complete

step ① :- Vertex cover belongs to NP

Decisⁿ prob:- Does G have a vertex cover of size $\leq k$?
(A set of vertices that touches every edge)

Input:-

- Graph $G = (V, E)$
- Integer k

Certificate:- A set $C \subseteq V$ with $|C| \leq k$

verificatⁿ (polytime) :- For every edge $(u, v) \in E$: check
 $u \in C$ or $v \in C$

This takes $O(E)$ time \leftarrow polynomial

Hence $\boxed{\text{vertex cover} \in NP}$

Step ②:- vertex cover is NP-Hard

we reduce Indep set (known decⁿ problem) to vertex cover

Construction:-

$\boxed{\text{Indep set} \leq_p \text{vertex cover}}$

Instance of indep set

- Graph $G = (V, E)$, Integer k

Construct instance of vertex cover

- use the same graph G
- set new integer $k' = |V| - k$

Key Lemma:- A set S is ^{an} independent set in G if and only iff $V \setminus S$ is a vertex cover in G

Proof:- If S is indep set, no two vertices in S share an edge.

\therefore Every edge must have atleast one endpoint outside S
so all edges are covered by $V \setminus S$

- conversely if C is a vertex cover, then no edge has both endpoints outside C . So $V \setminus C$ contains no adjacent vertices \rightarrow Independent set

Correctness:-

$(G, k) \in \text{Indep set} \iff (G, |V| - k) \in \text{vertex cover}$

Thus solving vertex cover solves indep set.

The transformation is clearly polytime

Conclusion:-

- vertex cover $\in NP$
- Indep set \leq_p vertex cover

$\therefore \boxed{\text{Vertex cover is NP-complete}}$

Que 2) Prove that Formula satisfiability \leq_p 3-CNF satisfiability and show SAT is NPC.

PART A:-

Formula SAT \leq_p 3-CNF SAT
(i.e. SAT \leq_p 3SAT)

Given:-

SAT = satisfiability of any Boolean.

3SAT = satisfiability of Boolean formula in 3CNF form
(each clause 3 literals)

we must show that SAT instance can be converted into 3SAT in poly time

Step 1:- Convert SAT to CNF

Any Boolean formula can be transformed into Conjunctive Normal form (CNF) using logical equivalences

- De Morgan's law
- Distributive law

This transformation takes poly time

So:- $\boxed{\text{SAT} \leq_p \text{CNF-SAT}}$

Step 2:- Convert CNF to 3-CNF

• If a clause has more than 3 literals

eg:- $(x_1 + x_2 + x_3 + x_4)$

replace by:- $(x_1 + x_2 + y_1) \wedge (y_1 + x_3 + x_4)$

• If a clause has less than 3 literals

eg:- $(x_1 + x_2)$

replace by:- $(x_1 + x_2 + z) \wedge (x_1 + x_2 + \neg z)$

Hence

$\boxed{\text{CNF-SAT} \leq_p \text{3SAT}}$

Part B :- Prove that SAT is NP-complete

Step 1 :- SAT \in NP.

Given a truth assignment for variables, we can verify whether the formula is true by evaluating it in polytime.

Hence :- SAT \in NP

Step 2 :- SAT is NP-Hard.

By Cooks Theorem, every problems in NP can be reduced to SAT in polytime. Since SAT and circuit SAT are polynomial-time equivalent, every NP problem is also reducible to circuit SAT.

Hence :- SAT is NP-Hard

conclusion :-

1. Since SAT \in NP
2. SAT is NP-Hard
3. SAT \leq_P 3SAT

we conclude

SAT is NP-complete and SAT \leq_P 3SAT

Que 4) Prove that Independent set \leq_P Circuit SAT and IS is NP complete

Soln * IS - Decision Problem

• Input :- Graph $G = (V, E)$, integer k

• Quest :- Does G contains indep. set of size atleast k ?
(no two vertices in the set are adjacent)

* Circuit satisfiability (Circuit-SAT)

• Input :- Boolean circuit C with some input variables

• Question :- Is there an assignment to the inputs such that the output of C is 1.

we must build, in polytime, a circuit $C_{G,k}$ that is satisfiable iff G has an indep set of size atleast k .

* Constructⁿ of circuit

Claim:- If circuit outputs to 1, then S forms an indep. set

Proof:- ① Variable Assignment

For each vertex $v_i \in V$ creates a boolean variable x_i such that
 $x_i = 1$ if v_i ~~part~~ ^{is} part of indep. set
 $x_i = 0$ if v_i is not part of indep. set

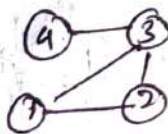
② Non Adjacency Constraint

For an edge $(v_i, v_j) \in E$ we need a constraint that includes only one of v_i, v_j . That is both can't come at a time

\therefore clause is $\neg(x_i \wedge x_j) \Rightarrow (\bar{x}_i \vee \bar{x}_j)$ [De Morgan's law]

These are ~~and~~ AND and OR operat^{ns} which can be implemented by logic gates

eg:-



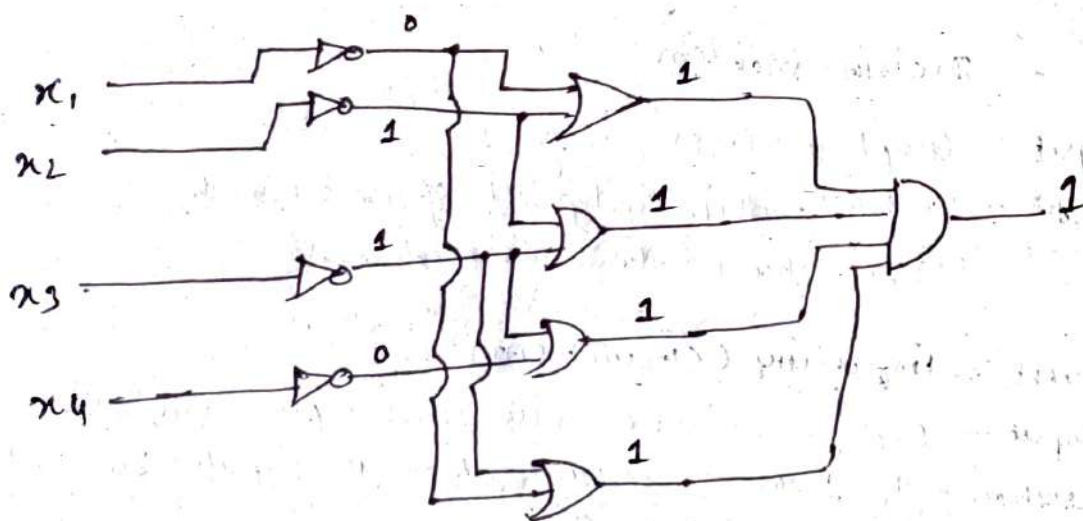
$k=2$, $G = \{v(1,2,3,4)$

$E[(1,2) (1,3) (1,2) (4,3)]\}$

Ind. set $\rightarrow S = \{1,4\}$

Variable assignment \rightarrow

x_1	x_2	x_3	x_4
1	0	0	1

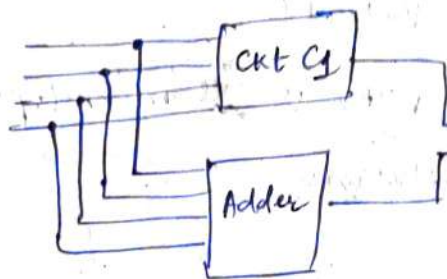


Circuit C_1

Above is logical circuit handling non adjacency vertices in the given subseq of graph

③ Size constraint.
 $|S| \geq k$ needs to be ensured in indep. set using adder circuit.

Such circuit can be built in polytime using adders



The output is a single boolean variable that determines whether indep set is valid.

The o/p of gate is satisfied if

- (i) Indep set is of size k
- (ii) No two adjacent vertices are part of Indep set.

Thus if circuit is satisfiable, this satisfying assignment corresponds to independent set

and mapping $(G, k) \leftrightarrow$ circuit is computable in polytime

Independent set \leq_p Circuit-SAT

Ques) Show that Max^m Independent set is polytime reducible to DLP.

Proof:- Max^m Independent set (MIS)

I/P: An undirected graph $G = (V, E)$

O/P: Largest subset $V' \subseteq V$ such that no two vertices in V' are adjacent i.e. $(u, v) \in V'$ but $(u, v) \notin E$

Integer linear programming (ILP):

I/P: A set of linear inequalities and an objective fn with variables i.e. A, B and objective fn

O/P: The values of the variables that maximize or minimize the objective fn while satisfying constraints i.e. x

such that $Ax \leq b$

* Reduction from MIS to ILP.

→ To represent MIS as ILP, we can use binary variables to model the selection of vertices in the independent set.

Step ① → Let's define binary variable

$$x_i = \begin{cases} 1 & \text{if vertex } v_i \text{ is in indep set} \\ 0 & \text{otherwise} \end{cases}$$

Step ② :- Objective f^n

To maximize the no. of vertices in Indep. set:

$$\text{maximize } Z = \sum_{i=1}^{|V|} x_i$$

Step ③ :- Constraints (No two adjacent vertices)

For every edge $(v_i, v_j) \in E$, we cannot select both vertices simultaneously, so add constraint

$$x_i + x_j \leq 1$$

$$x_i, x_j \in \{0, 1\} \quad \forall (i, j) \in E$$

The above constraint guarantees that if $x_i = 1$ (vertex i is in P_i) then $x_j = 0$ and vice versa, thus ensuring no two adjacent vertices are both selected.

* Polytime reduction

→ To convert an instance of MIS to ILP, we simply need to

- ① create a variables x_i for each vertex $i \Rightarrow O(V)$
- ② set up objective $f^n \sum_{i \in V} x_i \Rightarrow O(V)$
- ③ for each edge $(i, j) \in E$ add constraint $x_i + x_j \leq 1 \Rightarrow O(E)$

$$\therefore \text{overall time}_{\text{comp}} = O(V + V + E) \\ = O(V + E) = \text{polytime}$$

$$\therefore \boxed{\text{MIS} \leq_p \text{ILP}}$$