Santhosh Ramaiah

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OBJECTIVE

To be a part of an organization which provides a dynamic work environment to enhance my professional skills and use the skills to further the organization progress.

EDUCATION

Louisiana State University A&M College, Baton Rouge, LA – USA.   *JAN 2013 – DEC 2014*
- Master of Science in Electrical Engineering. GPA: 3.5

SJB Institute of Technology, Bangalore, KA – India.   *AUG 2008 – JUL 2012*
- Bachelor of Engineering in Electronics and Communication Engineering. GPA: 3.7

EXPERIENCE

Louisiana Transportation Research Center, Baton Rouge, Louisiana   *APR 2013- Present*
Hardware and Software Designer (Embedded/Audio/Video), IT Department.*
*Sponsored for Master's program as Graduate Research Assistant at LSU.

- Programming in Crestron Control System (Simple +/RTOS/Motorola Coldfire Processor).
- Developing Device Drivers for hardware to interface with Crestron Control System.
- Configuring Video Conferencing Equipment (Cisco C40, Polycom VSX8000, MCU).
- Computer and Network Administration.

PROJECTS

- Design of Robotic System for Aiding Partially Paralyzed Patient (FPGA/LabView).
- Gesture recognition system to control Robotic System (LPC/PIC Microcontroller/Zigbee).
- Arduino Controlled Robot system (Arduino/RF).
- Simulation of Carbon Nanotubes and its behavior (CUDA/OpenGL/NVidia GPU).
- Image Compression Using SPIHT Algorithm (MATLAB).
- Implementation of OFDM system (MATLAB).
- Implementation of TCP/UDP using RED, DropTail, and SFQ algorithms in Network Simulator and Chat process between Multiple Clients and Server using Python.

TECHNICAL PAPER

- **Content Based Image Retrieval**. 3rd NATIONAL CONF. AISE- 2010, October 28, 2010.

SKILLS

Programming	: C/C++, VHDL, Verilog, Python, Assembly Level Programming, OpenGL, CUDA, Bash.
Software/IDE	: MATLAB/Simulink, LabView, Mathematica, Pspice, Electronic Workbench, OpenCV, Eclipse IDE, Crestron VtPro-e, Crestron Simple/+, Keil uVision, MPLAB IDE.
Web & Networking	: HTML, CSS, PHP, JavaScript, WordPress, Tcl/Tk, Network Simulator 2, Cisco Packet Tracer.
Hardware	: Micro-Controller 8051, Microprocessor (8086, x86 & x86-64), PIC & ARM Microcontroller, Spartan 3 FPGA, NI Compact RIO, NVidia GPU, Motorola Coldfire processor, Arduino Uno.
Operating systems	: LINUX (Ubuntu, Fedora), FreeRTOS, Windows.