

Evolution of CSMA Protocols for the IEEE 802.11 Standard

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Abstract—In this paper we present the requirements of candidate protocols to replace the pervasive CSMA/CA medium access control. We discuss the possibility of further preventing collisions and provide an overview of the related work. We specify protocols that are candidates of replacing CSMA/CA in pseudocode and use simulation to assess performance metrics such as throughput, fairness and collision probability.

I. INTRODUCTION

This demo file is intended to serve as a “starter file” for IEEE conference papers produced under L^AT_EX using IEEEtran.cls version 1.7 and later. I wish you the best of success.

A candidate to replace CSMA/CA should

- Provide performance advantages, either in the form of throughput or short term fairness.
- Be backward compatible with current implementation.
- Be simple a simple evolution implementation to ease the transition and reduce time to market (Optional but desirable).

II. RELATED WORK

Since the popularization of IEEE 802.11, several papers have proposed modifications to the contention protocol that is used for sharing the medium. They can be categorized in three groups regarding the approach they use. The first one prevents that the contention window is reset to its minimum value after successful transmissions. Examples of this first group include [1], [2]. This solution improves throughput in saturation conditions at the price of lowering short term fairness.

The second groups involves the accurate estimation of the number of contenders to adjust the contention parameters. Two examples of this group are [3], [4]. This approach offers some throughput and fairness gains at the expense of increased implementation complexity. As the number of contenders is estimated relying on the number of collisions, the presence of channel errors further complicates the estimation. Furthermore, there is a fundamental trade-off between the accuracy and the reaction time of the estimation.

The aforementioned solutions are not able to fairly share the medium with legacy devices. In fact, these proposals are, generally speaking, less aggressive than the currently

implemented protocol. Consequently, in a hypothetical mixed network in which the new and old protocols coexist, the new stations would receive a smaller share of the available bandwidth in a scenario.

A more important limitation of the solutions exposed so far is that the throughput is bounded by that of CSMA/CA with optimal configuration [3], [5]. In the present paper we focus on a third group of solutions that delivers throughput above the maximum attainable by CSMA/CA.

This third group of solutions uses a deterministic backoff after successful transmissions to further reduce the chances of collisions. Under certain conditions, collision-free operation is reached. It was introduced in [6] and a more detailed analysis that includes both saturated and non-saturated conditions is presented in [7]. A more in-depth study is carried out in [8], including realistic elements such as clear channel assessment errors. Different aspects of fairness are addressed in [8]–[10]. The performance in realistic channels taking into account the Auto Rate Fallback mechanism is evaluated [11], [12].

Even though initial research efforts were focused on the WLAN collision problem, more recent papers try to extend the idea to multi-hop networks. In [13], the multi-hop slotted case explored. The more realistic situation in multi-hop networks in which the time is not slotted is studied in [14].

The same principles that we exploit to prevent collisions in WLANs can be used in other areas of radio resource management in wireless area networks [15]–[18].

In all the previous work on collision-free operation in WLANs mentioned so far, there is the limitation that the number of contenders should not exceed the value of the deterministic backoff used after successful transmissions. If this value is exceeded, it is no longer possible to achieve collision-free operation. A first solution to solve this problem is presented in [19], but it requires the presence of a central entity (typically the access point) that instructs the other nodes to adjust the value of their deterministic backoff.

In the present paper, we study a completely distributed solution to accommodate a large number of contenders in a fair collision-free fashion.

```

1  $b \leftarrow \mathcal{U}[0, CW_{\min} - 1]$ ;
2 while there is a packet to transmit do
3    $a \leftarrow 0$  ;
4   while  $a < A$  do
5     while  $b > 0$  do
6       wait 1 slot ;
7        $b \leftarrow b - 1$  ;
8     end
9     Attempt transmission ;
10    if success then
11      /* Random backoff.
12       $b \leftarrow \mathcal{U}[0, CW_{\min} - 1]$ ;
13      break ;
14    else
15      /* Random backoff.
16       $a \leftarrow a + 1$  ;
17       $b \leftarrow \mathcal{U}[0, CW_{\min} 2^{\min(a,m)} - 1]$ ;
18    end
19  end
20 end

```

Algorithm 1: CSMA/CA

```

1  $b \leftarrow \mathcal{U}[0, CW_{\min} - 1]$ ;
2 while there is a packet to transmit do
3    $a \leftarrow 0$  ;
4   while  $a < A$  do
5     while  $b > 0$  do
6       wait 1 slot ;
7        $b \leftarrow b - 1$  ;
8     end
9     Attempt transmission ;
10    if success then
11      /* Deterministic backoff.
12       $b \leftarrow CW_{\min}/2$ ;
13      break ;
14    else
15       $a \leftarrow a + 1$  ;
16      /* fall to random backoff.
17       $b \leftarrow \mathcal{U}[0, CW_{\min} 2^{\min(a,m)} - 1]$ ;
18    end
19  end
20 end

```

Algorithm 2: CSMA/ECA

III. ENHANCED CSMA

IV. PERFORMANCE EVALUATION

V. CONCLUSION

The conclusion goes here.

ACKNOWLEDGMENT

The authors would like to thank...

```

1  $b \leftarrow \mathcal{U}[0, CW_{\min} - 1]$ ;
2 /* Hysteresis: The backoff stage is
3    reset only when a node joins the
4    contention or the queue is empty. */
5  $a \leftarrow 0$  ;
6 while there is a packet to transmit do
7   while  $a < A$  do
8     while  $b > 0$  do
9       wait 1 slot ;
10       $b \leftarrow b - 1$  ;
11    end
12    Attempt transmission ;
13    if success then
14      /* Deterministic backoff.
15       $b \leftarrow CW_{\min}/2$ ;
16      break ;
17    else
18       $a \leftarrow a + 1$  ;
19      /* fall to random backoff.
20       $b \leftarrow \mathcal{U}[0, CW_{\min} 2^{\min(a,m)} - 1]$ ;
21    end
22  end
23 end

```

Algorithm 3: CSMA/ECA with hysteresis

```

1  $b \leftarrow \mathcal{U}[0, CW_{\min} - 1]$ ;
2 /* Hysteresis: The backoff stage is
3    reset only when a node joins the
4    contention or the queue is empty. */
5  $a \leftarrow 0$  ;
6 while there is a packet to transmit do
7   while  $a < A$  do
8     while  $b > 0$  do
9       wait 1 slot ;
10       $b \leftarrow b - 1$  ;
11    end
12    /* Fair-share:  $2^a$  packets are
13    transmitted.
14    Attempt aggregate transmission of  $2^{\min(a,m)}$ 
15    packets;
16    if success then
17      /* Deterministic backoff.
18       $b \leftarrow CW_{\min}/2$ ;
19      break ;
20    else
21       $a \leftarrow a + 1$  ;
22      /* fall to random backoff.
23       $b \leftarrow \mathcal{U}[0, CW_{\min} 2^{\min(a,m)} - 1]$ ;
24    end
25  end
26 end

```

Algorithm 4: CSMA/ECA with hysteresis and fair-share

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