# Future Evolution of CSMA Protocols for the IEEE 802.11 Standard

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Abstract—In this paper we present the requirements of candidate protocols to replace the prevalent CSMA/CA medium access control. We discuss the possibility of further preventing collisions and provide an overview of the related work. We specify a protocol that is a candidate for replacing CSMA/CA in pseudocode and use simulation to assess performance metrics such as throughput, fairness and collision probability.

### I. INTRODUCTION

Since the popularization of the IEEE 802.11 standard, several works [1]–[11] (missing any?) have proposed modifications to CSMA/CA (Carrier Sense Multiple Access with Collision Avoidance), the contention protocol used in the standard for sharing the medium. Despite the throughput improvement offered by all these works, none of the proposed modifications have yet been adapted by the standard.

It is essential for a candidate CSMA/CA replacement to

- provide performance advantages in terms of throughput and/or short term fairness;
- be backward compatible with the current implementation;
- support a large number of simultaneous contenders; and
- be a simple evolution, in terms of implementation, to ease the transition and reduce the time to market (optional but desirable).

The aforementioned works can be categorized in three groups regarding the approach they use. In this paper, we will focus on the last of these three groups, proposing an additional enhancement to make it more adaptable to network size variations, and make the case for its adaptation by the standard.

In the first group, represented by [1], [2], a throughput increase is obtained in saturation conditions by preventing the contention window from resetting to its minimum value after each successful transmission. However this performance gain comes at the expense of reduced short term fairness, since nodes that have recently gone through a sequence of consecutive collisions are forced to stay at a higher back-off stage and thus are further penalized by less frequent transmissions.

In the second group of works, exemplified here by [3], [4], an accurate estimate of the number of contenders is used to adjust the contention parameters. This approach offers some throughput and fairness gains, but at the expense of increased implementation complexity. In addition, as the number of

contenders is estimated relying on the number of collisions, the presence of channel errors further complicates the estimation [Complicates, or renders it inaccurate? Is there a workaround offered?]. Furthermore, there is a fundamental tradeoff between the stability and the speed of reaction of the estimation.

When it comes to backward compatibility, neither of the aforementioned solutions are able to fairly share the medium with legacy devices. In fact, these proposals are, generally speaking, less aggressive than the currently implemented CSMA/CA protocol. Consequently, in a hypothetical mixed network in which the new and old protocols coexist, the new stations would receive a smaller share of the available bandwidth.

A more important limitation of the two approaches exposed so far is that the throughput is bounded by that of CSMA/CA with optimal configuration [3], [12].

The third group of solutions, which is the focus of the current paper, can deliver a throughput above the maximum attainable by CSMA/CA. This performance boost is achieved mainly by the use of deterministic backoff after successful transmissions, which reduces the chances of collisions for nodes that were successful in their previous transmission. Furthermore, under certain conditions, a collision-free operation can be reached. We will refer hereafter to the class of algorithms that use deterministic backoffs after successful transmissions as CSMA with Enhanced Collision Avoidance (CSMA/ECA). This approach was first introduced in [5] (Does this contain analysis?), and later a more detailed analysis for both saturated and non-saturated conditions was presented in [6]. A more in-depth study is carried out in [7], including realistic elements such as the possibility of Clear Channel Assessment errors. Different aspects of fairness are addressed in [7]-[9]. The performance in realistic channels, taking into account the Auto Rate Fallback mechanism, is evaluated in [13], [14].

Even though initial research efforts where focused on the WLAN collision problem, more recent papers try to extend the idea to multi-hop networks. In [10], the multi-hop slotted case explored. The more realistic situation in multi-hop networks in which the time is not slotted is studied in [15].

The same principles that we exploit to prevent collisions in WLANs can be used in other areas of radio resource management in wireless area networks [16]–[19].

In all the previous work on collision-free operation in WLANs mentioned so far, there is the limitation that the number of contenders should not exceed the value of the deterministic backoff used after successful transmissions. If this value is exceeded, it is no longer possible to achieve collision-free operation. A first solution to solve this problem is presented in [11], but it requires the presence of a central entity (typically the access point) that instructs the other nodes to adjust the value of their deterministic backoff.

In the current paper, we present a completely distributed solution, called *backoff stage hysteresis* or simply hysteresis, to accommodate a large number of contenders in a fair collision-free fashion. We furthermore detail pseudo-code algorithms for three different types of CSMA/ECA and highlight how they can be easily implemented using simple modifications to the currently-used CSMA/CA algorithm. We will then present some performance evaluation, quantifying the performance gain that these three types of CSMA/ECA can achieve, both in terms of throughput and fairness.

-comment about backward compatibility and other items in the candidate requirements.

### II. ENHANCED CSMA/CA

```
1 while the device is on do
         r \leftarrow 0 ; s \leftarrow 0 ;
         b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
 3
         while there is a packet to transmit do
 4
 5
               repeat
                    while b > 0 do
 6
                         wait 1 slot;
 7
                         b \leftarrow b - 1;
 8
                    Attempt transmission of 1 packet;
                   if collision then
10
11
                         r \leftarrow r + 1;
                         s \leftarrow \min(s+1,S);
12
                         b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
13
               until (r = R) or (success);
14
              if success then
15
                   r \leftarrow 0:
16
                   s \leftarrow 0;
17
                   b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
18
19
                   Discard packet;
20
                    r \leftarrow 0;
21
                    s \leftarrow 0;
22
                   b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\min} - 1];
23
         Wait until there is a packet to transmit;
                         Algorithm 1: CSMA/CA
```

In this section we describe the CSMA/CA protocol as it is currently implemented in the IEEE 802.11 and an evolution of the standard that satisfies the four requirements specified in the introduction. Therefore, the presented protocol is a valid

```
r \leftarrow 0; s \leftarrow 0;
 2
         b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\min} - 1];
 3
 4
         while there is a packet to transmit do
              repeat
 5
                   while b > 0 do
 6
                        wait 1 slot;
 7
                        b \leftarrow b - 1;
                   Attempt transmission of 1 packet;
 9
                   if collision then
                         r \leftarrow r + 1;
11
                         s \leftarrow \min(s+1, S);
                        b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
              until (r = R) or (success);
14
              if success then
15
                   r \leftarrow 0;
16
17
                   s \leftarrow 0;
                   b \leftarrow (2^s \text{CW}_{\text{min}})/2;
18
19
              else
                   Discard packet;
20
                   r \leftarrow 0:
21
                   s \leftarrow 0:
22
                   b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
23
         Wait until there is a packet to transmit;
24
                       Algorithm 2: CSMA/ECA
 1 while the device is on do
         r \leftarrow 0; s \leftarrow 0;
         b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
 4
         while there is a packet to transmit do
              repeat
 5
                   while b>0 do
 6
                        wait 1 slot;
 7
                       b \leftarrow b - 1;
 8
 9
                   Attempt transmission of 1 packet;
                   if collision then
10
                        r \leftarrow r + 1:
11
                        s \leftarrow \min(s+1, S);
12
                        b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
13
              until (r = R) or (success);
14
              if success then
15
                   r \leftarrow 0;
16
                   b \leftarrow (2^s \text{CW}_{\text{min}})/2;
17
18
              else
19
                   Discard packet;
20
                   r \leftarrow 0;
```

1 while the device is on do

**Algorithm 3**: CSMA/ECA with hysteresis

 $b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];$ 

Wait until there is a packet to transmit;

21

```
1 while the device is on do
         r \leftarrow 0 : s \leftarrow 0:
 2
         b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
 3
         while there is a packet to transmit do
 4
 5
               repeat
                    while b > 0 do
 6
                         wait 1 slot;
                         b \leftarrow b - 1;
 8
                    Attempt transmission of 2^s packets;
                    if collision then
10
                         r \leftarrow r + 1;
11
                         s \leftarrow \min(s+1,S);
12
                         b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
13
               until (r = R) or (success);
14
              if success then
15
                   r \leftarrow 0;
16
                    b \leftarrow (2^s \text{CW}_{\text{min}})/2;
17
               else
18
                    Discard packet;
19
20
                    r \leftarrow 0;
                    b \leftarrow \mathcal{U}[0, 2^s \text{CW}_{\text{min}} - 1];
21
         Wait until there is a packet to transmit;
22
```

Algorithm 4: CSMA/ECA with hysteresis and fair-share

candidate to replace CSMA/CA in the upcoming revisions of the standard.

There are three changes in the new protocol compared to the legacy one. Firstly, a deterministic backoff is used after successful transmissions. Secondly, the backoff stage is not reset after a packet is serviced. The backoff stage is reset only when the station leaves the contention because it has no packet to be transmitted. And thirdly, the number of packets transmitted in every transmission attempt is a function of the backoff stage. Note that current standards already support the transmission of multiple packets in a single slot.

In the following we describe the original protocol and then we describe each of the modifications that we propose, one by one.

Algorithm 1 describes the CSMA/CA algorithm that is used in current networks. When a station joins the contention it initializes the retry attempt counter r and the backoff stage s to zero. The backoff counter b is initialized using a uniform random distribution and a contention window. After each collision, the retry attempt counter and the backoff stage counter are incremented. As a consequence of the incremented backoff stage, a larger contention window is used. Note that there is maximum backoff stage S and a maximum retry limit S. When the maximum retry limit is reached the packet is discarded, S and S are reset and a new value for S is computed. Similarly, if the packet is successfully transmitted, the S and S counters are reset and a new random backoff counter is computed.

Algorithm 2 describes CSMA/ECA in which a determin-

istic backoff is used after successful transmission. The only change with respect to CSMA/CA is in line 14, where the deterministic assignment takes place.

Hysteresis is included in 3. Adding hysteresis is as simple as removing lines 18 and 23 from Algorithm 2. Note that when hysteresis is used, the backoff stage is reset only when the node has no packet to serve.

Finally, fair-share is implemented in 4. Line 10 of 3 is modified to increase the number of transmitted packets from one to  $2^s$ .

### III. PERFORMANCE EVALUATION

In this section, the performance of the different CSMA protocols is evaluated through simulations. The presented results show the saturation throughput and fairness (Jain's Fairness Index (JFI) [20]) when the number of contenders increases. Additionally, the evolution of the probabilities that one slot is empty, contains a successful transmission or a collision, are plotted to provide further insights on the operation of each protocol.

To evaluate the four protocols, a network of N nodes is considered, where each node is within the coverage area of the others. The nodes are set to be under saturation (always have packets to transmit). The number of contenders, N, ranges from 2 to 50, and one thousand instances of the simulation are performed for each N. All plotted results show the 95% confidence intervals.

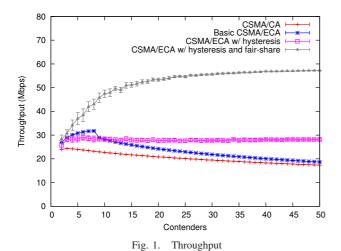
A simulator of such scenario has been built, from scratch, using the C++ language and based on the COST (Component Oriented Simulation Toolkit) libraries [21]. The specific parameters of the IEEE 802.11n amendment [22] are considered, and listed on Table I. In case packets are aggregated, default A-MPDU aggregation is considered. Further MAC-related parameters, as well as the code for the four CSMA protocols, are available online [23].

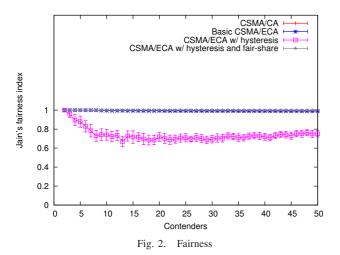
Parameter	Value
$\overline{CW_{\min}}$	16 slots
S	5 stages
SIFS, DIFS	9 $\mu s$ , 34 $\mu s$
$\sigma$	$16 \mu s$
$T_{ m PHY}$	$32~\mu s$
MAC header length (MH)	288 bits
Service Field length (SF)	16 bits
MPDU Delimiter length (MD)	32 bits
Block ACK length ( $L_{\text{BACK}}$ )	256 bits
Data Length $L_{\mathrm{DATA}}$	12000 bits
Tail Bits (TB)	6 bits
Data Bits Per Symbol $L_{DBPS}$	260 bits/OFDM symbol
OFDM Symbol Duration $T_{\rm s}$	$4 \mu s$

 $\label{table I} \textbf{TABLE I}$  Parameters considered for the performance evaluation

In Figure III, the throughput of all four schemes is plotted. As can be observed, the throughput for CSMA/CA decreases

with the number of nodes, as the binary exponential backoff reduces the number of transmission attempts to keep the collisions low. The basic CSMA/ECA is able to achieve the collision-free operation if the number of nodes is lower than the deterministic backoff value, i.e.,  $N \leq \mathrm{CW_{min}}/2$ . This is why in this figure, a phase transition is observed at N=8. When N is larger than  $\mathrm{CW_{min}}/2=8$ , although the collision-free operation is not possible, the throughput obtained remains higher than in CSMA/CA. This is because, regardless of whether or not the collision-free operation is reached, in CSMA/ECA, a node always has a lower collision probability immediately after a successful transmission, without affecting the collision probability of other nodes.





Clearly, reaching a collision-free operation is highly desirable, but not always possible in the basic CSMA/ECA. CSMA/ECA with Hysteresis exactly addresses this issue by simply not resetting the backoff stage after a successful transmission. In other words, the deterministic backoff value  $(2^s \text{CW}_{\min}/2)$  is chosen based on the backoff stage, s, in which the transmission was successful. This way, a larger number of contenders can reach a collision-free operation (Figure III),

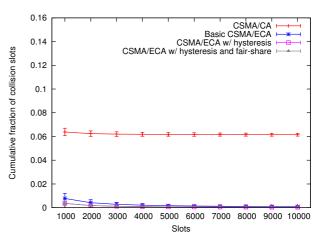


Fig. 3. Cumulative fraction of slots spent in collision for N=6 nodes

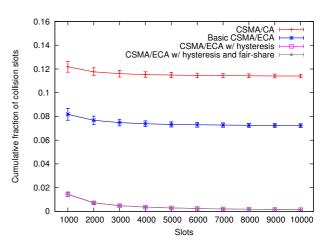


Fig. 4. Cumulative fraction of slots spent in collision for N=12 nodes

but at the expense of a lower throughput for  $N \leq 8$  compared to the basic CSMA/ECA. This is because if collision-free operation can be reached with a lower deterministic backoff value, there will be fewer empty slots, hence higher channel access efficiency. Furthermore, CSMA/ECA with hysteresis has a lower long-term fairness (Figure III) compared to the basic CSMA/ECA or even the legacy CSMA/CA protocol, as once the network has reached the collision-free state, nodes that use a large backoff stage will have fewer transmission opportunities than others.

To address the aforementioned fairness issue when hysteresis is used, CSMA/ECA with hysteresis and fair-share was introduced. It achieves greater throughput than CSMA/ECA with hysteresis only, while being fair for any number of contenders. This is achieved by using packet aggregation, transmitting  $2^s$  packets per attempt. As observed in Figure III, using aggregation we are not only able to provide a fair access, but also we are able to significantly increase the throughput. Note that the throughput increases with the number of nodes because for larger N, the collision-free operation is reached at higher backoff stages, which means more packets will be aggregated and transmitted at every

## attempt, thus improving the efficiency of the network in terms of channel use.

In Figures III and III, the temporal (in number of slots) evolution of the cumulative fraction of slots spent in collision is shown for N=6 and 12 nodes. As expected, for N=6, with all CSMA/ECA variants, collision-free operation is achieved (since N<8) and the probability that a slot contains a collision tends rapidly to zero. The collision-free operation is reached faster when hysteresis is used. When N=12, only the protocols using hysteresis reach collision-free operation.

### IV. CONCLUSION

The conclusion goes here.

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