

Technical report: WMP-Editor

Luis Sanabria-Russo

Abstract—This report covers the technical aspects of the implementation of CSMA/ECA in Wireless MAC Processors (WMP). Upon each iteration, the abstract will change and its sections will probably lengthen and increase in number. This first version covers the aspects related to WMP-Editor layout and parameters. Also, the last section is composed of the current highlights on the research.

Index Terms—WMP, MAC, Collision-free, CSMA/ECA.

I. INTRODUCTION

As was mentioned on the *Future Directions* section in [1], one of the goals in the roadmap is to implement CSMA/ECA in cheap commodity hardware.

This report gathers all the necessary software, equipment and procedures that led to the translation of CSMA/ECA into WMP-Editor [2] [3]. It is intended to anyone who is interested on playing with MAC protocols in real hardware.

II. FLYING THROUGH WMP'S CONCEPTS

On the other hand, this report is not written as a survey or an explanation on the subject of Finite State Machines (FSM); and neither details the work of FSM as an abstraction level capable of modifying the default behavior of wireless network cards. Instead the reader will be introduced to the terms used in the documentation as well as how they are appreciated through the graphical interfaces of the different applications used along.

A. Directing the behavior

The WMP allows us to *program* different MAC protocols through a user interface (UI). This interface is what will be called the WMP-Editor from now on. In the WMP-Editor you can build blocks and connect them to other blocks. Some of these blocks can be conditional blocks and redirect the instruction flow according to an evaluation. In turn, every connection between blocks can execute an *action* that can admit *parameters*.

In the WMP-Editor, blocks are *states* of the MAC program waiting for some trigger to unleash further actions and redirect the flow towards another state. Figure 1 shows an overview of the workspace in WMP-Editor.

When following the flow of an example state machine (like the one in Figure 1), it is easy to see how arrows represent *state changes*. These are the ones capable of executing actions during the state transitions. Figure 2, shows how you can edit the parameter of action 0x0D TX_PKT_SCHEDULER by using the drop-down menu on the left side (setting it to BK_SLOT=16).

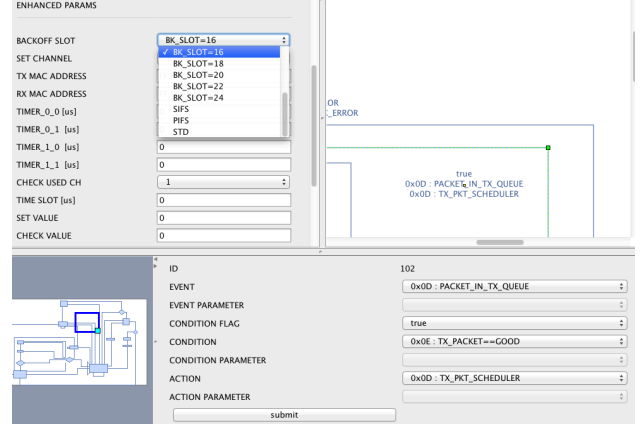


Fig. 2. Changing the parameter of action 0x0D TX_PKT_SCHEDULER

III. IMPLEMENTATION

It is possible to find everything that is needed to run WMP-Editor at the project's Github repository [3]. But first, it is worthy to mention the exceptions related to hardware.

A. Hardware compatibility issues

As of the time of this report, it is necessary to have a wireless card with the Broadcom chipset 4311v1 or 4318. This is not an obstacle for testing WMP-Editor, but in order to test the work it is imperative to have a compliant card. Instructions on how to flash the firmware and other compatibility tasks that must be performed, can be found in the documentation folder of [3] (Chapter 8).

B. Running the WMP-Editor

WMP-Editor is written in Java, and also can be downloaded from [3]. Navigating to `wmp-editor/wmpeditor-v2.37/` reveals the `WMPeditor.jar` file. In order to execute it, just `cd` to the mentioned directory and type: `java -jar WMPeditor.jar` to open WMP-Editor.

Once inside, it is possible to open other `.sm` files like the ones under `mac-example/dcf/` through the *file* menu on the top left corner. The `dcf-master.sm` example is shown in Figure 1.

C. Current state of our implementation

Today we had the opportunity of getting a question answered by one of FLAVIA team member. This was related to the adjustment of the backoff value through a 0x0D TX_PKT_SCHEDULER action parameter, namely BACKOFF SLOT.

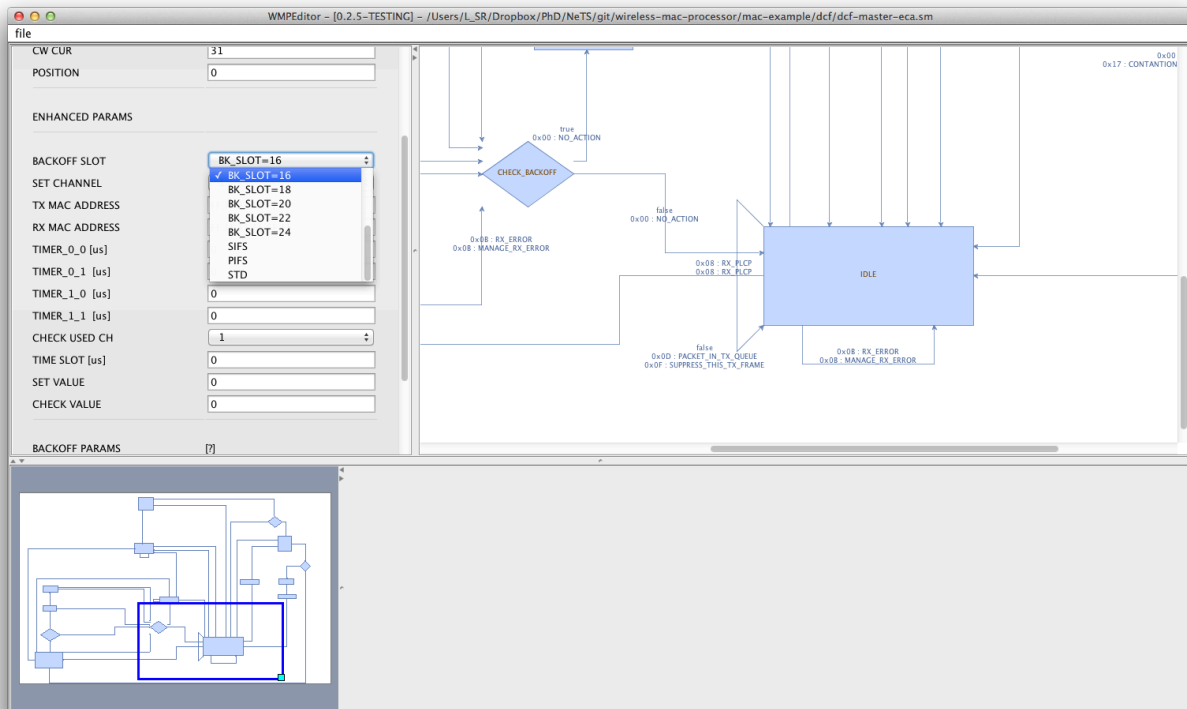


Fig. 1. WMP-Editor layout

As of now, I believe this measure just resets the backoff counter to the specified value. I could not find any other indication that this behavior can be further adjusted. Nevertheless, the option STD of the same BACKOFF SLOT parameter is supposed to mimic the behavior of DCF.

As for further software requirements: there are a lot of files in the Github repository, and rather fewer are the ones with clear documentation.

In relation to the hardware requirements, the wireless card used in the documentation is called *Broadcom Air Force One*, and what I believe to be a similar model can be found at [4].

Summarizing the questions resulting from this first report:

- 1) *Where is the STD parameter programmed?*
- 2) *Can we modify it to behave as our new backoff scheme?*
- 3) *Do we have all what we need to try the different MAC protocols provided as examples?*

REFERENCES

- [1] L. Sanabria-Russo, J. Barcelo, and B. Bellalta, "Fairness in Collision-Free WLANs," *ArXiv e-prints*, Feb. 2013.
- [2] G. Bianchi. (2010) FLAVIA Project: Flexible Architecture for Virtualizable future wireless Internet Access. Webpage. [Online]. Available: <http://www.ict-flavia.eu>
- [3] D. Garlisi, F. Giuliano, and P. Gallo. (2012) Wireless MAC Processor: a Github code repository. [Online]. Available: <https://github.com/ict-flavia/wireless-mac-processor>

- [4] Broadcom. (2012) NEW PC BCM4318 Wireless LAN PCI Network Card 54Mbps WiFi 11g. [Online]. Available: <http://amzn.to/Y76l8a>