Sanchith Padmaraj

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Dear Recruiter,

I am an Electrical and Electronics Engineering Masters student at Illinois institute of technology, Chicago with specific emphasis on VLSI/hardware. I am writing to apply for Verification Engineer at Intel, which came to my attention through LinkedIn.

I have worked as an intern at Swarm Robotix, Naperville, IL. During this time, I have worked in Communication team for communication between robots to form a swarm. I have also interacted with clients and understood the requirement and lead my team in building a battery management system as well as a kill switch.

As a master’s student in Electrical and Electronics Engineering, I have worked on hardware and SoC projects, specifically on Design and implementation of a MIPS CPU with multicycle Datapath, CAD Tool Design for Time Slack Analysis with Tcl/Tk and C Programming, FinFET Transistor Characterization and Domino Logic Operation, 32-bit Pipelined CPU design. My technical skills include using EDA tools like Cadence, ModelSim in Verilog, VHDL, system Verilog, Tckl/Tk languages. I am excited about the great opportunity to work alongside the expert hardware team at Intel.

I have attached resume with application for your kind review. I would certainly appreciate an opportunity to speak with you for discussing my interests, qualifications and eager to learn more about your expectations, in more detail. Please feel free to contact via email [spadmaraj@hawk.iit.edu](mailto:spadmaraj@hawk.iit.edu) or by phone at 312 843 3256. Thank you for your time and consideration.

Sincerely,

Sanchith Padmaraj