Digital IC Design

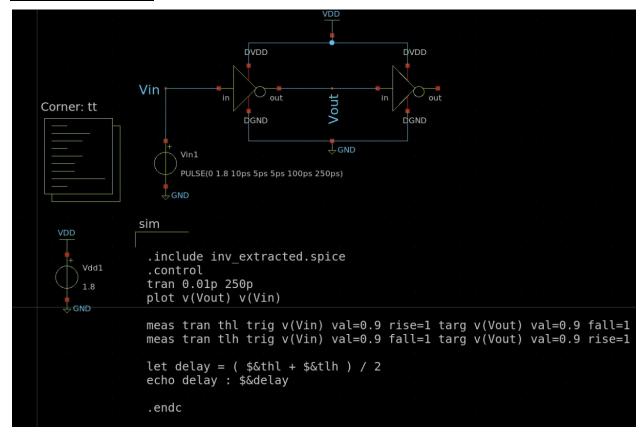
EE5311

Mourya Sai Sandeep EE22B045

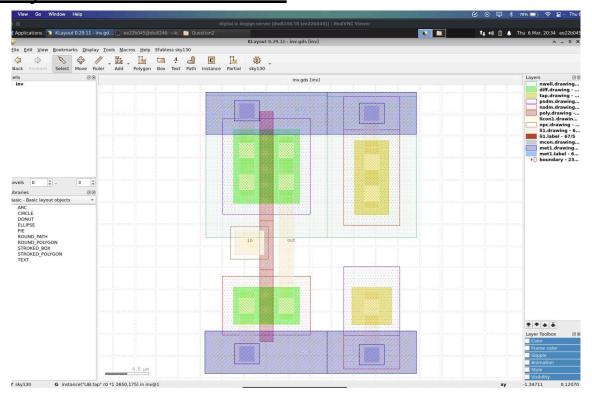
Tutorial - 5
Report

Experiment - 1

Schematic



KLayout for inverter:



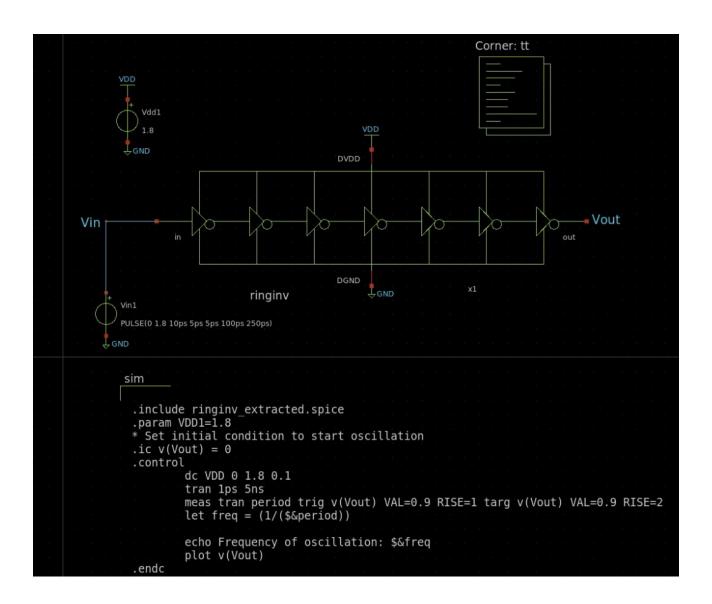
Extracted Parasitics:

```
inv_extracted.spice
                                                                                                                                    Save ≡
          Open ▼ +
1 * NGSPICE file created from inv.ext - technology: sky130A
comp.0 2 .subckt inv in out DVDD DGND
3 X0 out.tl in.t0 DVDD.tl DVDD.t0 sky130_fd_pr_pfet_0lv8 ad=0.252 pd=2.28 as=0.252 ps=2.28 w=0.84
         4 X1 out.t0 in.t1 DGND.t1 DGND.t0 sky130_fd_pr__nfet_01v8 ad=0.126 pd=1.44 as=0.126 ps=1.44 w=0.42 l=0.15
       5 R0 in.n0 in.t0 251.139
inv.syr 6 R1 in.n0 in.t1 201.333
7 R2 in in.n0 153.423
8 R3 DVDD DVDD.t0 949.414
          9 R4 DVDD DVDD.t1 397.99
        10 R5 out out.t1 410.955
11 R6 out out.t0 280.94
12 R7 DGND DGND.t0 4634.07
13 R8 DGND DGND.t1 275.142
        14 C0 out DVDD 0.102792f
        15 C1 in DVDD 0.106182f
16 C2 out in 0.039803f
        17 C3 out DGND 0.127169f
        18 C4 in DGND 0.232052f
19 C5 DVDD DGND 0.773714f
        20 .ends
                                                                                         Plain Text ▼ Tab Width: 8 ▼
                                                                                                                                    Ln 20, Col 6
                                                                                                                                                               INS
```

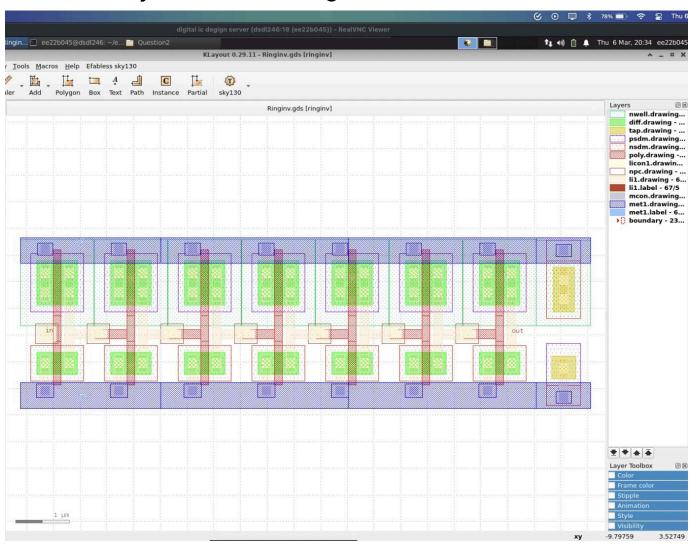
- A) The Delay of the Circuit(without the Parasitics) = $2.07 \times 10^{4}-11$ sec
- B) The Delay of the Circuit With Parasitics = $2.52 \times 10^{4}-11$ sec

Experiment - 2:

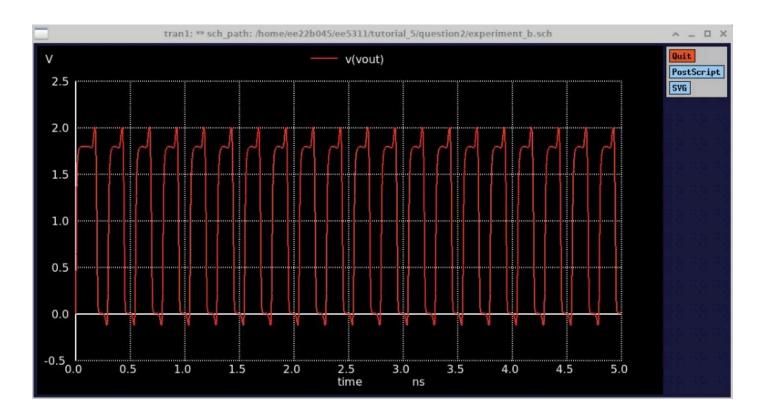
<u>Schematic</u>



Kayout of Ring Oscillator



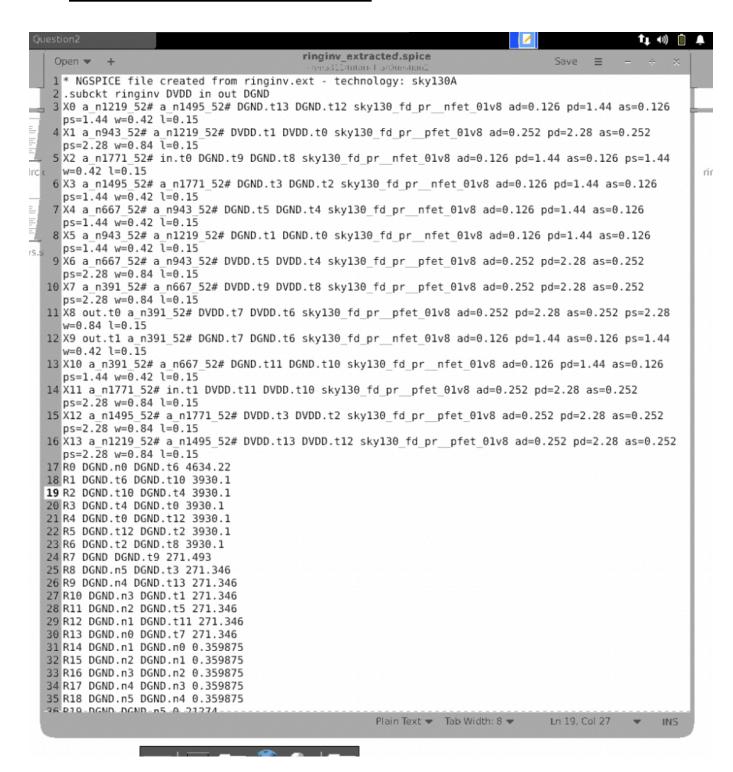
c) When parasitics are not Included in the Schematic:



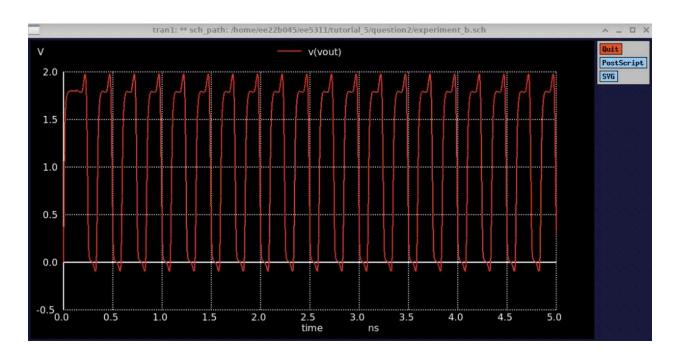
This graph shows the Oscillations without counting for the Parasitics Frequency of oscillation : 3.37×10^{9} Hz or 3.37GHz

X	Experiment_B.spice" -a sh	^	_		×
Using SPARSE 1.	.3 as Direct Linear Solver				
Initial Transie	ent Solution				
Node	Voltage				
x1.net1 vin vdd x1.net2 x1.net3 x1.net4 x1.net5 x1.net6 vout vin1#branch vdd1#branch	1,8 0 1,8 6,71229e-07 1,8 6,71229e-07 1,8 6,71229e-07 0 0				
No. of Data Row period	= 2.959575e-10 targ= 3.008172e-10 trig scillation: 3.37886E+09	= 4.8596	78e-	12	

Extracted Parasitics:



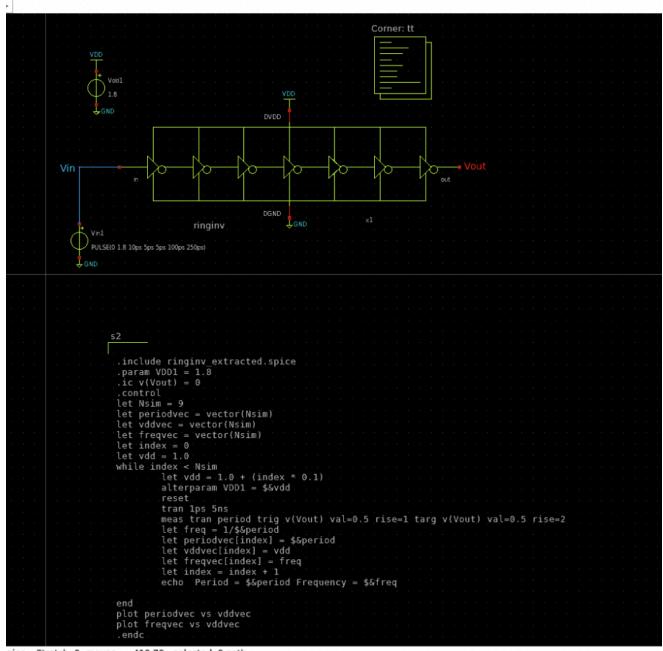
A) When Parasitics are included in the schematic:



This graph shows the Oscillations without counting for the Parasitics Frequency of oscillation : 2.97×10^{9} Hz or 2.97GHz

X	Experiment_B.spice" -a sh	1 X
x1.dgnd.n0	6.86543e-10	
x1.dand.n5	1.55376e-10	
x1.dgnd.n4	3,31166e-10	
x1.dand.n3	5,06945e-10	
x1.dgnd.n2	5,95778e-10	
x1.dgnd.n1	6.84599e-10	
x1.dvdd.n0	1.79972	
vdd	1.8	
x1.dvdd.n5	1,79997	
x1.dvdd.n4	1.79992	
x1.dvdd.n3	1.79987	
x1.dvdd.n2	1.79982	
x1.dvdd.n1	1.79977	
x1.in.n0	0	
vin	ň	
vout	0	
vin1#branch	ň	
vdd1#branch	-0.000141195	
Vdd1#branch	-0,000141135	
Reference valu	e: 4.44150e-09	
No. of Data Row	s : 5248	
period	= 3.389173e-10 targ= 3.455911e-10 trig= 6.673829e-12	
	cillation: 2.95057E+09	

B)Code and Schematic:

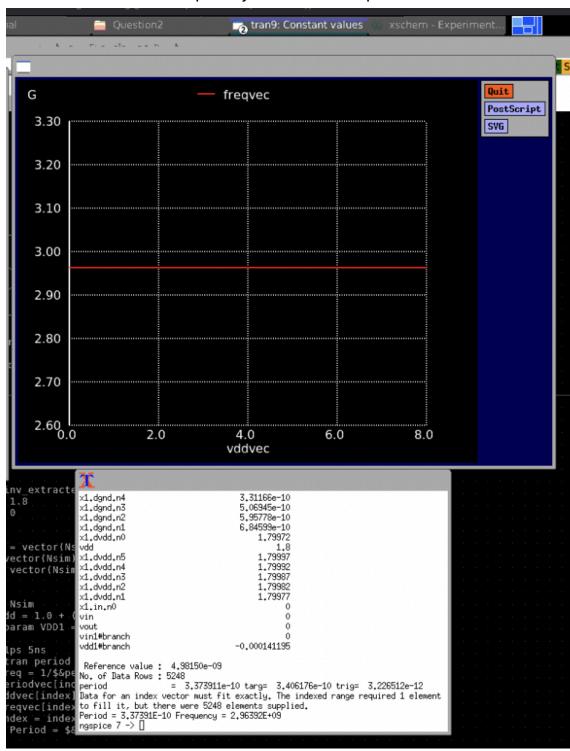


pice Stretch: 0 mouse = -410 70 - selected: 0 path: .

Observation:

Pre-layout frequency (no parasitics): Higher Post-layout frequency (with parasitics): Lower

Frequency vs VDD response



- The End -