

Digital IC Design

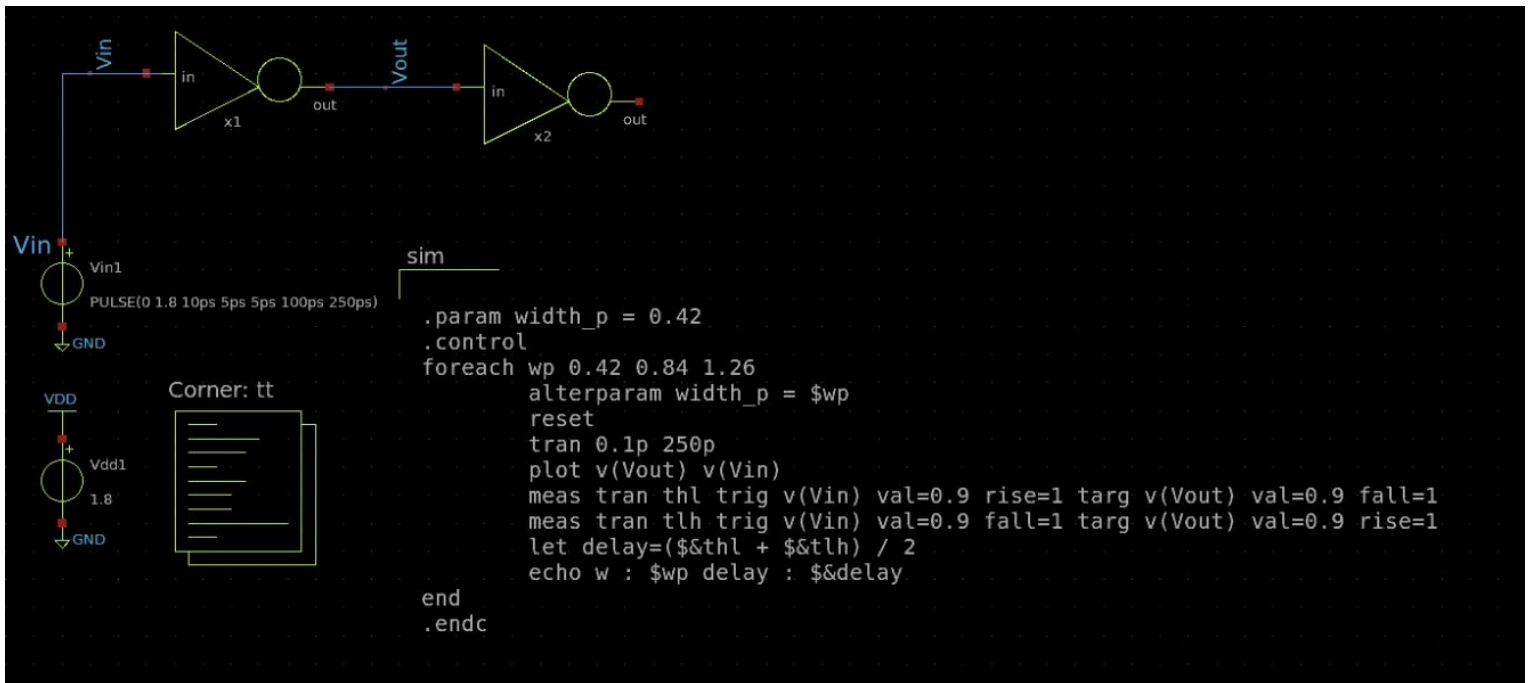
EE5311

Mourya Sai Sandeep
EE22B045

Tutorial - 3
Report

Experiment - 1

Schematic for (A),(B):



NgSpice response

```

T
asss1.spice" -a || sh
Doing analysis at TEMP = 27.000000 and TNOM = 27.000000
Using SPARSE 1.3 as Direct Linear Solver
Initial Transient Solution
-----
Node                                Voltage
-----
vdd                                1.8
vin                                0
vout                                1.8
net1                                9.60947e-07
vin1#branch                        0
vdd1#branch                        -3.47833e-10

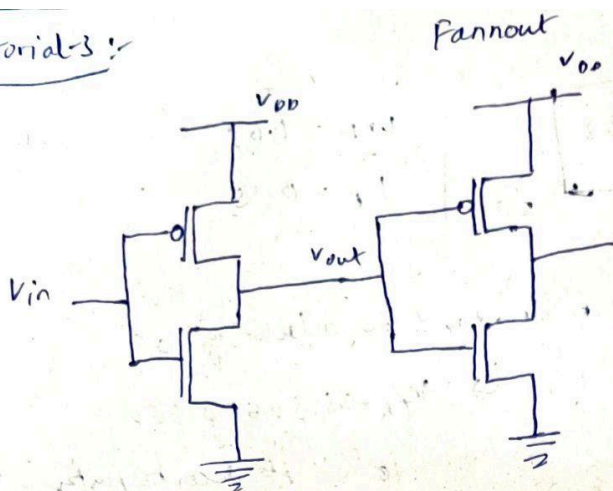
Reference value : 8.03500e-11
No. of Data Rows : 2520
thl                                = 2.327333e-11 targ= 3.577333e-11 trig= 1.250000e-11
tlh                                = 1.896577e-11 targ= 1.364658e-10 trig= 1.175000e-10
w : 1.26 delay : 2.11195E-11
ngspice 7 -> #
ngspice 8 -> █

```

Calculation:

Tutorial-3:-

① a)



$$V_{DD} = 1.8 \quad ; \quad L_n = L_p = 0.15 \mu m \quad \quad \omega_n = 0.42 \mu m$$

$$t_{PLH} = \frac{C_{total} V_{DD}/2 (E_{CPLP} + V_{DD} - |V_{TP}|)}{k_p (W/L)_p E_{CPLP} (V_{DD} - |V_{TP}|)^2}$$

$$t_{PHL} = \frac{C_{total} V_{DD}/2 (E_{CNLn} + V_{DD} - |V_{TN}|)}{k_n (W/L)_n E_{CNLn} (V_{DD} - V_{TN})^2}$$

$$C_{total} = C_{oxn} \omega_n L_n + C_{oxp} \omega_p L_p + C_j \omega_n L_{diff} + C_j \omega_p L_{diff} \\ + C_{jsw} (\omega_p + 2L_{diff}) + C_{jsw} (\omega_n + 2L_{diff})$$

$$+ 2C_{gdop} \omega_p + 2C_{gdon} \omega_n$$

$$\approx C_{oxn} \omega_n L_n + C_{oxp} \omega_p L_p \approx (0.00834) (0.42 \times 10^{-6}) (0.15 \times 10^{-6}) \\ + (0.00816) (\omega_p \times 10^{-6}) (0.15 \times 10^{-6})$$

$$C_{total} \approx (\omega_p + 0.42) (0.08834 \times 10^{-12} \times 0.10 + 0.00816 \times 0.10 \times 10^{-12})$$

$$C_{total} \approx (\omega_p + 0.42) (2.475) \times 10^{-15}$$

$$t_p = \frac{t_{PLH} + t_{PHL}}{2}$$

$$t_{PLH} = \frac{C_{total} (0.9) (3.1275 \times 10^{-6} + 1.0 \times 10^{-6})}{\left[\frac{1}{2} (0.025) (0.00834) \right] \left(\frac{\omega_n}{L_n} \right)^2 (3.1275 \times 10^{-6}) \times (1.8 - 0.7)^2}$$

$$t_{PLH} = \frac{C_{total} (0.9) (1.1)}{9.88 \times 10^{-4}}$$

$$t_{PHL} = \frac{C_{total} (0.9) (1.224 \times 10^{-6} + 1.1)}{\left(\frac{1}{L} \right) (0.009) (0.00816) \left(\frac{\omega_p}{L_p} \right)^2 (0.009) (0.00816) \left(\frac{\omega_p}{L_p} \right) (1.1)^2}$$

$$= \frac{C_{total} (0.9) (1.1)}{145023 \times (\omega_p)^3}$$

for 0.42 μm :-

$$a) C_{total} = 2.079 \times 10^{-15}$$

$$t_{PHL} = 2.3 \times 10^{-11}$$

$$t_{PLH} = 1.8 \times 10^{-11}$$

$$t_p = 2.05 \times 10^{-11}$$

b) for $0.85 \mu\text{m}$

$$t_{PHL} = 3.5 \times 10^{-11}$$

$$t_{PLH} = 1.3 \times 10^{-11}$$

$$t_P = 2.4 \times 10^{-11} \text{ sec}$$

c) for $1.26 \mu\text{m}$

$$t_{PHL} = 1.25 \times 10^{-11}$$

$$t_{PLH} = 1.175 \times 10^{-11}$$

$$t_P = 1.21 \times 10^{-11} \text{ sec}$$

(b) ω_P such that t_P is minimum

$$t_P \propto \frac{V_{DD} (C_{CL} + V_{DD} - V_T)}{(V_{DD} - V_T)^2}$$

$$t_P = D \times \frac{(1.8) (C_{CL} + 1.1)}{(1.1)^2}$$

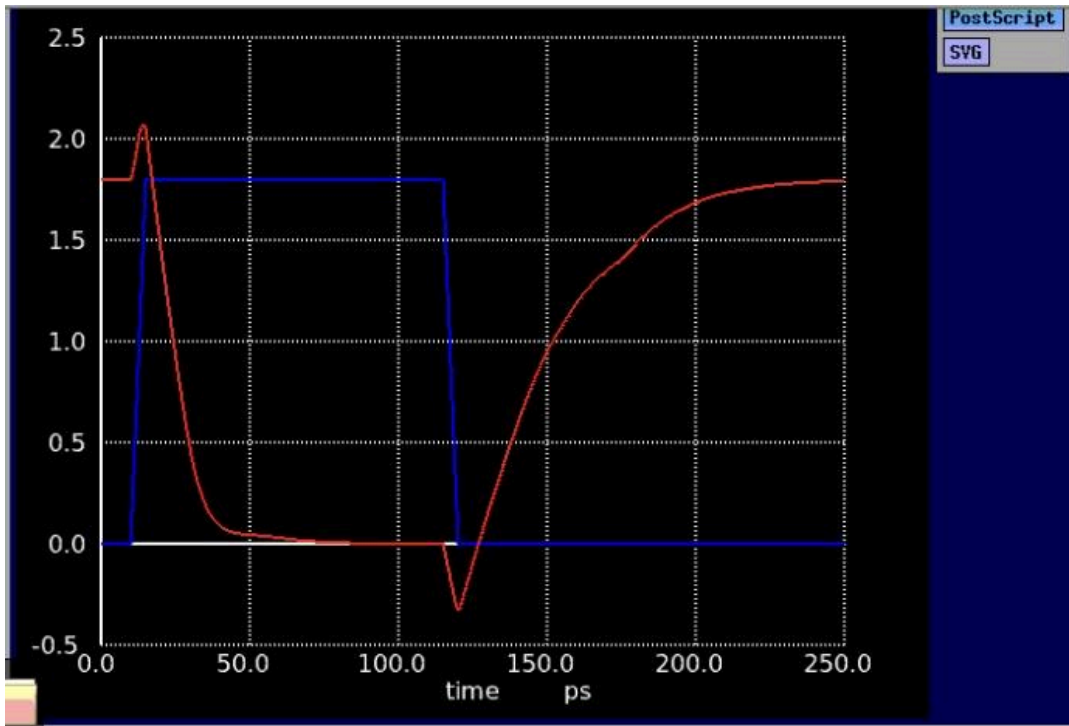
on taking this; $\omega_P \approx \sqrt{\frac{k_n}{k_P}} \omega_n$

$$k_n = \frac{1}{2} \mu_n C_{ox,n} \frac{W_n}{L_n} = \frac{1}{2} (0.025) (0.00834) \times \frac{0.92}{0.15}$$

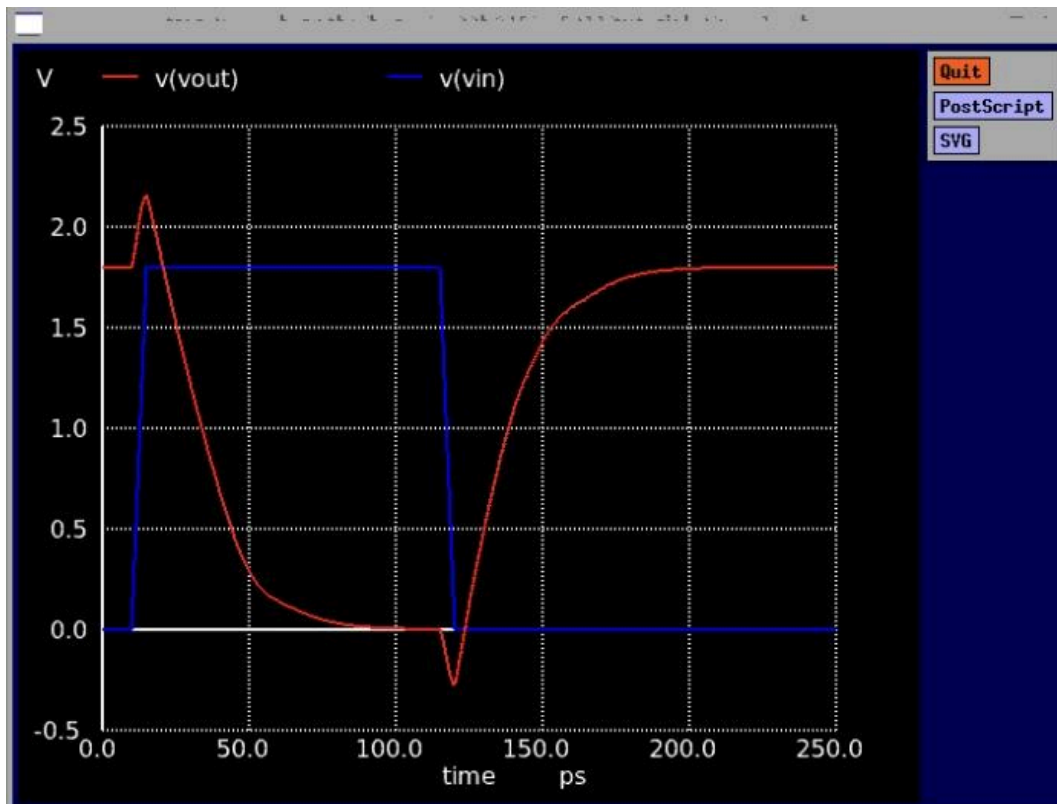
$$k_n = 2.919 \times 10^{-5}$$

$$k_P = \frac{1}{2} \mu_P C_{ox,p} \frac{W_P}{L_P} = \frac{1}{2} (0.009) (0.008816) \times \frac{W_P}{0.15}$$

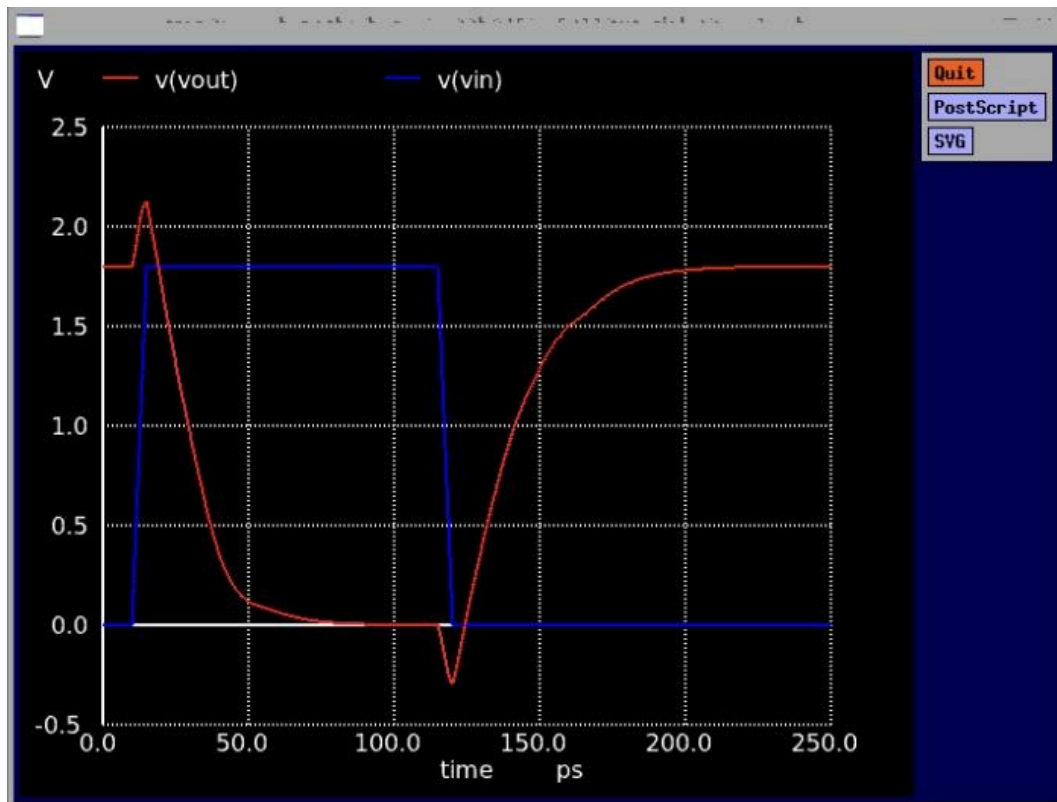
$$k_P = 2.448 \times 10^{-5}$$



Above figure shows the graph for $(W)p = 0.42\mu\text{M}$



Above figure shows the graph for $(W)p = 0.84\mu\text{M}$.



Above figure shows the graph for $(W)p = 1.26\mu\text{M}$

T_p for $W_p = 0.42\mu\text{M} = 2.18 \times 10^{-11}$ sec

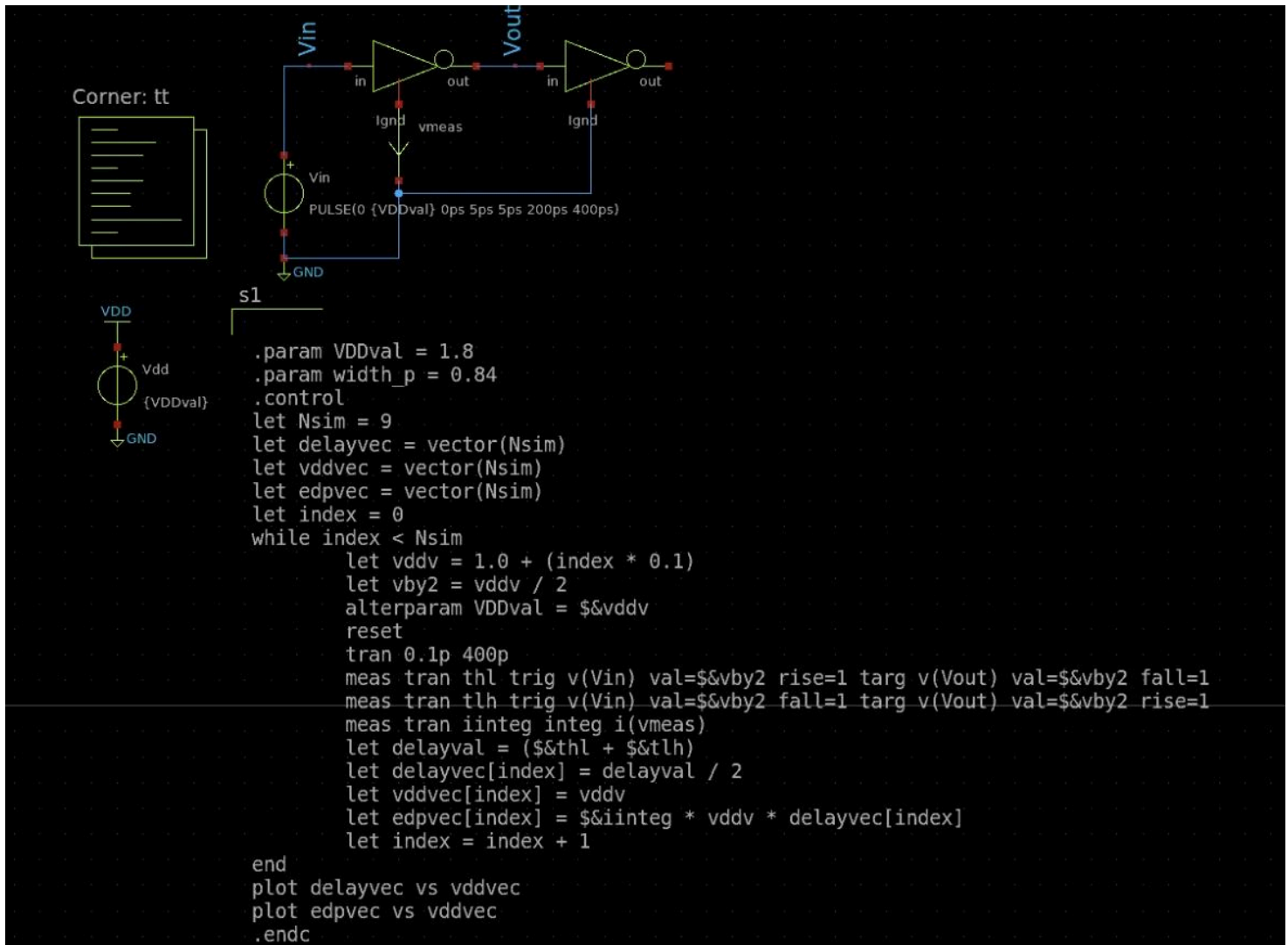
T_p for $W_p = 0.84\mu\text{M} = 1.99 \times 10^{-11}$ sec

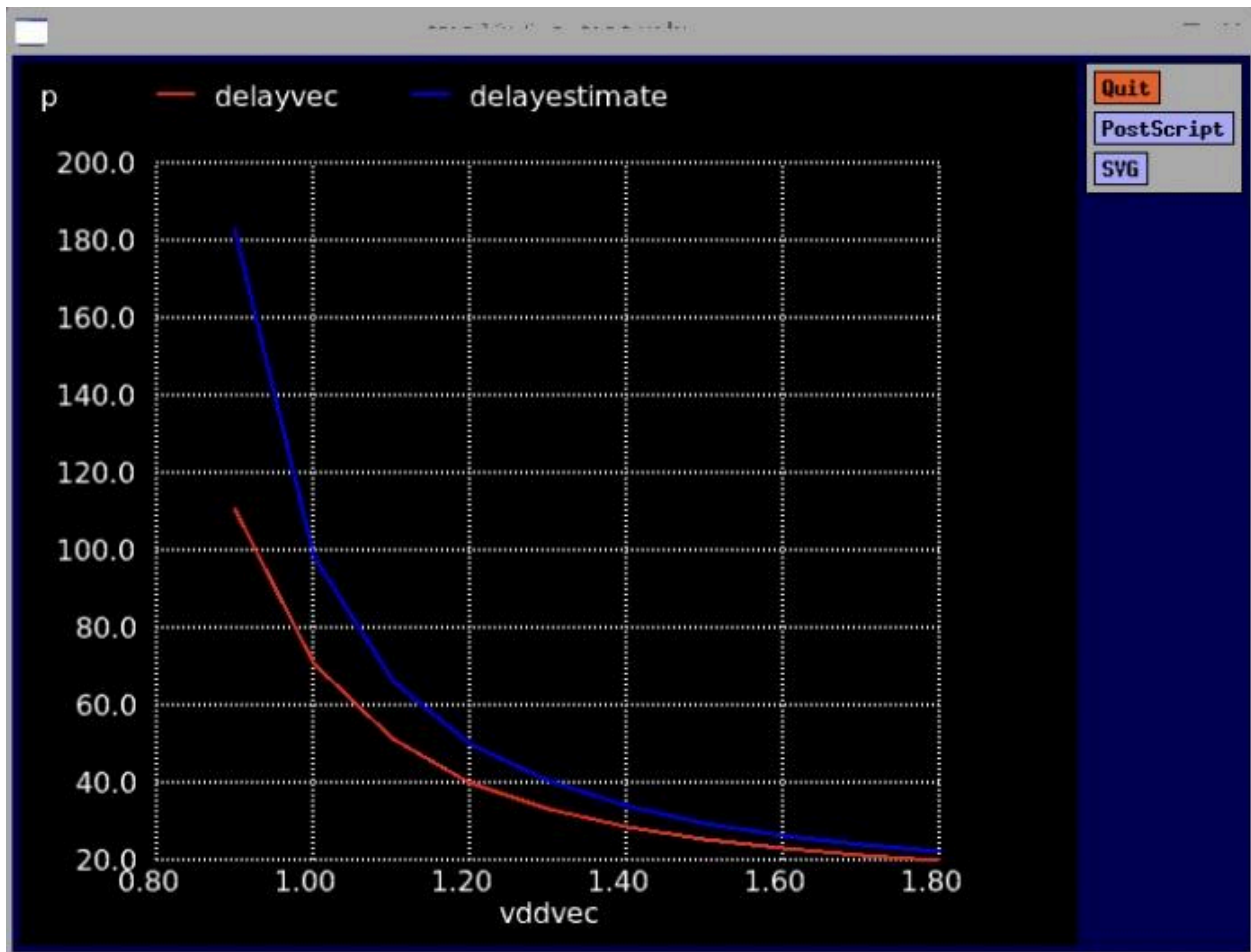
T_p for $W_p = 1.26\mu\text{M} = 2.11 \times 10^{-11}$ sec

Optimal width for less delay here is $0.84\mu\text{M}$

Experiment - 1

Schematic for (B) & (C):





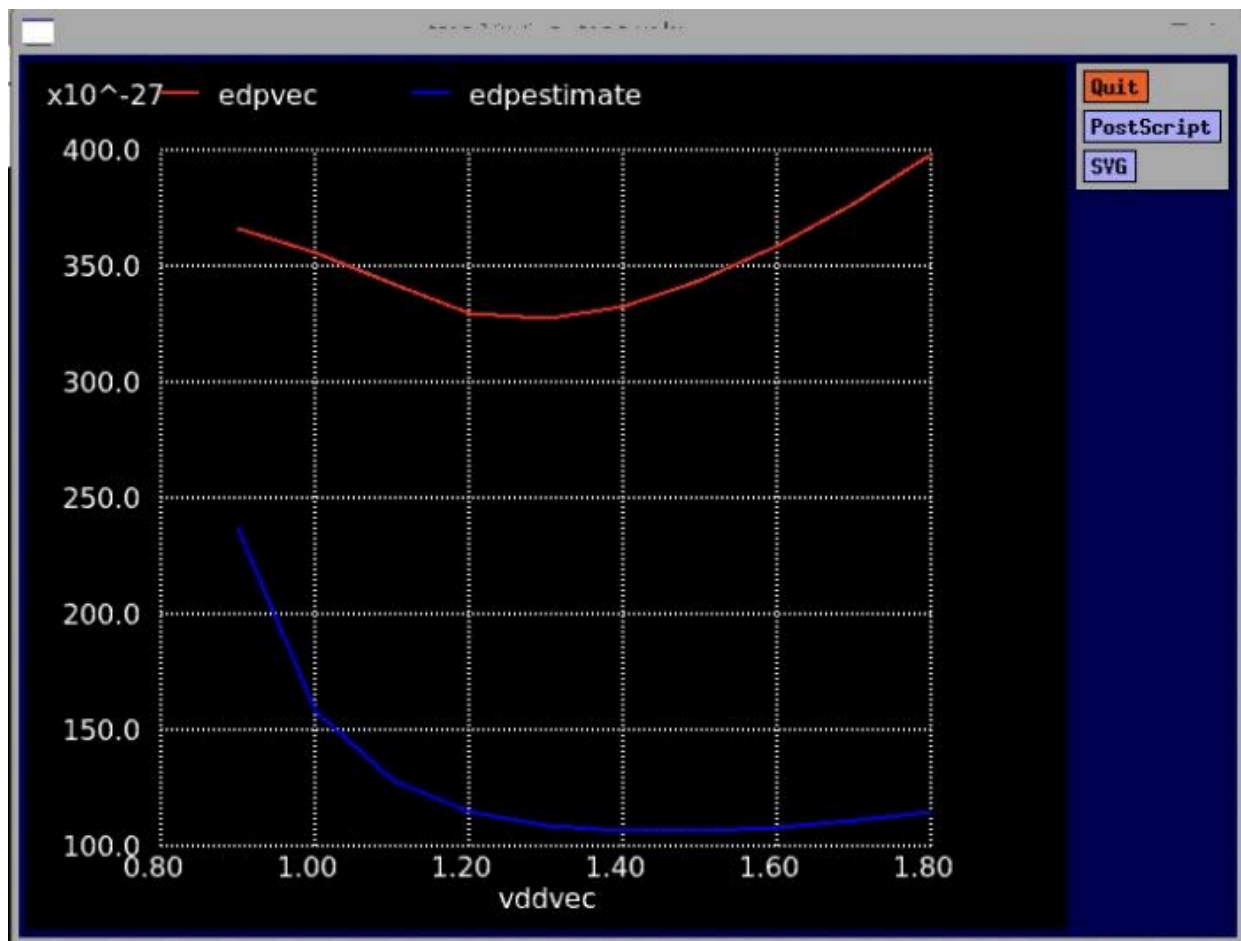
Above figure shows Delay as a function of VDD increased from 1 to 1.8V as steps of 0.1

The Blue Plot is the Estimated Delay and the Red Plot is the Calculated Delay.

Analytical Expression:

The delay decreases as VDD increases, following an Inverse relationship.

$$T_p = K * V_{dd} / (V_{dd} - V_t)^2$$



Above Figure shows the Energy Density Product vs VDD. The Blue Plot is the estimated EDP. and The Red Plot is the Calculated EDP.

NgSPice Response:

```

Experiment_1_B_C.spice" -a || sh
Using SPARSE 1.3 as Direct Linear Solver
Initial Transient Solution
-----
Node                Voltage
-----
vdd                  1.8
vin                  0
vout                 1.8
net1                 0
net2                 6.71229e-07
vmeas#branch         3.51842e-14
vin#branch            0
vdd#branch           -2.43518e-10

Reference value : 6.15000e-11
No. of Data Rows : 623
thl                  = 1.821226e-11 targ= 2.071226e-11 trig= 2.500000e-12
tlh                  = 2.167016e-11 targ= 2.291702e-10 trig= 2.075000e-10
iinteg               = 1.11079e-14 from= 0.00000e+00 to= 6.00000e-10
edp: 3.9871E-25 vdd: 1.8
ngspice 7 -> █
ngspice 8 -> █

```

Calculations:

$$\omega_p \approx \sqrt{\frac{k_n}{k_p}} \omega_n$$

$$\omega_p \approx \frac{1}{\sqrt{\omega_p}} \times 1.09 \times 0.42 \Rightarrow \omega_p = (0.458)^{2/3}$$

$$\boxed{\omega_p \approx 0.59} \rightarrow \text{after approximation}$$

(c) Optimum V_{DD} :

$$\frac{\partial EDP}{\partial V_{DD}} = 0 \Rightarrow V_{DD_{min}} = \frac{5V_T - E_{CL}}{4} + \sqrt{\frac{(5V_T - E_{CL})^2 + 24V_T(E_{CL} - V_T)}{4}}$$

$$V_{DD_{min}} = \frac{4.5}{4} + \sqrt{\frac{20.25}{4} + 24 \times (0.7) \times (-0.7)}$$

$$= \frac{4.5}{4} + 0.72 \approx 1.845V$$

$$V_{DD_{optimal}} \approx 1.845V$$

- The End -

