

Timing Specifications

Figure 4 SCCB Timing Diagram

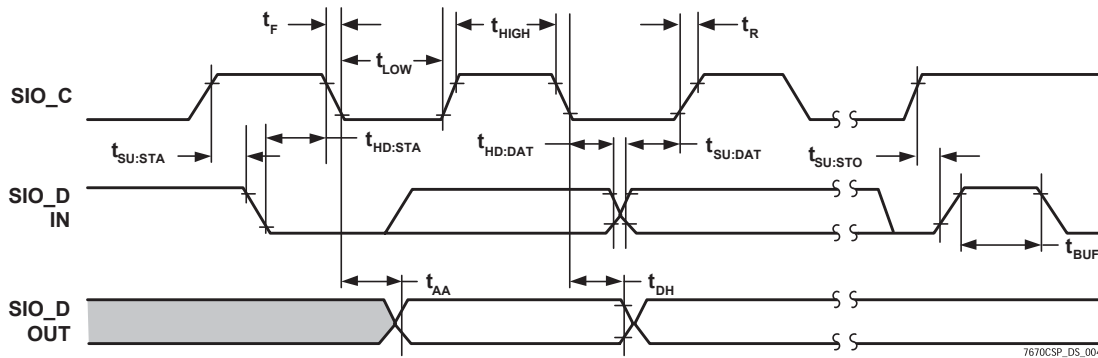


Figure 5 Horizontal Timing

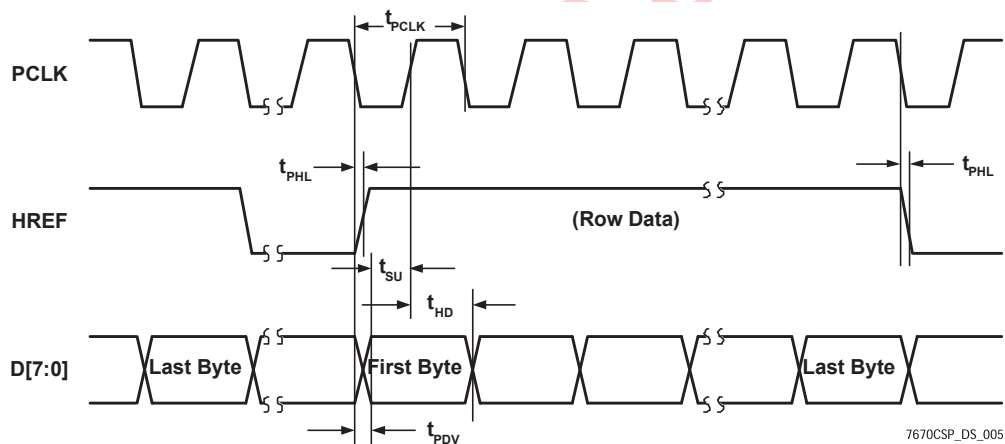
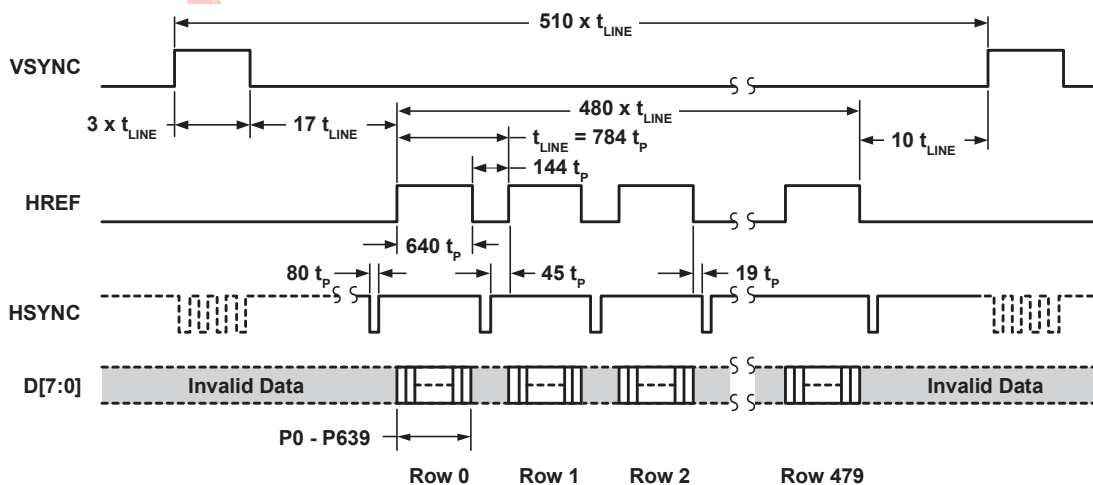


Figure 6 VGA Frame Timing



NOTE:

For Raw data, $t_p = t_{PCLK}$

For YUV/RGB, $t_p = 2 \times t_{PCLK}$

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Figure 7 QVGA Frame Timing

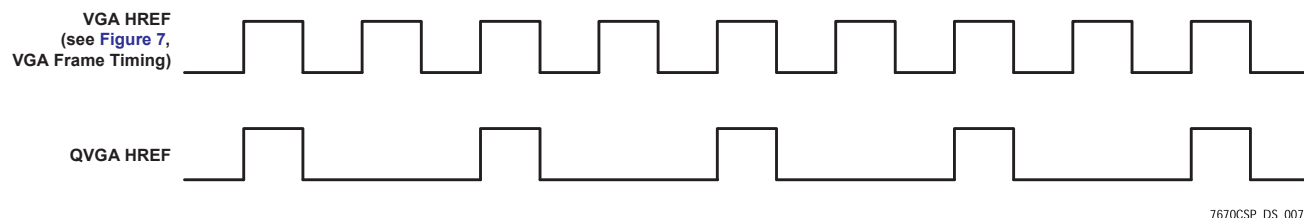


Figure 8 QQVGA Frame Timing

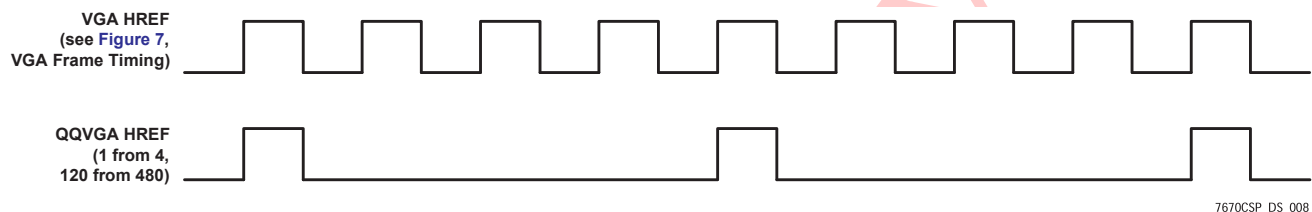


Figure 9 CIF Frame Timing

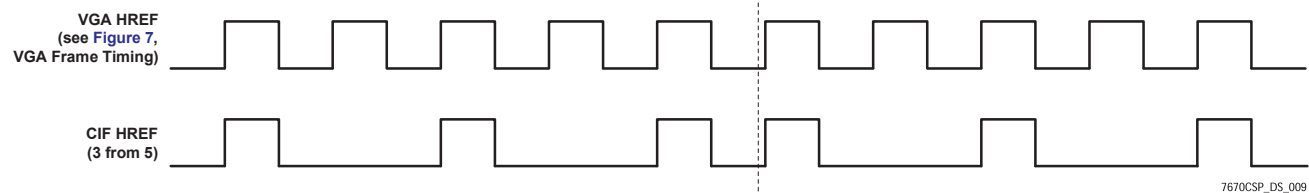


Figure 10 QCIF Frame Timing

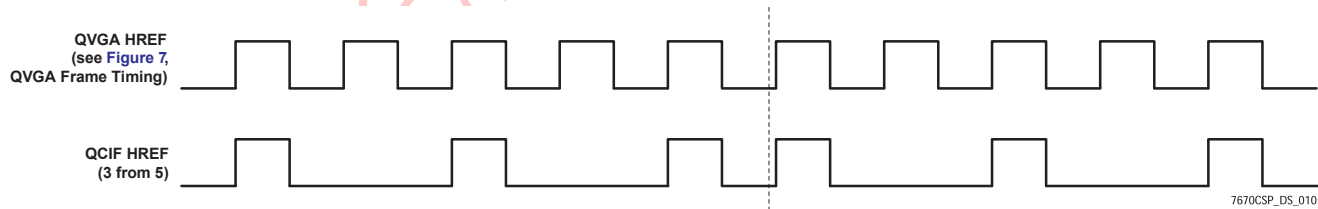


Figure 11 RGB 565 Output Timing Diagram

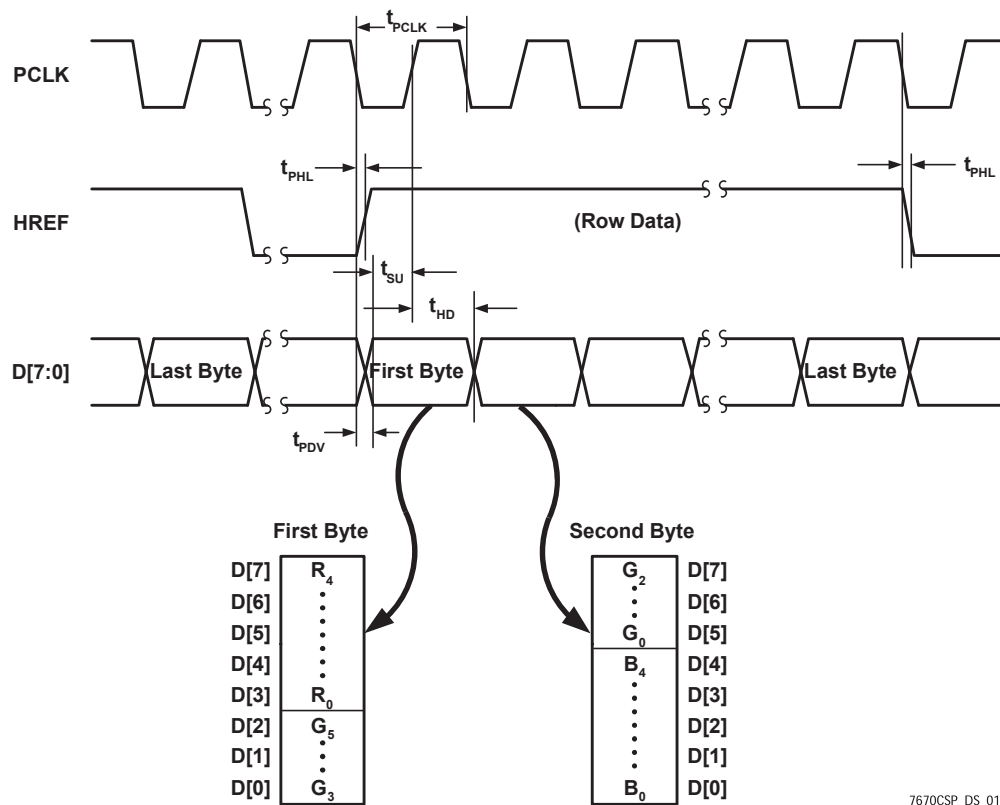


Figure 12 RGB 555 Output Timing Diagram

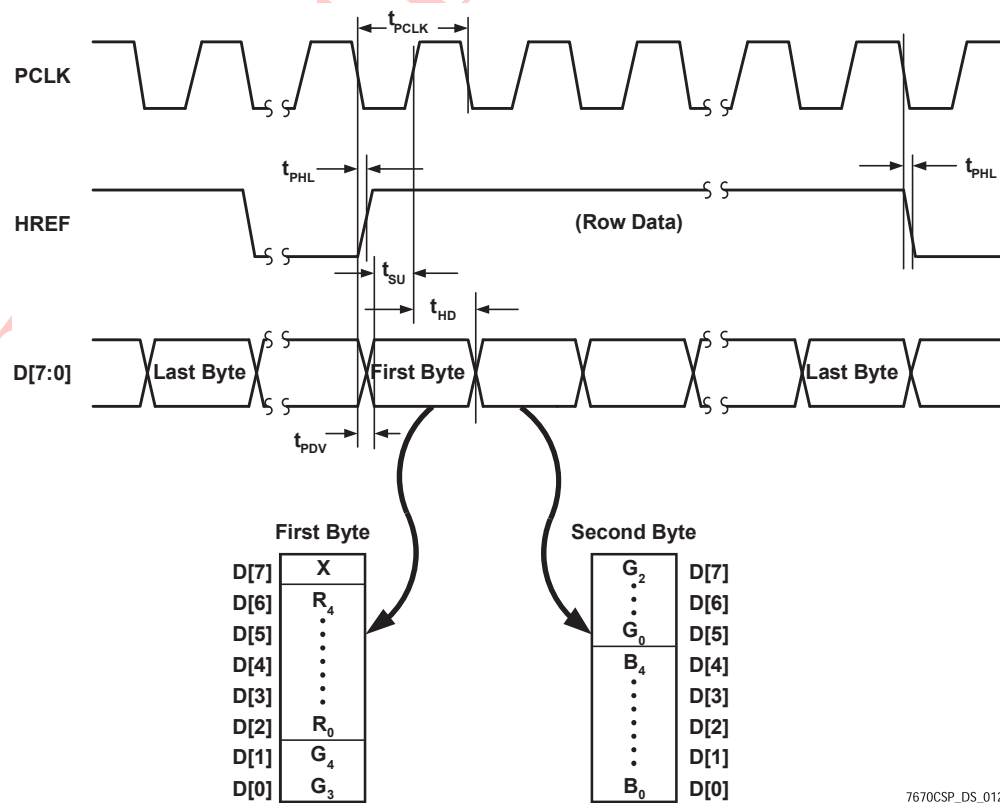
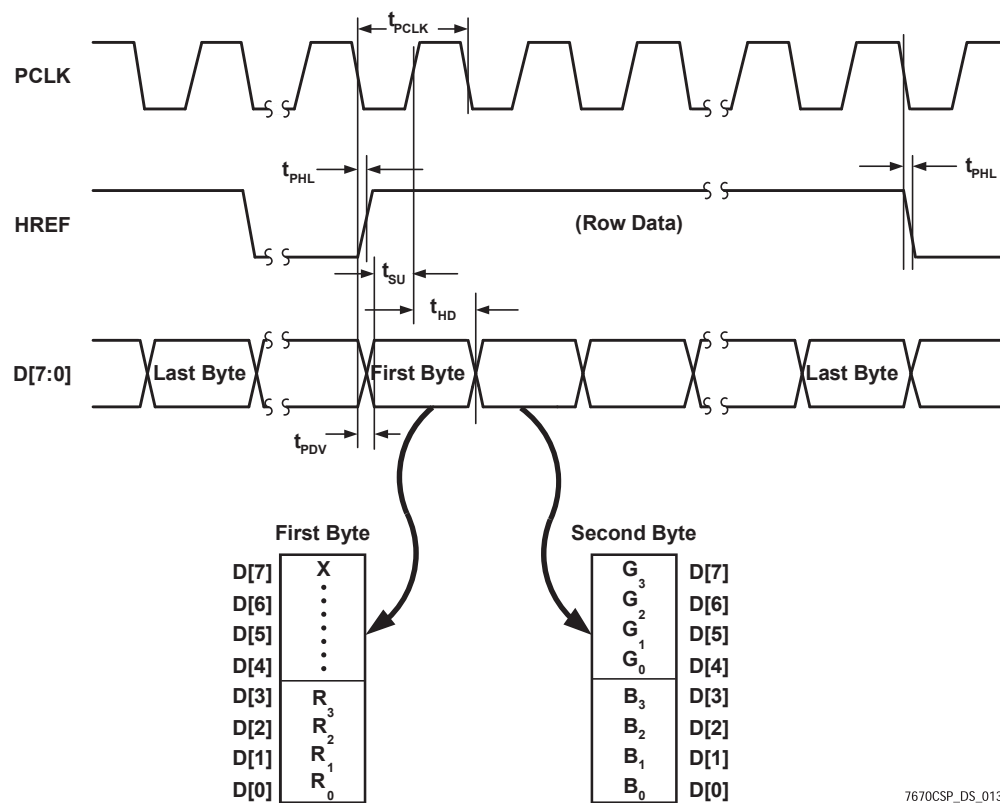


Figure 13 RGB 444 Output Timing Diagram



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