Standard cell Layout Design Portfolio
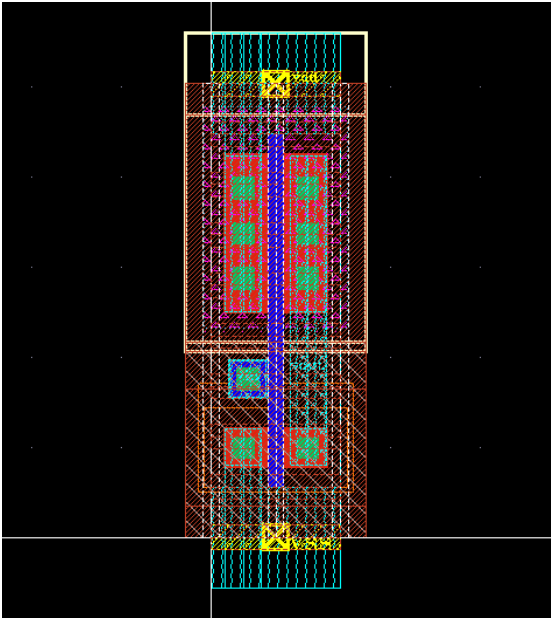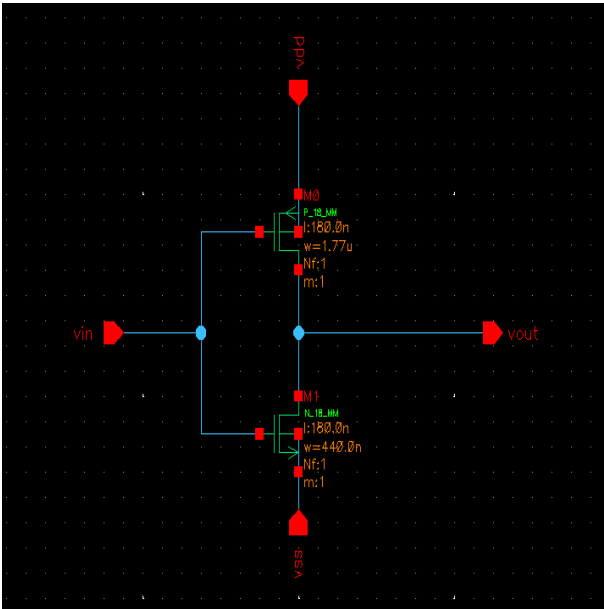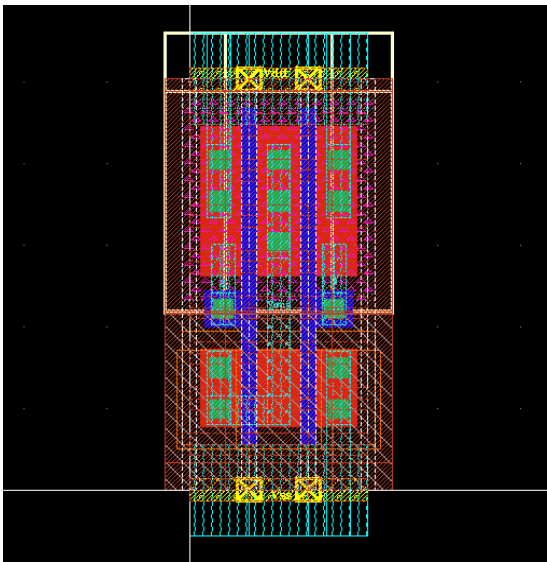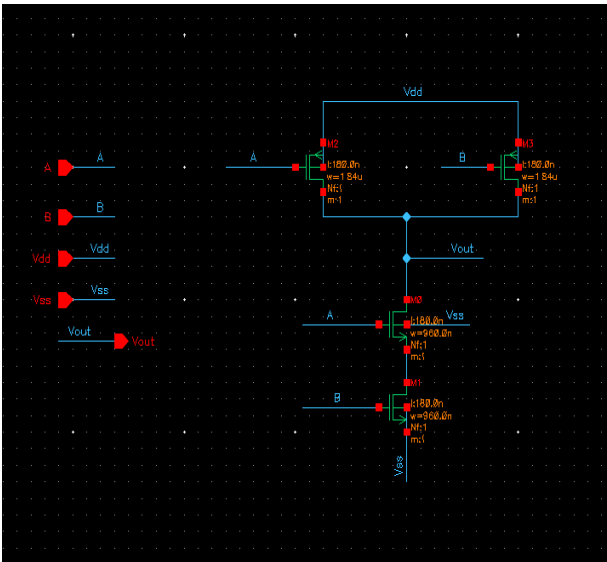
Designed and verified INV, NAND, XOR, MUX, D-Latch

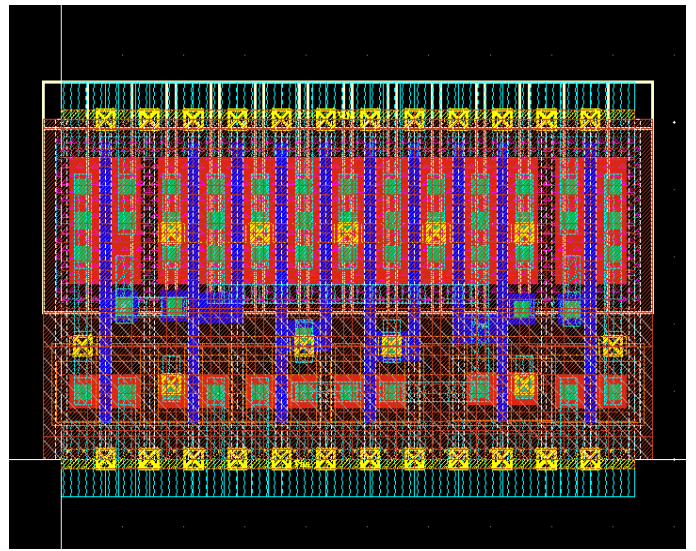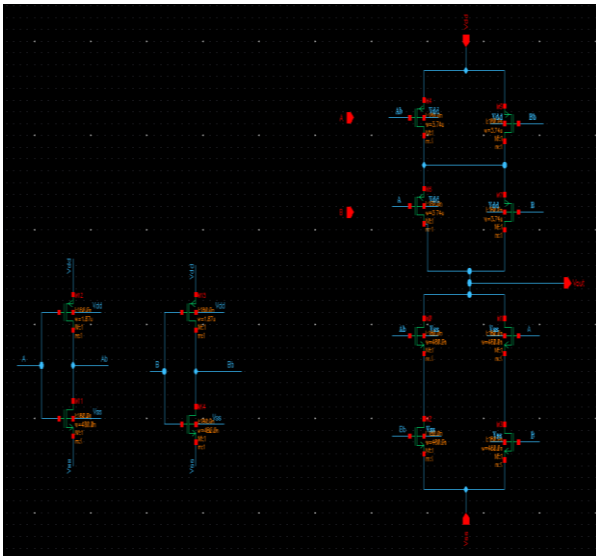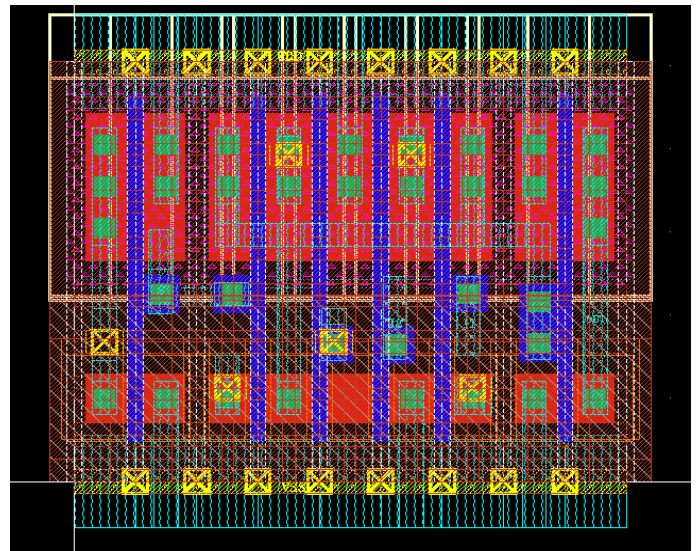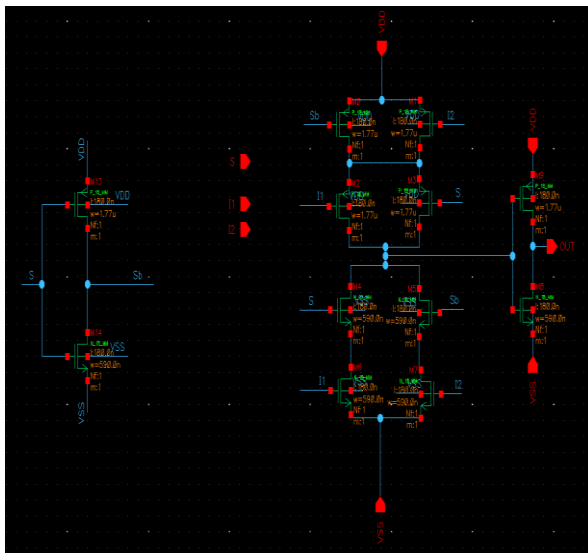### INVERTER

NAND

XOR

MUX

D-Latch