Init of hardware finished.
Allocate Word Size
/source/unitTest.c:26: failed:IsEqual(Test_Block_Start_Address,Global_BSA_Test)
=======================================
/source/unitTest.c:27: failed:EndTestcase()
*************
Testcases: failed: 1
passed: 0
Checks: failed: 1
passed: 0
************
**************************************
=======================================
Allocate Word Size_Range
/source/unitTest.c:44: failed:IsInRange(allocate_words(TEST_ALLOCATE_WORD_SIZE),LOWER_HEAP_LIMIT,UPPER_HEAP_LIMIT)
-=====================================

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**********	*
Testcases: failed: 2	
passed: 0	
Checks: f	failed: 2
	passed: 0
	******************
=======================================	=
Getting Address_BitSet	
=======================================	
/sou	urce/unitTest.c:60: failed:IsBitSet(Test_Address[4],7)
=======================================	=
/source/unitTest.c:61: failed:EndTestcase(	
	· :====================================
	**********
	Testcases: failed: 3
	passed: 0
	Checks:
failed: 3	
passed: 0	
***********	**********
==:	=======================================
	Getting Address_BitClear

=======================================			
/source/unitTest.c:77: passed:IsBitClear(Test_Address[4],7)			
Tostease passed			
Testcase passed.			
=======================================			
***********************			
Testcases: failed: 3			
passed: 1			
Checks: failed: 3			
passed: 1			
pusseu. 1			
*******			
Allocated address_8Bit			
=======================================			
/source/unitTest.c:95:			
failed:Is8Bit(Test_Block_Start_Address[4])			
=======================================			
/source/unitTest.c:96: failed:EndTestcase()			
=======================================			
*************			

Testcases: failed: 4

passed: 1

Checks: failed: 4		
passed: 1		
************	*****	
=======		======
	Allocated addres	s_16Bit
/source/unitTest.c:113: failed:Is16Bit(Test_Block	k_Start_Address[5])	
=======		======
failed:EndTestcase()	/source/unitTe	est.c:114:
=======================================		
************	****	
Testcases: faile		
Testcases. Talle		
	passed: 1	
	Checks: failed: 5	
********	F	passed: 1
*****		
=======================================		
Allocated address_3	32Bit	
=======================================		
/source/unitTest.c:132: passed:Is32Bit(Test_Block_Start_Address	5[2])	
=======================================	=====	
Testcase passed.		

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**********	
Testcases: failed: 5	
passed: 2	
Checks: failed: 5	
passed: 2	
**********	******
Allocated Address Isbuill	
Allocated_Address_IsNull	
/saursa /unitTast a:151, failaddaNull/Tast (	Dlack Start Addraga
/source/unitTest.c:151: failed:IsNull(Test_E	block_start_Address
=======================================	
/source/unitTest.c:152: failed:EndTestcase()	
=======================================	
**********	*****
Testcases:	failed: 6
	passed: 2
failed: 6	Checks:
passed: 2	
yasseu. z *************************	
=======================================	======
Allocated_Addre	

	Testcase passed.
	restcase passed.
	=======================================
******	********
Tes	stcases: failed: 6
	passed: 3
	Checks: failed: 6
	passed: 3
********	*********
*********	=======================================
 Mem_St	======================================
======================================	======================================
./source/unitTest.c:191: f	======================================

passed: 3

failed: 7		Checks:
	passed: 3	
	***********	

System shutdown.