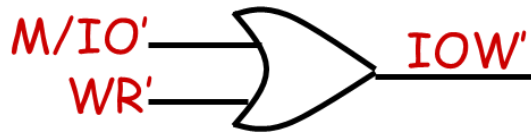
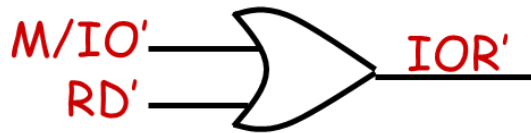
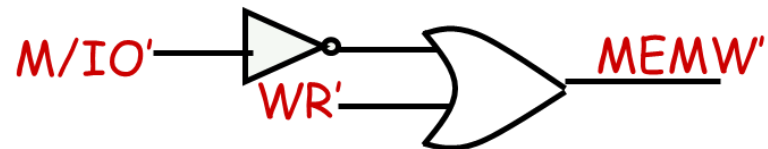
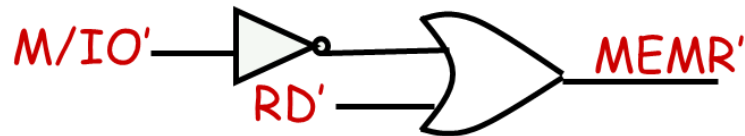
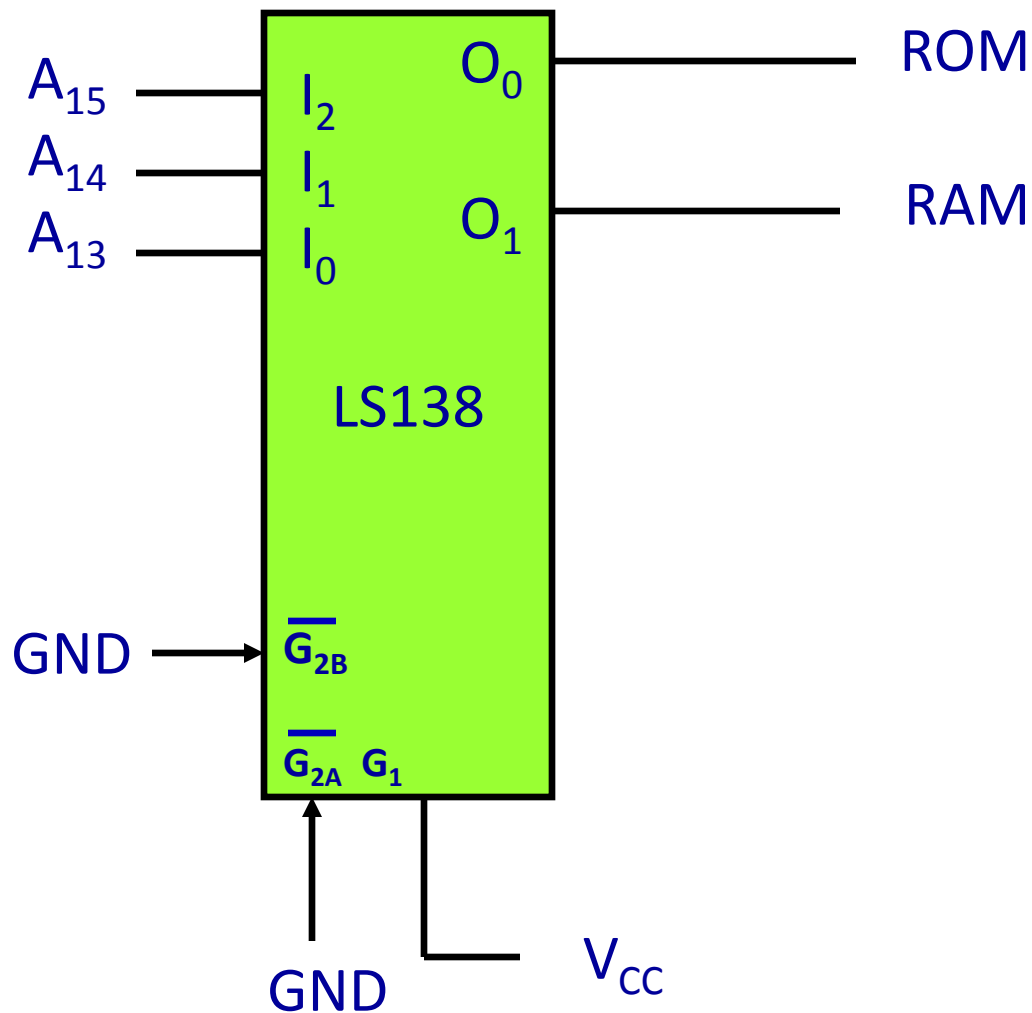


System Bus of 8086(Data + Control)



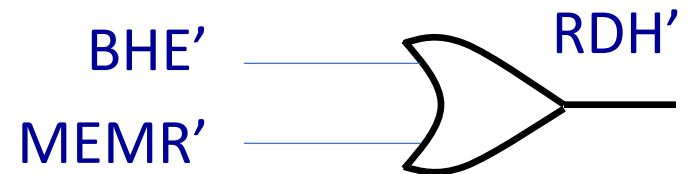
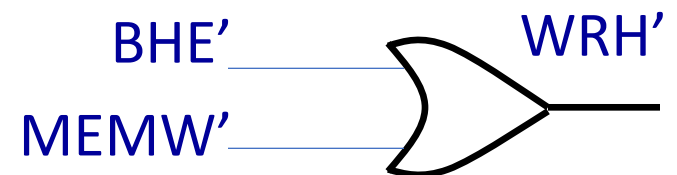
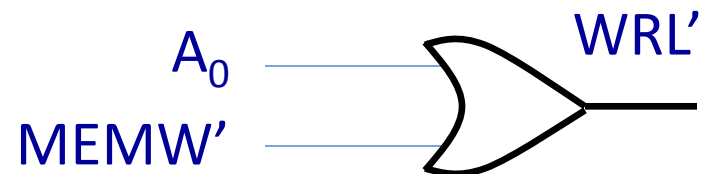
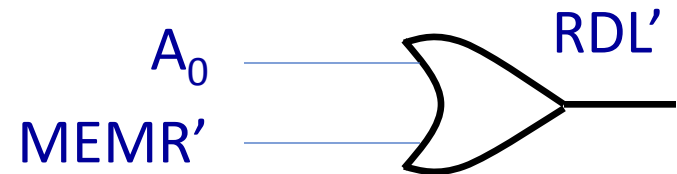
$M/IO'$	$RD'$	$WR'$	Bus cycle
1	0	1	$MEMR'$
1	1	0	$MEMW'$
0	0	1	$IOR'$
0	1	0	$IOW'$



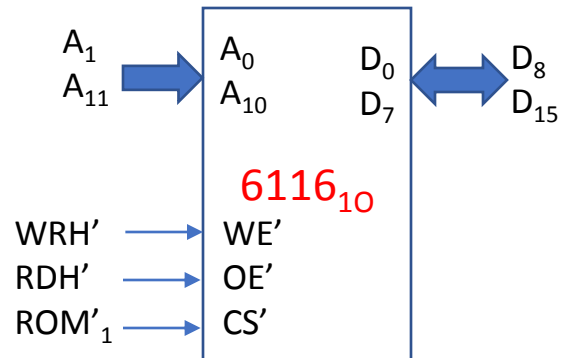
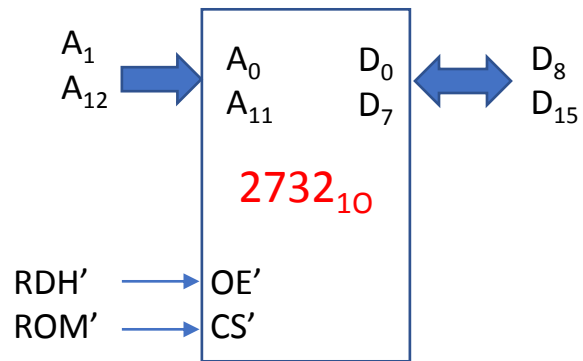
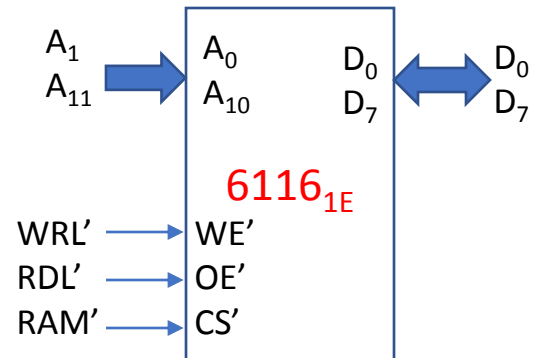
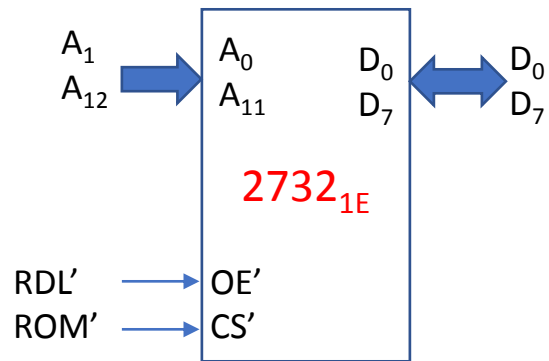


ROM

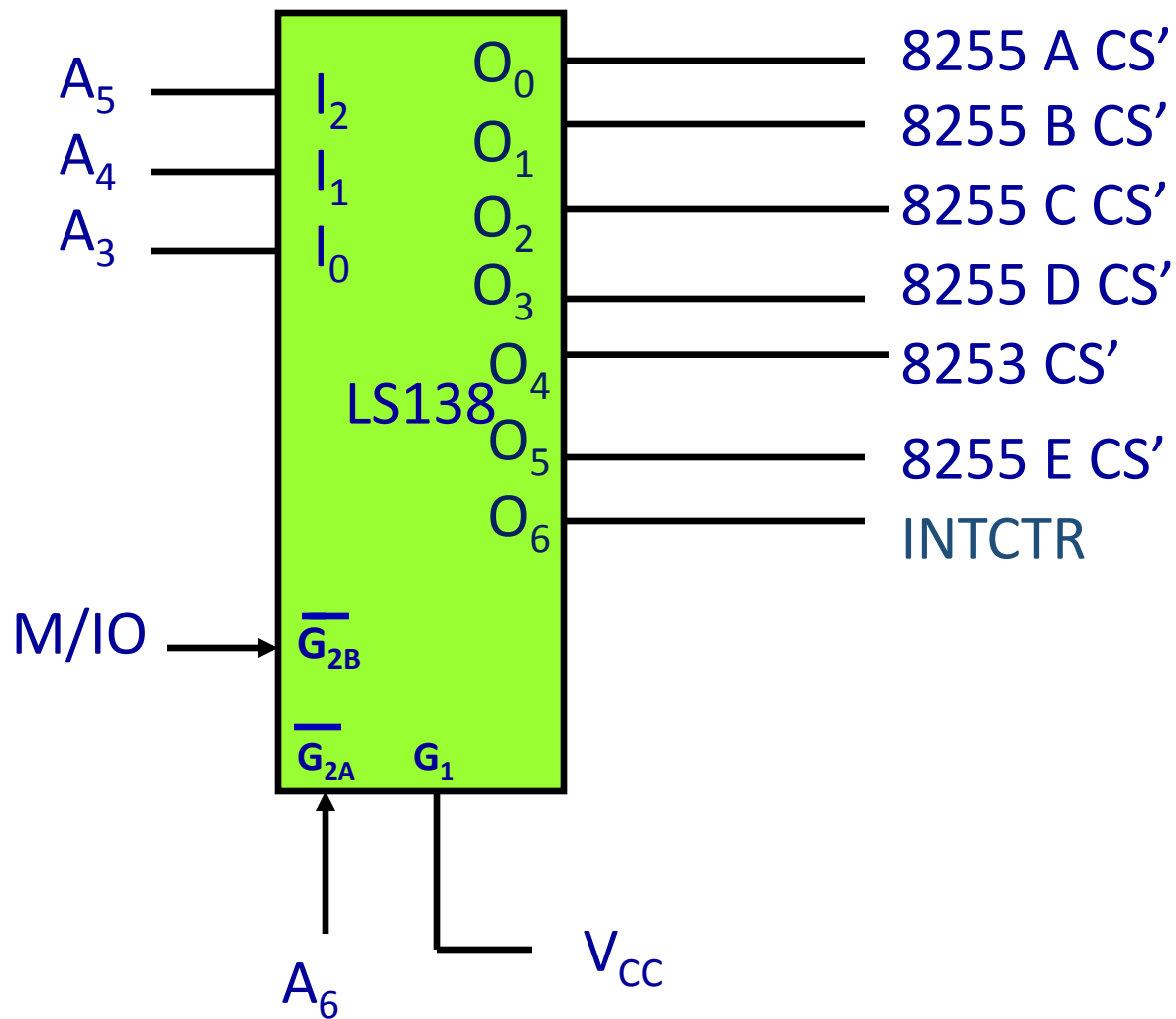
RAM



Memory Decoder

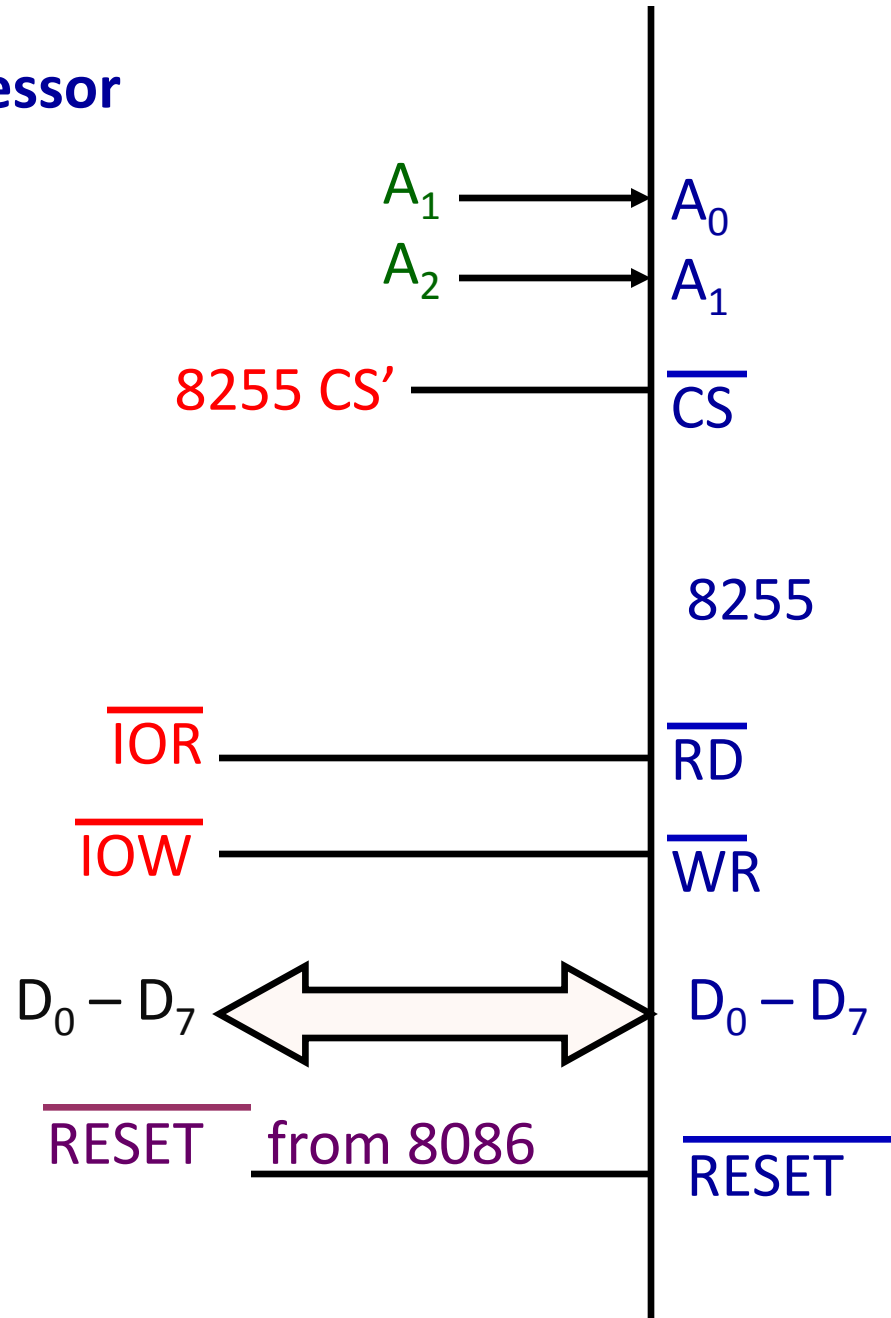


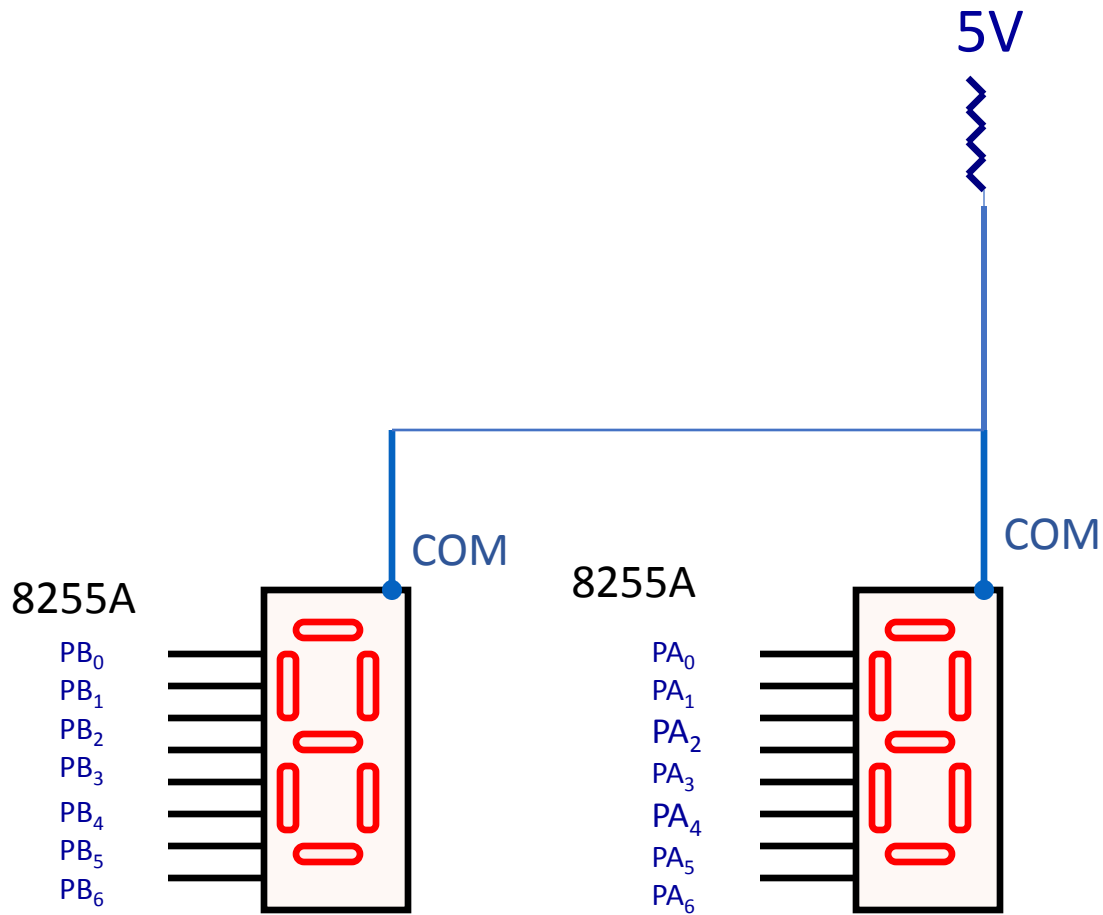
## Memory Interfacing



I/O Decoder

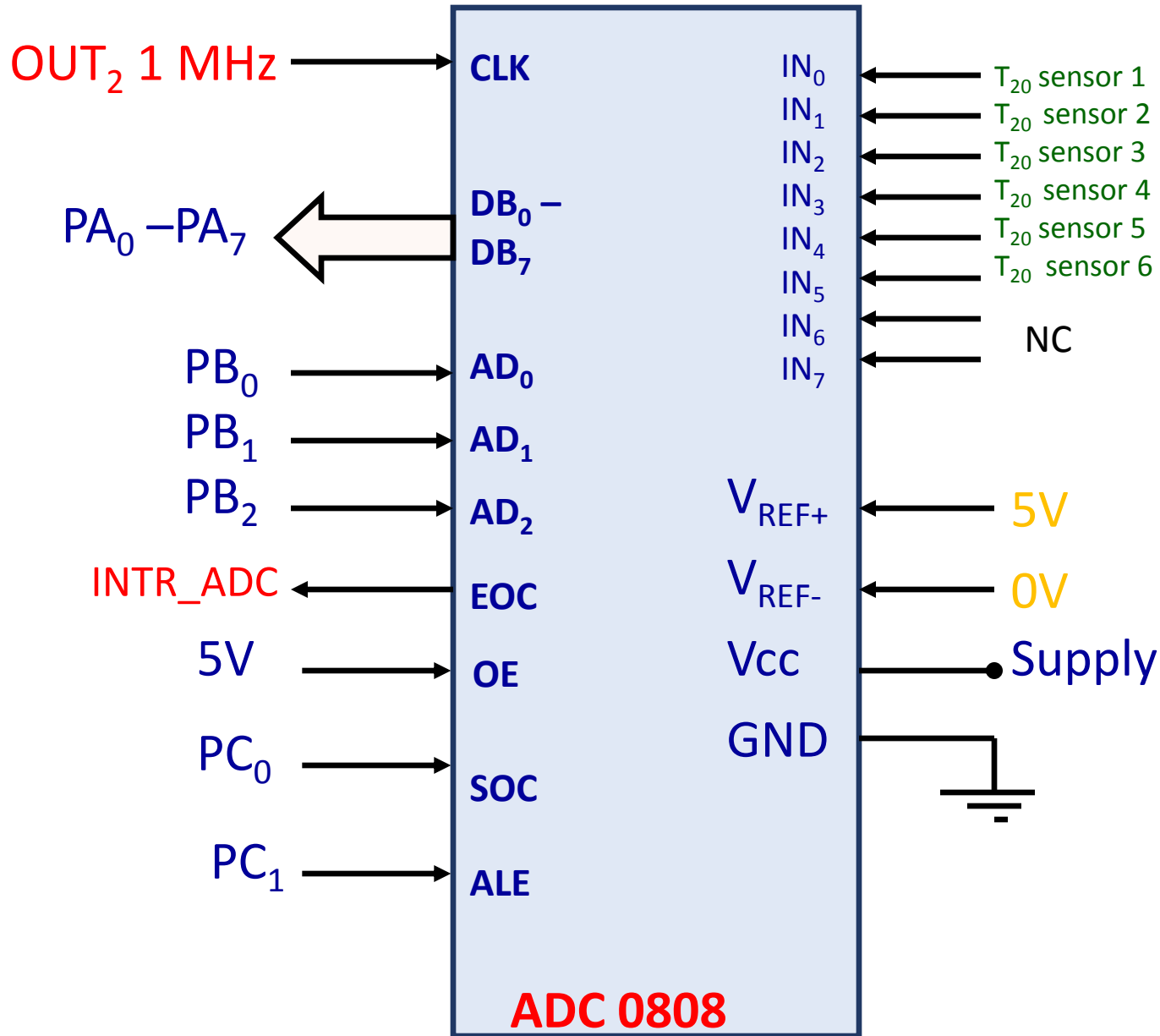
## 8255 Interface to the processor



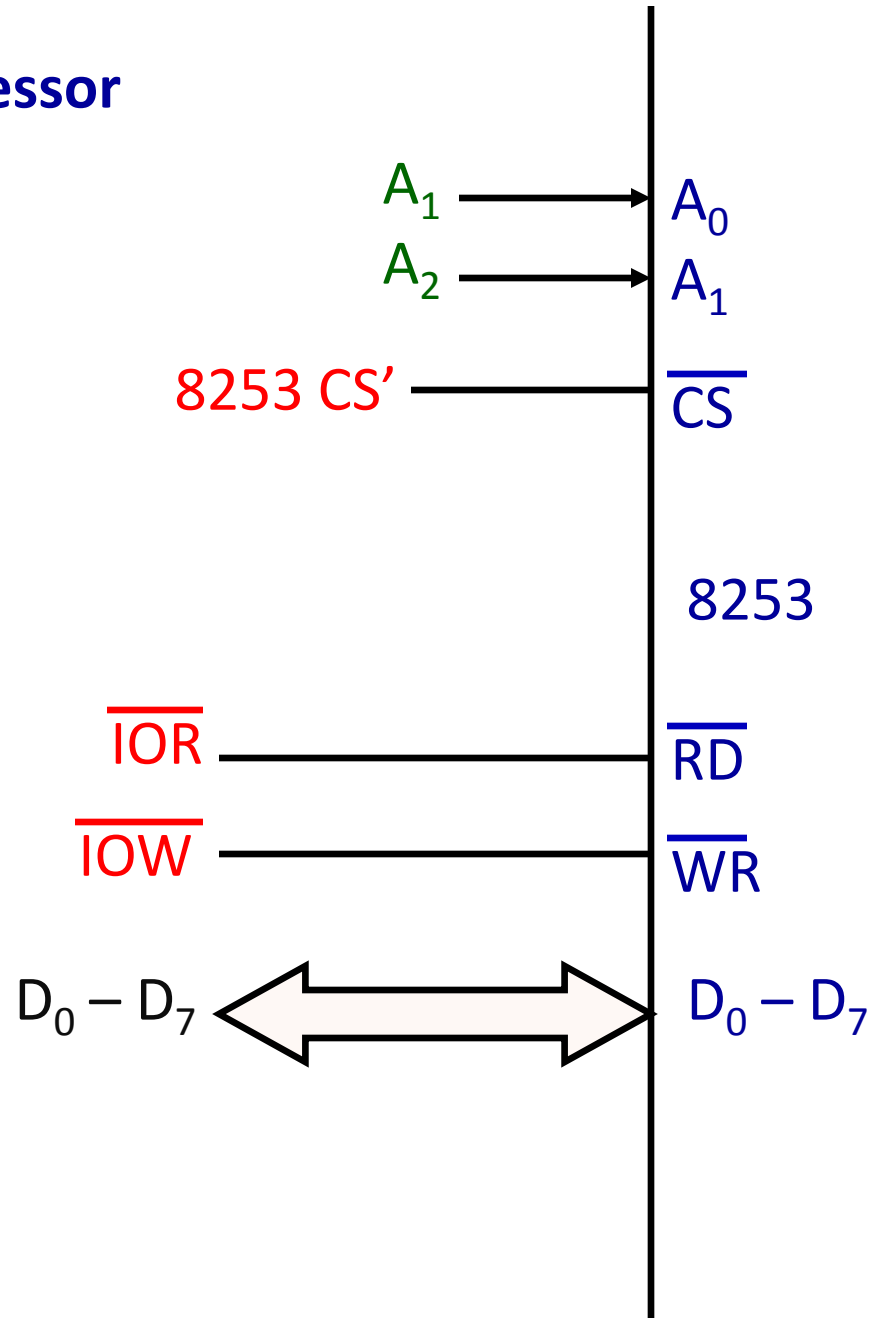


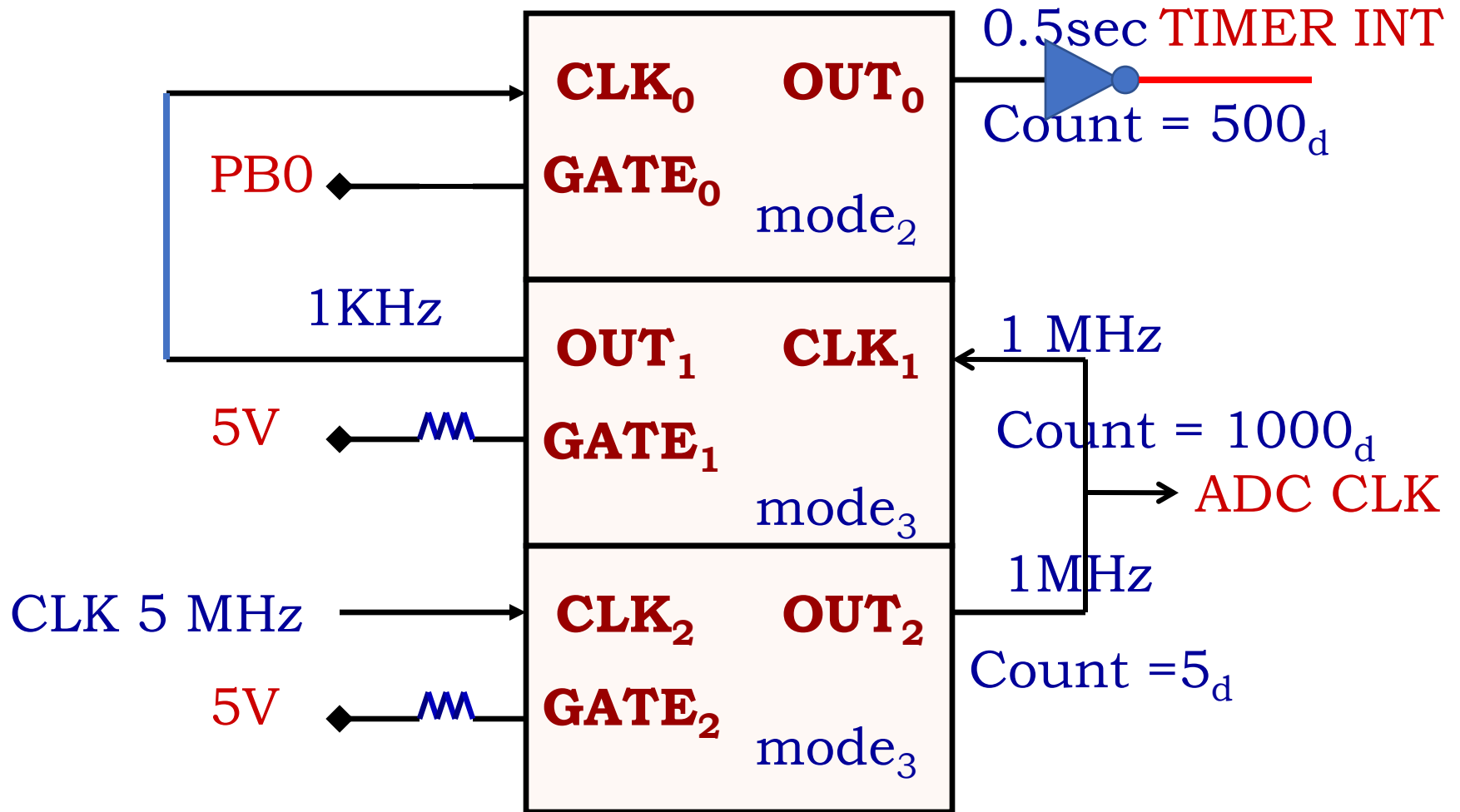
Common anode 7-segment display



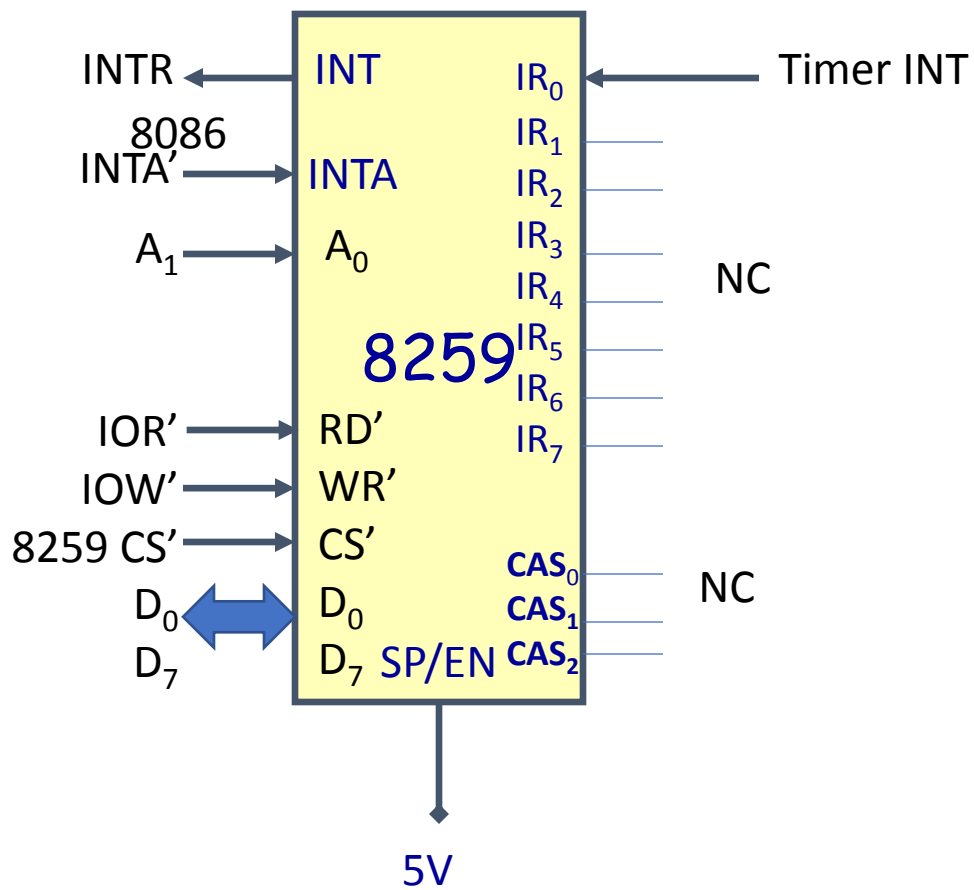


## 8253 Interface to the processor



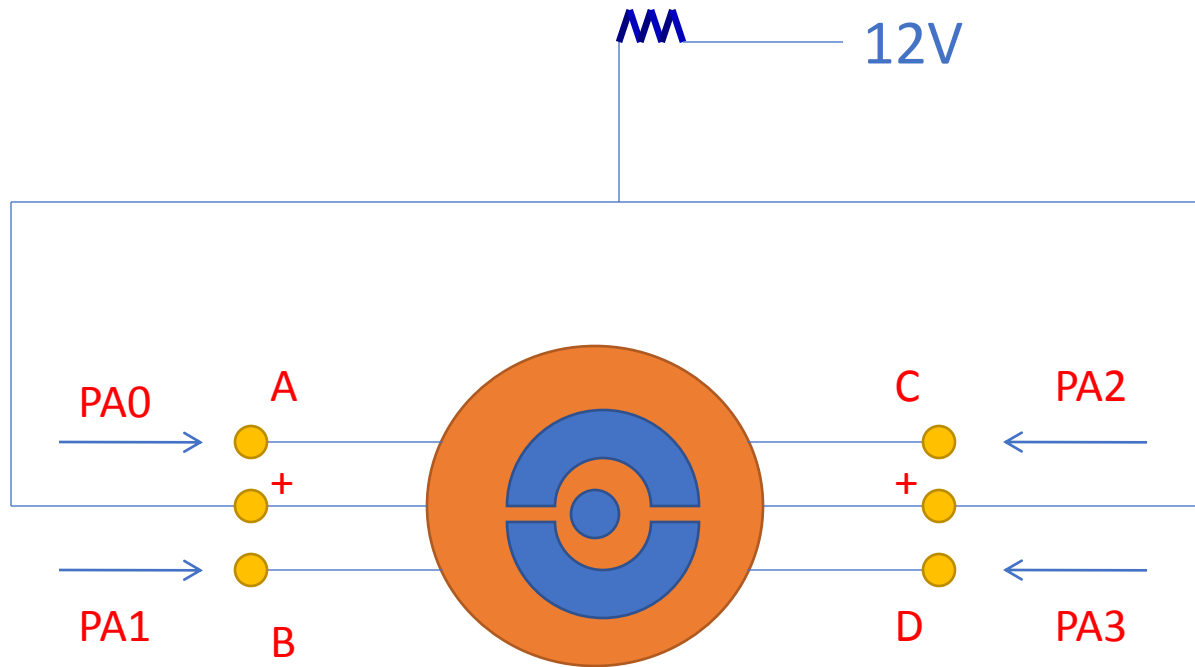


8253



8259





**UNIPOLAR STEPPER MOTOR**

## Keypad I/f

