Title : Write a assembly language program for memory block transfer (i) Internal to internal memory

EXPERIMENT NO. – 5

TITLE: assembly language Program

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(ii) Internal to external memory

Objective : (i) To learn implementation of simple 8051/8951 programs.

(ii) Study of simulator software of 8051/8951.

(iii) Study of instruction set of 8051.

Input : (i) Computer with simulator software of 8051.

Theory : (i) Algorithm of simple programs

1. Initialize two blocks with starting addresses.

2. Take length of blocks in register as counter.

3. In non overlapping method, transfer the contents from starting locations.

4. Transfer the contents from block1 location to block2 location.

5. Increment the addresses of two blocks, decrement the counter and check

   Whether it is zero, if not go to step 4.

6. In overlapping method, transfer the contents from the bottom locations.

4. Transfer the contents from block1 location to block2 location.

5. Decrement the addresses of two blocks, decrement the counter and check

   Whether it is zero, if not go to step 4.

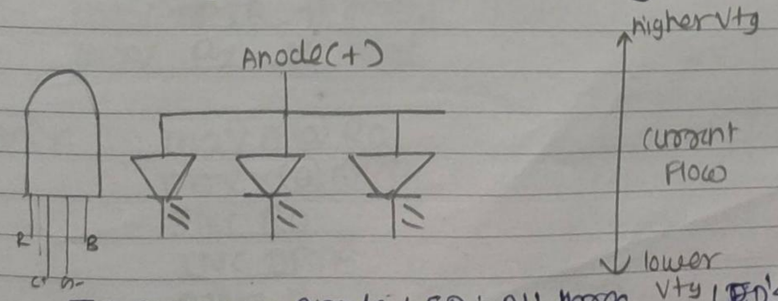
(ii) Flowchart of the above programs.

conclusion :- In this practical we have studied about write assembly language programs for memory block transfer internal to internal memory & external to internal memory.

7FH	Scratch pad RAM	2F	7F	78
		2E	77	70
30H		2D	6F	68
2FH	Bit addressable RAM	2C	67	60
		2B	5F	58
20H		2A	57	50
1FH	Register Bank 3	29	4F	48
		28	47	40
18H		27	3F	38
17H	Register Bank 2	26	37	30
		25	2F	28
10H		24	27	20
0FH	Register Bank 1	23	1F	18
		22	17	10
08H		21	0F	08
07H	Register Bank 0	20	07	00

Bit Addressable

2 Draw & explain common anode & common cathod configuration of interfacing LED.



In commone Anode LED : All three LED's share a positive connection (Anode)