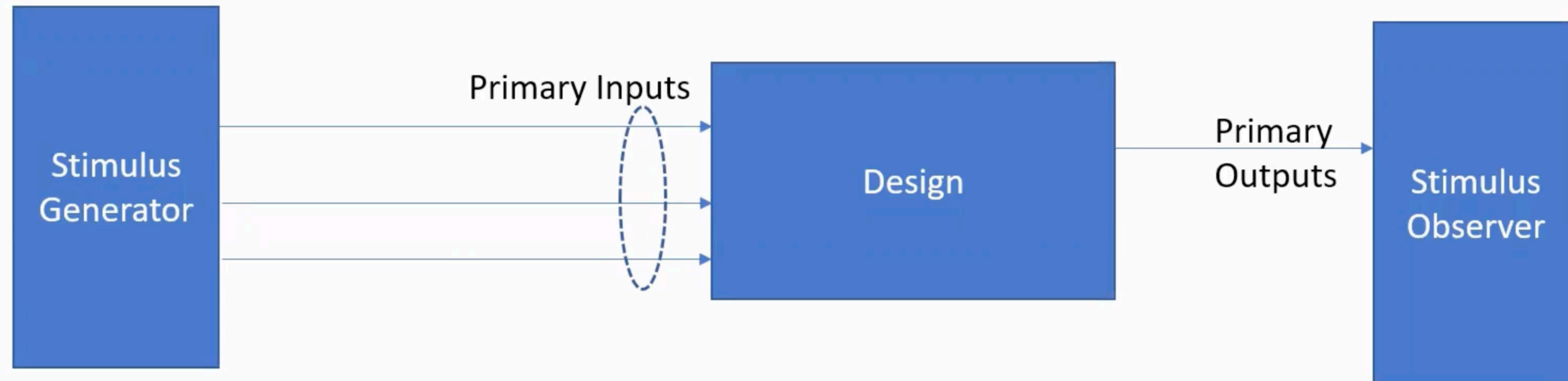


## TEST BENCH



### NOTE :

- Design may have 1 or more Primary Inputs , 1 or more Primary outputs
- TB Doesn't have a Primary input or Primary outputs