

UNIT - 2

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DATA LINK LAYER.

Data link layer: ~~Design issue / Services / Functions~~

- Services provided to Network layer
- Frame Synchronization
- Error control
- Flow control
- ~~Adr~~ Addressing
- Data and control on same link
- link Mgt Management (connection establishment / use / termination)
- Access Mgt Management. (MAC), (NIC)
 - ↓
 - medium access control

* * SERVICES PROVIDED TO NETWORK LAYER

- ① Unacknowledged Connection less Services.
- ② Acknowledged connection less services.
- ③ Acknowledged connection oriented Services.

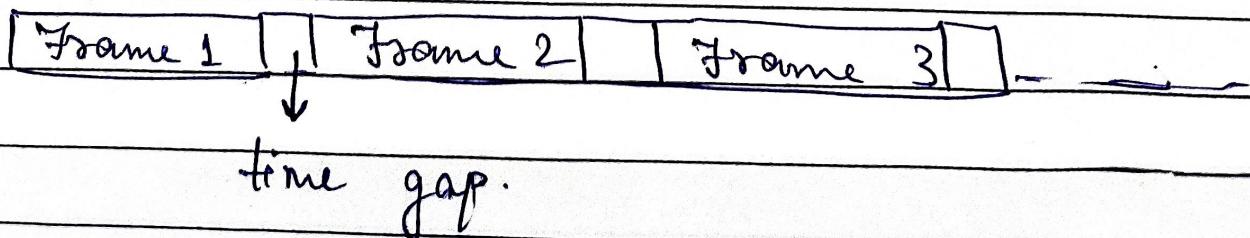
* * FRAMING METHODS -

- ① Character Count
- ② Starting and Ending Character with character stuffing.

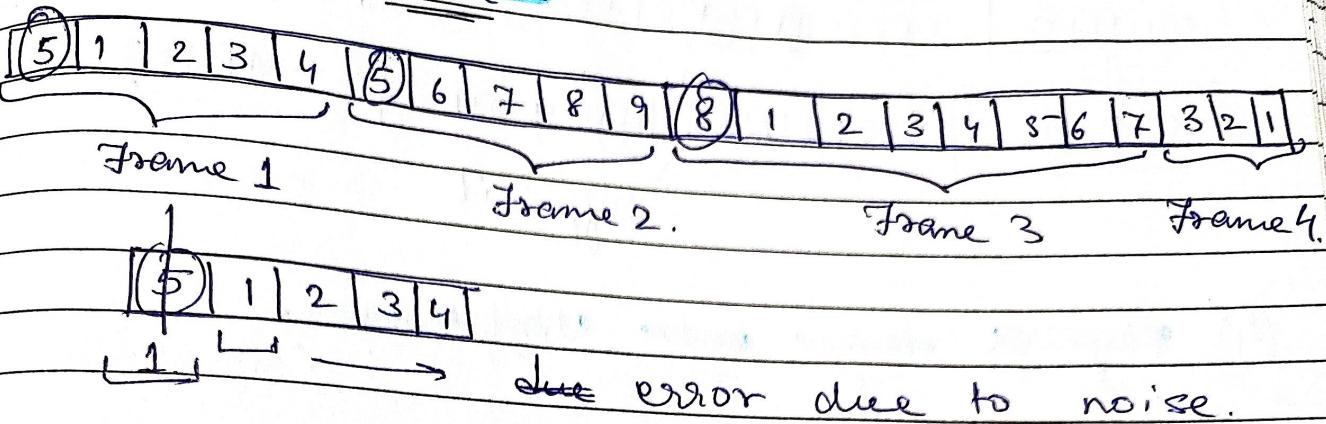
Starting and ending Flag with Bit Stuffing.

Physical layer coding violation.

Time gap method failed. (it was not efficient).

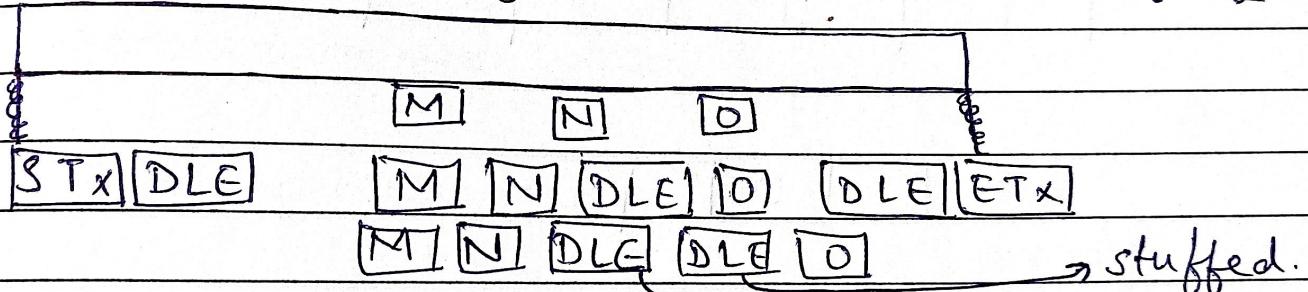


① Character Count -



Here, Frames are defined by the first character.

② Starting and ending char. with char. stuffing -

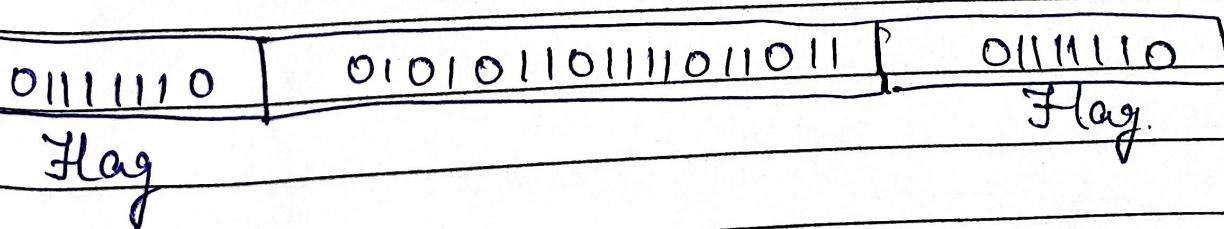


STx - Starting of text → part of data

DLE - Data link escape

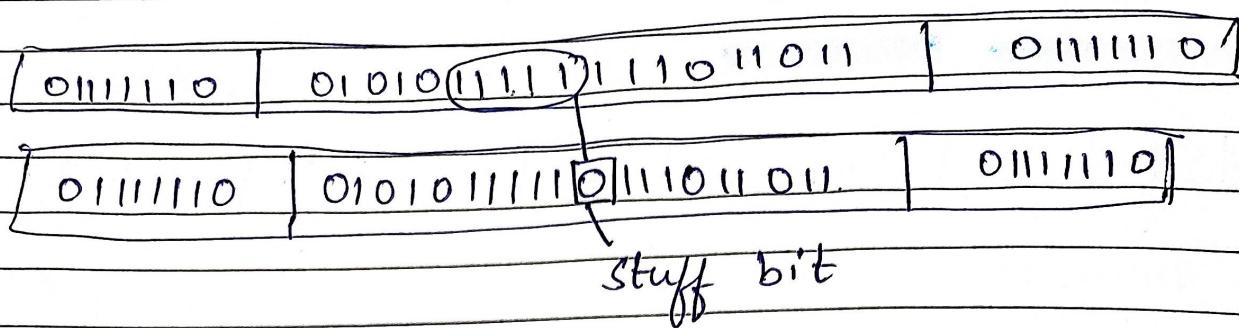
ETx - Ending of text.

Starting and ending Flag with Bit Stuffing -

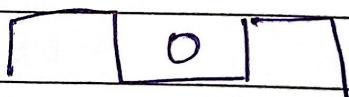
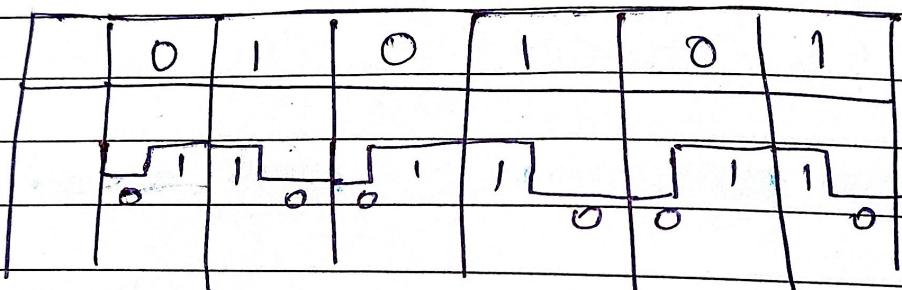


6 consecutive 1 → Flag.

5 consecutive 1 → we will stuff 0 → stuff



(4) Physical Layer Code Violations - (Manchester)



~~ERROR CONTROL~~

t t t **ERROR** CONTROL

- (1) Single Bit **ERROR**
- (2) Burst error.

e.g.,

original 1 0 0 1 0 1

1 0 0 **0** 0 1 → error.

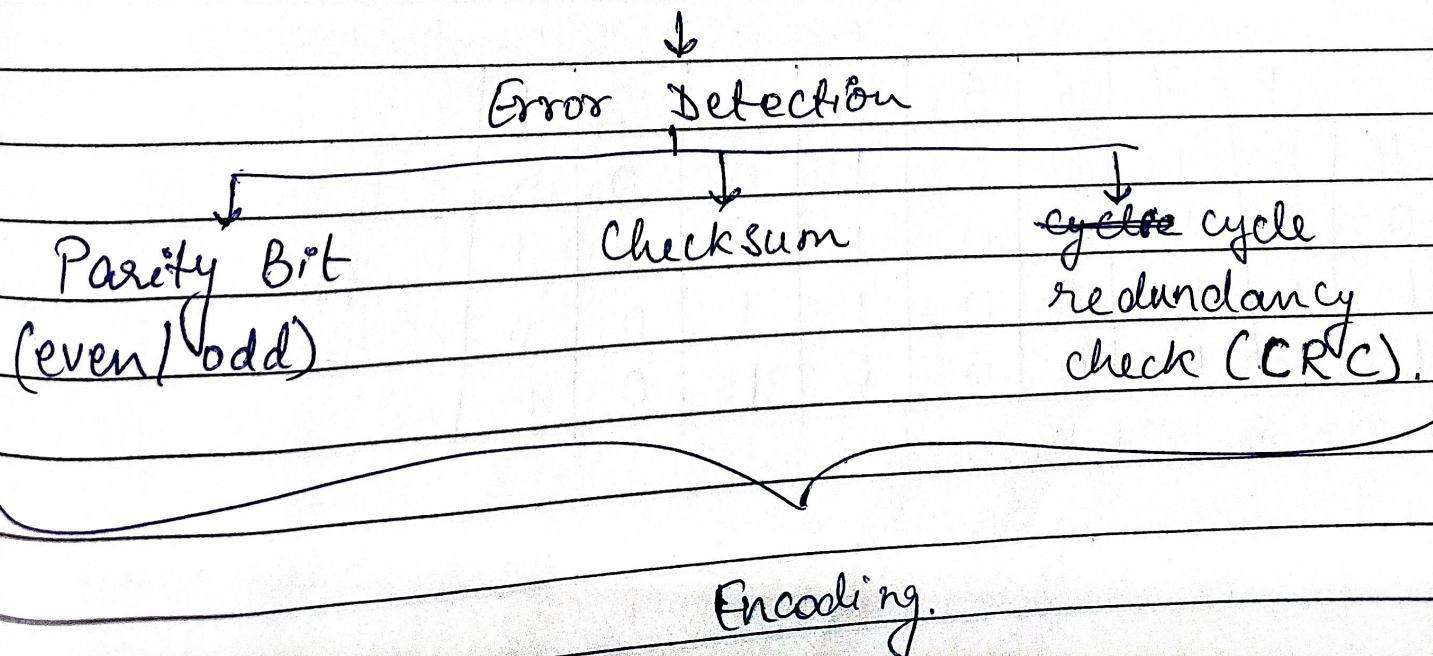
1 0 **1 0** 0 1 → error.

Acknowledgement can be +ve or -ve.

→ Error detection

→ Timer.

t **Flowchart:** Error Control



PARITY BIT

(1,0)

P	0	1	0	0	1	1	0
---	---	---	---	---	---	---	---

K → 7 bit →

if no. of 1 in the data is even then
 $P = 1$.

if no. of 1 in the data is odd then
 $P = 0$.

Can only identify 1 bit error.

Parity

Priority Method (even / odd)

↓ LRC

↓ VRC

Horizontal / longitudinal
redundancy check.

Vertical redundancy check

	P	7	6	5	4	3	2	1
H	0	1	0	0	1	0	0	0
O	1	1	0	0	1	1	1	1
L	1	1	0	0	1	1	0	0
E	0	1	0	0	0	1	0	1

even bit parity.

tt LRC / VRC -

(Two dimension priority check)

	C	O	M	P	U	T	E	R	LRC priority bit
b ₁	1	1	1						1
b ₂	1	1	0	0	1	0	1	0	1
b ₃	0	1	1	0	0	0	0	1	0
b ₄	0	1	1	0	1	1	1	0	0
b ₅	0	0	0	1	1	1	0	0	01
b ₆	0	0	0	0	0	0	0	1	01
b ₇	1	1	1	1	1	1	1	1	01
VRC bit (even parity)	0	0	01	10	10	0	0	0	0

t Codeword = original data + check sum.

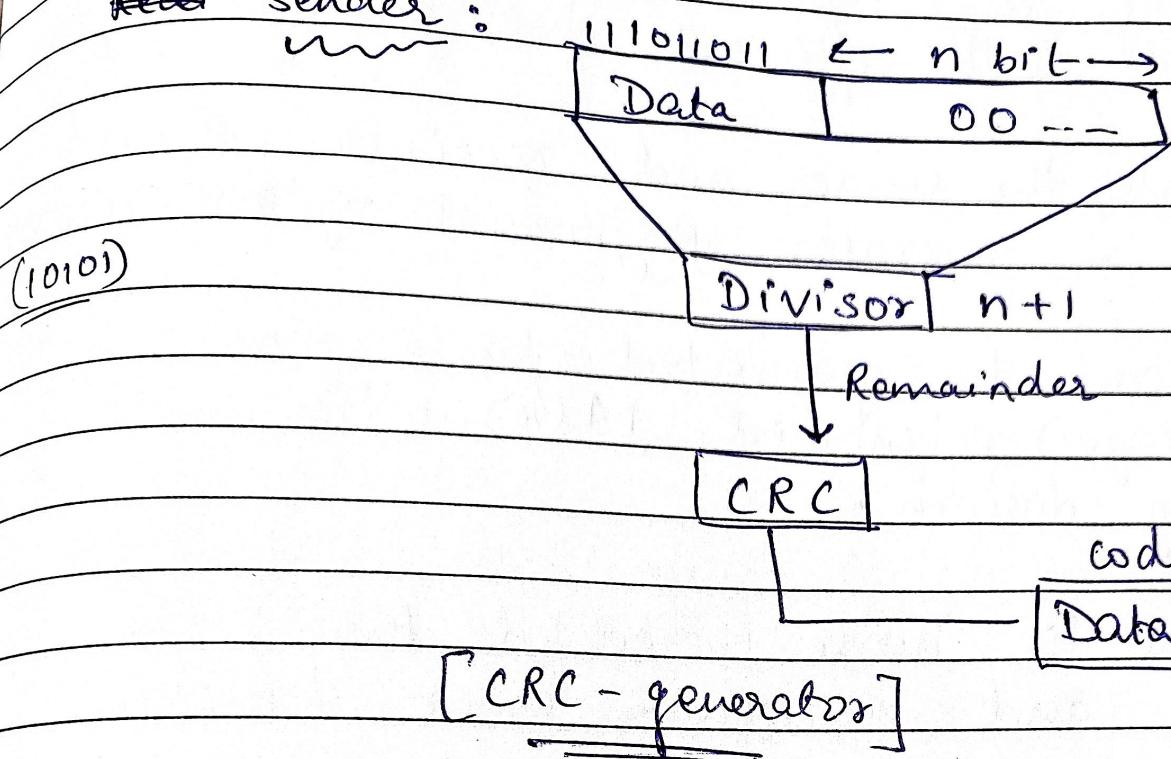
tt CHECKSUM

Codeword = Original Data + Checksum.

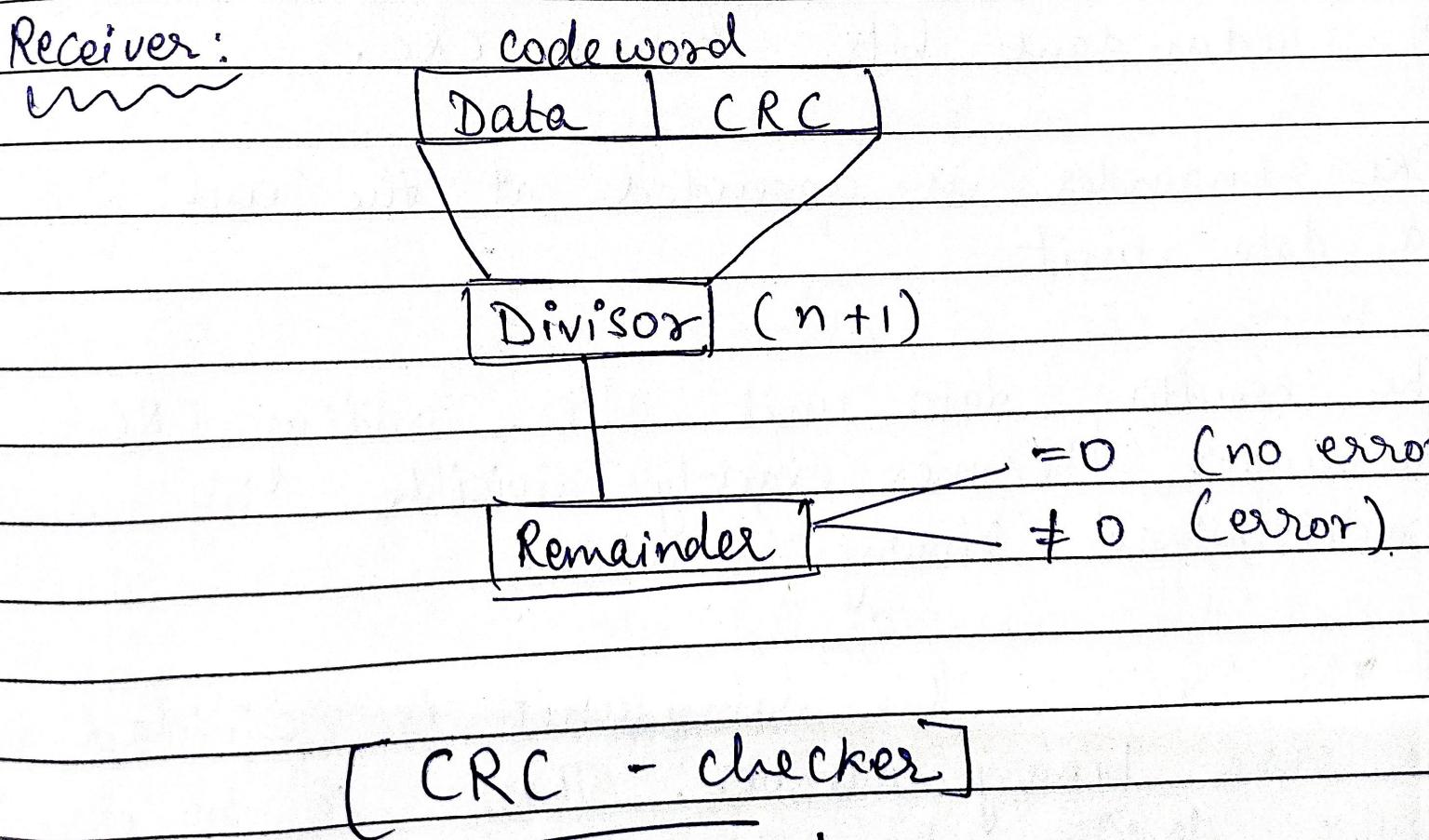
- t If m bit checksum is used, the data unit to be transmitted is divided into segments of m bits.
- t All the m bit segments are added.
- t The result of the sum is then complement using 1's complement arithmetic.
- t The value so obtained is called as check

CYCLE REDUNDANCY CHECK - CRC

Recd Sender:



Receiver:



Polynomial generator

- # +
t CRC is a type of polynomial code in which a bit string is represented in the form of polynomials with coefficient of 0 and 1 only.
- Pro
t For CRC code, the sender and receiver should agree upon a generator polynomial $g(x) \cdot g(x)$.
- t A codeword can be generated for a given dataword (message) polynomial $M(x)$ with the help of long division.
- a)
t This technique is more powerful than the parity check and checksum error detection.
- b)
t CRC is based on binary division, a sequence of redundant bits called CRC.
- t CRC remainder is appended at the end of a data unit.
- f The resulting data unit after adding CRC remainder becomes exactly divisible by another predetermined binary number.
- t At the receiver this data unit is divided by the same binary number. There is no error if this division does not yield any remainder.

1) PROCEDURE

~~procedure to obtain CRC~~

2) Procedure to obtain CRC

The redundancy bits used by CRC are derived by following the procedure given below

- divide the data unit by predetermined divisor
- obtain the remainder. It is the CRC

3) REQUIREMENT OF CRC

A CRC will be valid if and only if it satisfies the following requirements -

- It should have exactly 1 less bit than divisor.
- Appending the CRC to the end of data unit should result in the bit sequence which is exactly divisible by the divisor.

CRC Generator -

- Append a string of n zeroes to the data unit where n is one less than the data unit no. of bits in the pre-decided divisor ($n+1$ bits long)
- Divide the newly generated data unit in Step 1 by the divisor. This is a binary division.
- The remainder obtained after the division in Step 2 is the end bit CRC.
- This CRC will replace the n zeroes appended to the data unit in Step 1, to get the code word to be transmitted as figure (Sender).

CRC checker -

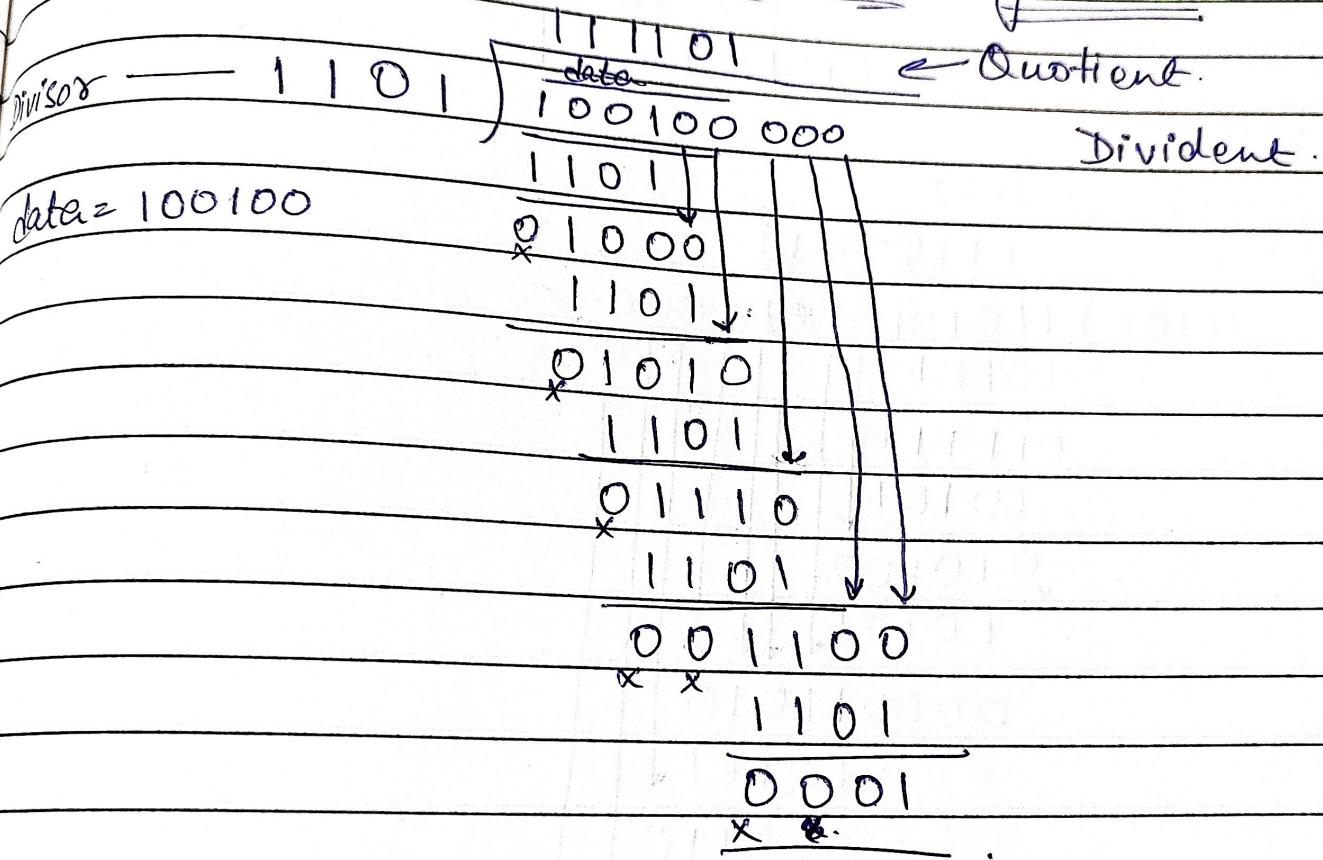
The codeword received at the receiver consists of data and CRC. The receiver treats it as 1 unit and divides it by the same $n+1$ bit division which was used at the transmitter. The remainder of this division is then checked. If the remainder is 0, then the received codeword is error free and hence should be accepted. But a non zero remainder indicates presence of error, hence the corresponding codeword should be rejected.

$$\left. \begin{array}{l} 0+1=0 \\ 0+0=0 \\ 1+0=1 \\ 0+1=1 \end{array} \right\} \text{Sub/Add in modulo -2 - division.}$$

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- A CRC checker functions exactly like a generator if it does the same modulo -2 - division.

Binary Division in a CRC generator



$$Q = 111101$$

$$R = 001$$

$$\boxed{100100001} \rightarrow \text{data} + R.$$

P.T.O.

Error Correction

FEC / FET

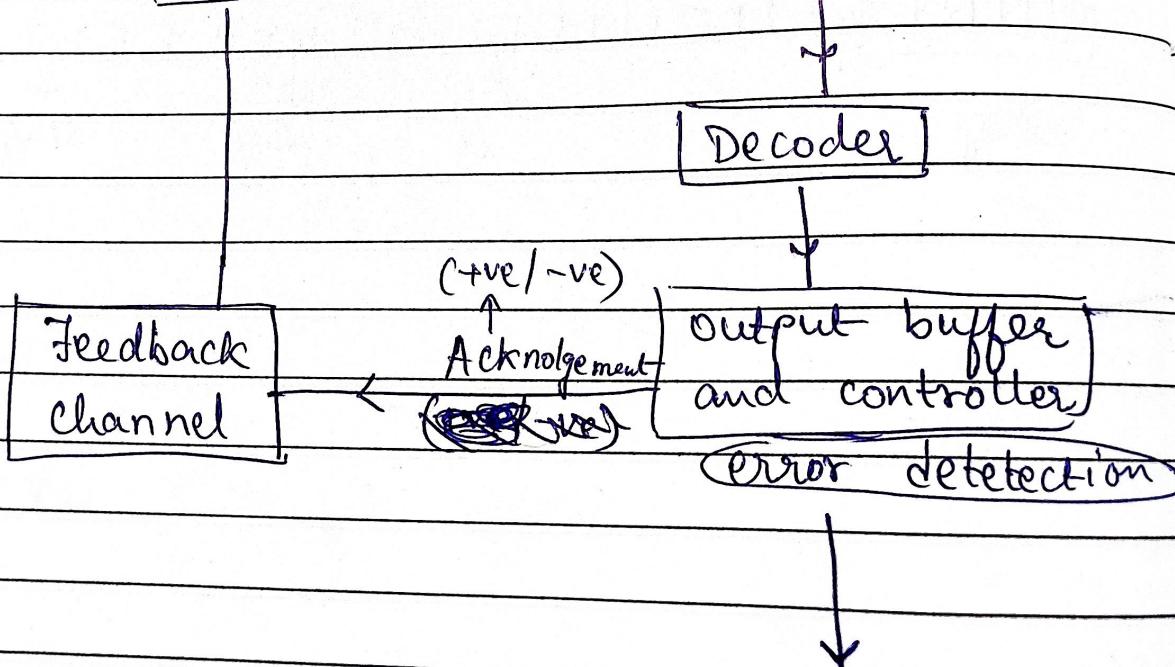
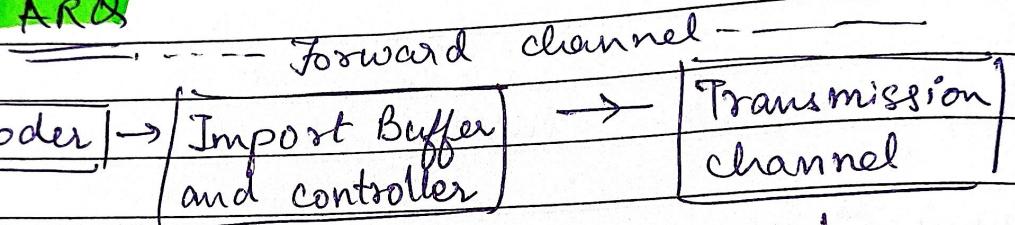
ARQ

Automatic Request for
Retransmission

Feed Forward
Technique / Forward
error correction

~~Message~~

ARQ



ARQ

Message output

ARQ.

↓
stop and
wait ARQ↓
Go back
n ARQ↓
Selective Repeat
ARQ.

~~ARQ (Automatic Request for Retransmission)~~

- Q. The code word is received as 1100100101011. Check whether there are errors in the received code word if the divisor is 10101.

$$\begin{array}{r}
 & 111110001 \\
 10101) & 1100100101011 \\
 & \underline{10101} \\
 & 011000 \\
 & \underline{1010} \\
 & 011010 \\
 & \underline{10101} \\
 & 01111 \\
 & \underline{10101} \\
 & 010100 \\
 & \underline{10101} \\
 & \hline
 & 000011011 \\
 & \underline{10101} \\
 & \hline
 & 01110
 \end{array}$$

Since $R \neq 0$, error is there

*** ARQ -~~*** AN ELEMENTARY DATA LINK~~

* There are 2 basic systems for error detection and correction. The first one being the forward error correction system (FEC) and second one is the ARQ (Automatic repeat request).

* In the ARQ system of error control, when an error is detected, a request is made for the retransmission of that signal. Therefore a feedback channel is required for sending the request for retransmission.

* The ARQ system differ from the FEC system in 3 important respects. They are as under-

(i) In ARQ system, less no. of checkbits (parity bits) are required to be sent. This will increase (K/N ratio) for an (n, k) block code if transmitted using the ARQ system.

return

(ii) A written transmission path or additional hardware in order to implement repeat transmission of codeword will be required.

(iii) The bitrate of forward transmission must make allow for the backward repeat transmission.

Working system operation of ARQ system

The encoder produces codewords for each message signal as its input. Each codeword at the encoder output is stored temporarily and transmitted over the forward transmission channel. At the destination, a decoder will decode the codewords and look for errors. A decoder will output a +ve acknowledgement if ~~there~~ & no errors are detected and it will output a -ve acknowledgement if error is detected. On receiving a -ve acknowledgement via the return transmission path, the controller will transmit the appropriate word from the words stored by the input buffer. A particular word may be retransmitted only once, or it may be retransmitted twice or more no. of times.

The output controller and buffer on the receiver side assemble the output bit stream from the codewords accepted by the dec-

Error Probability on the return path

The bitrate of the return transmission which involves the return transmission of acknowledgement is low as compared to the bitrate of the forward transmission. The error probability of the return transmis-

is negligibly small.

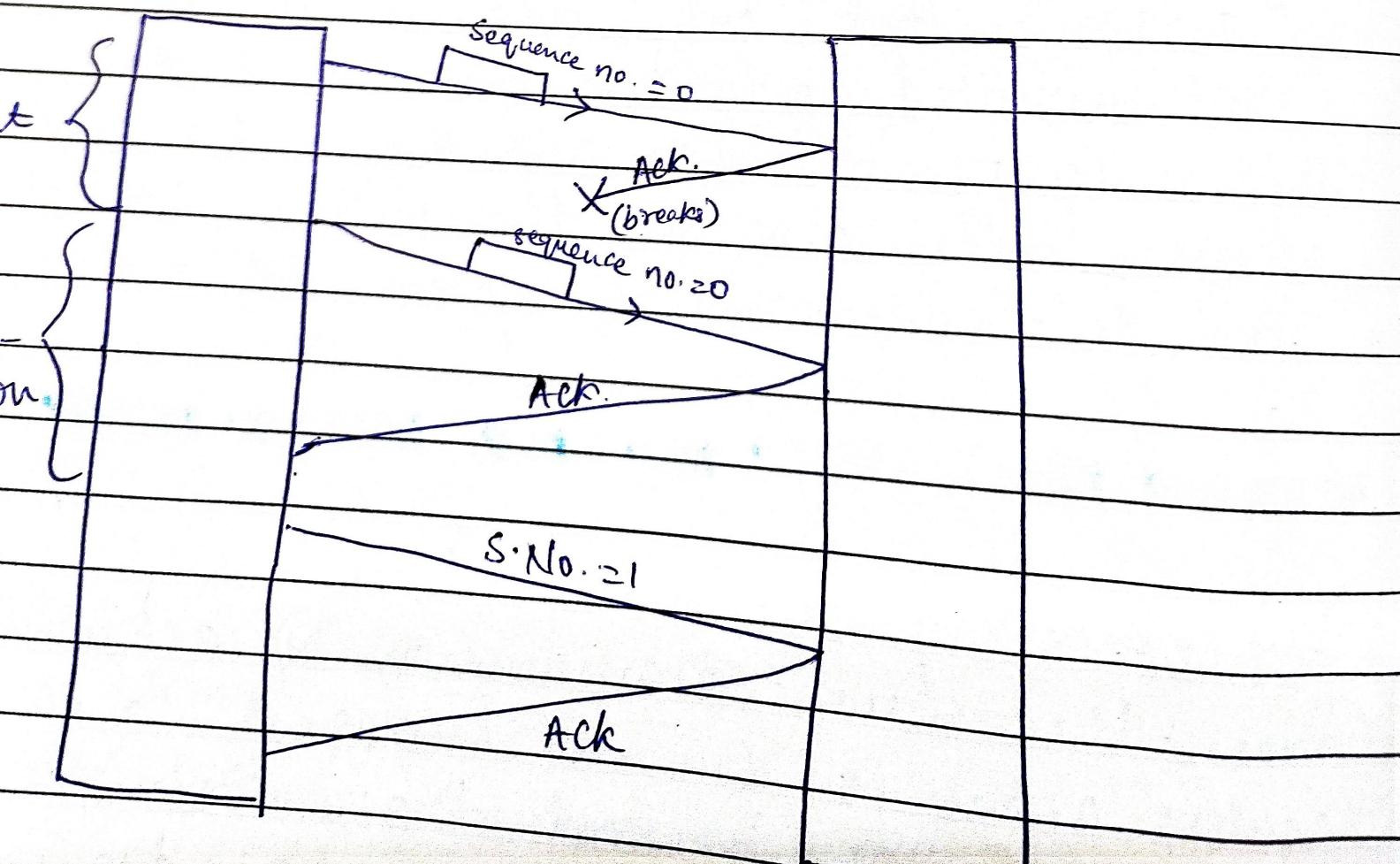
AN ELEMENTARY DATA LINK LAYER

PROTOCOL

- ① An unrestricted simple protocol
- ② Simplex Stop and wait protocol
- ③ A ~~stop~~ simplex protocol for noisy channel
- ④ Piggy backy concept

Sender

Receiver



- + The transmission of data needs to be bi-directional. This is called full duplex transmission.
- + One way of achieving full duplex transmission is to have 2 separate channels, one for forward data transfer and other for reverse data transfer but this will waste the bandwidth of the reverse channel almost entirely. A better solution would be to use each channel (forward and reverse) to transmit frames both ways, the receiver waits with both channels having the same capacity.
- + Let A and B be the user then the data frames from A and B are intermixed with the acknowledgements from A to B, one more improvement can be made when the data frame arrives, the receiver waits, does not send a control frame (acknowledgement back immediately. The receiver waits until its network layer ~~process~~ passes it in the next data packet. The ack. is then attached to this outgoing data frame. This technique in which the outgoing acknowledgement is delayed

delayed temporarily is called piggy back.

+ Advantages:

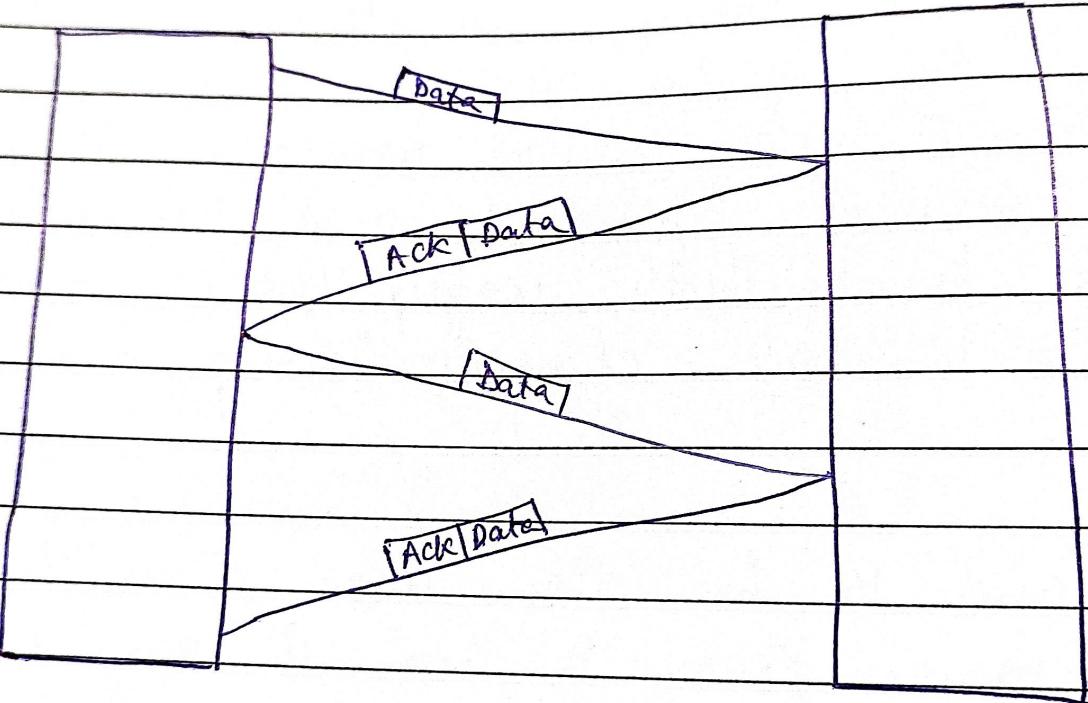
Better use of available channel bandwidth

+ Disadvantages:

Increase overall system complexity.

Sender

Receiver



What is HDLC (High Level Data Link Control Protocol) -

- + Developed by ISO.
- + It is the most widely accepted data link protocol.
- + It offers adaptability, a high level of flexibility, reliability and efficiency of operation.
- + HDLC is a bit oriented data link protocol and it satisfies a variety of data control requirements.
- + To make HDLC protocol applicable to various possible network configurations, 3 types of stations have been defined -
 - (a) Primary stations
 - (b) Secondary stations
 - (c) Combined stations

A primary station looks after the data link management. In the event of communication b/w the primary and secondary station. The primary station has a responsibility of connecting and disconnecting to data link. The frames sent by the

point to point config. → 1 sender - 1 receiver
point to multipoint config. → 1 sender - many receivers

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primary station are commands.

t **Secondary station** - Secondary station operates under the control of a primary station. The frames sent by secondary stations are called responses.

t **Combined Stations** - It can act both as a primary and as well as secondary station therefore it can issue both command and responses.

OPERATING MODES FOR DATA TRANSFER

t In HDLC both synchronous and asynchronous modes of communications are permitted. Following modes of operation are responsible for data transfer -

- (i) Normal Response Mode (NRM)
- (ii) Asynchronous Response Mode (ARM)
- (iii) Asynchronous Balance Mode (ABM)

~~ASYNCHRONOUS RESPONSE MODE (NRM)~~ -
NRM -

This mode is suitable for point to point and point to multipoint configuration. Here the primary station will control the overall data link management. It is a synchronous

mode of communication

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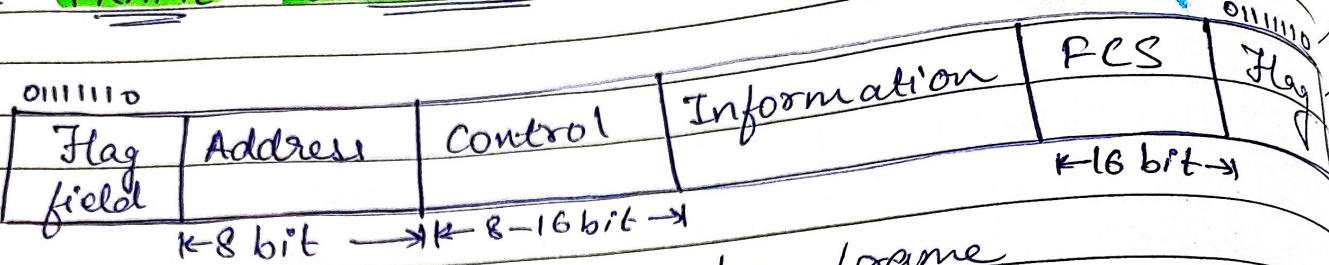
ARM

- In ARM, the secondary station can transmit the response (in form of frame) without taking permission from the primary station. This is not allowed in NRM. Therefore NRM is a more disciplined mode than ARQ. The responsibility of link management system still lies with the primary station.

ABM

- This mode is applicable to the point to point communication b/w 2 combined stations. As both these stations are combined stations, they are also capable of link management functions. As the communication is asynchronous, one station can transmit a frame without permission from the other station, in this mode information frames can be transmitted in full duplex manner.

Frame Structure in HDLC



(a) Information transfer frame



(b) Supervision and Unnumbered frame

Flag Field

* The flag is a unique 8 bit word pattern (0111110). It is used to identify the start and end of each frame.

Address Field

* It consists of the address of secondary station respective of whether a frame is being transmitted by primary or secondary station. It consists of 8 bits hence it is capable of addressing 256 addressing.

CONTROL FIELD

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It usually consists of 8 bit but the no. of bits can be extended to 16. It carries the sequence number of the frame, acknowledgement control, request for transmission and other commands and responses.

INFORMATION FIELD

This is a 16 bit field size of which this field is variable and it can consists of any no. of bits. It consists of the user's data bit and it is completely transparent.

FRAME CHECKSUM (FCS)

This is a 16 bit field which is used for detection of errors in the address, control and information field. It is nothing else but a 16 bit CRC code for error detection.

P.T.O.

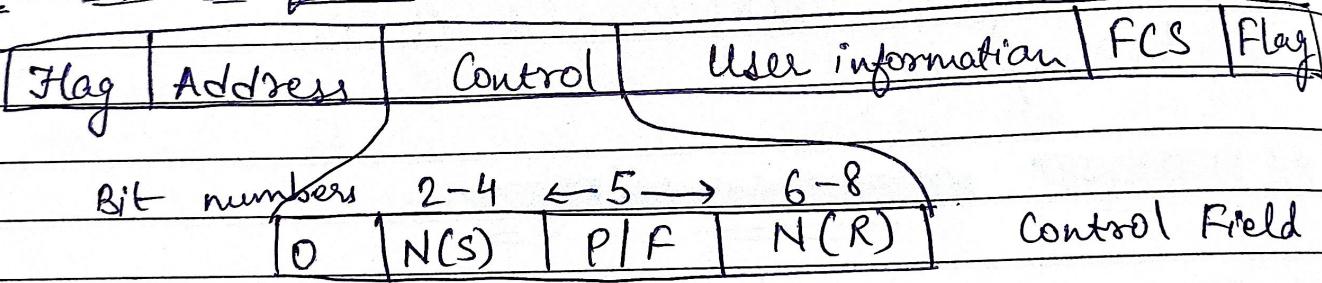
P/F = Poll / Final bit
N(S) = Sequence no. of frame sent.
N(R) = Sequence no. of next frame expected.

8 FRAME TYPES IN HDLC

There are 3 types of frames defined under HDLC. They are -

- ① The I frame or Information frame (data)
- ② The S frame or Supervisory frame (control)
- ③ The U frame or Unnumbered frame (control)

① The I frame



I-frame
8 bit

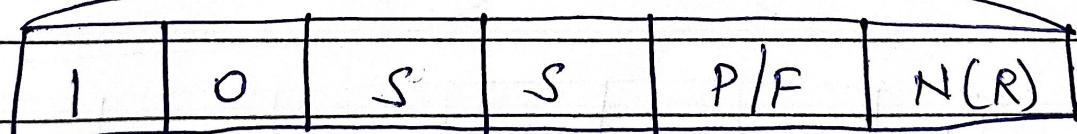
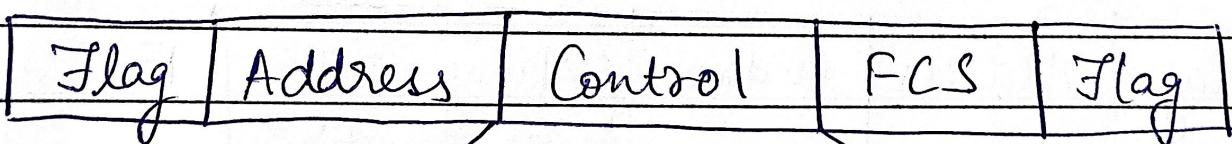
It is supposed to carry the user data from the network layer. It is also possible to include the flow and error control information which is also called piggybacking.

It is the first bit in the control field

is zero it is defined as an information frame. The next three bits 2-4 are called N(S) and they are used to define the sequence no. of the frame.

The next bit 5 is Pole/Final. It can have two value 0 & 1 out of which it has meaning only when $P/F = 1$, it means pole when a frame is sent by a primary station to secondary and if $P/F = 0$, it means frame is sent by the secondary station to primary station. When $P/F = 1$ it store address of secondary station and vice-versa. The last 3 bit 6-8 define the N(R) field. It is usually for piggy backing. The three bits in N(R) field correspond to the acknowledgement when piggy backing is used.

② The S frame -



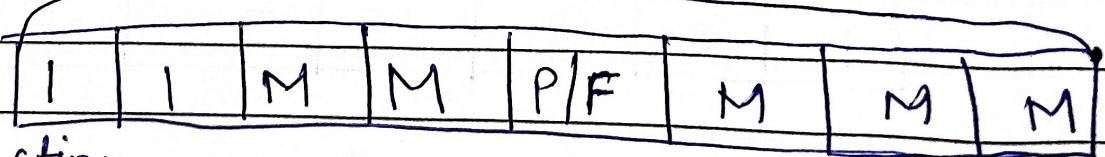
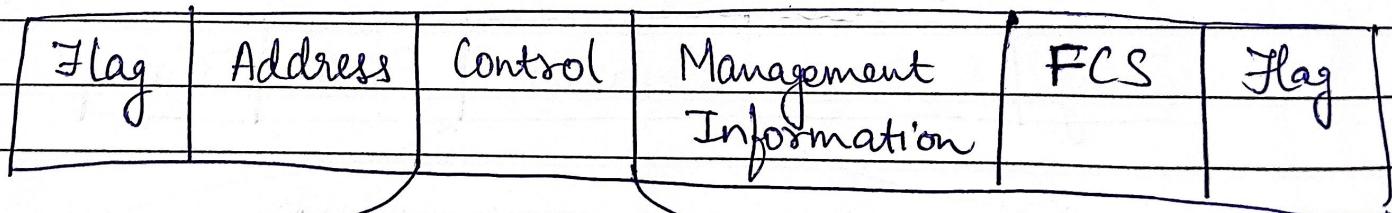
The S frame does not contain any information field. These frames are used for flow and error control. When piggybacking

is not possible, first two bits (0,1) of the control fields identify it as supervisory frame the next two bits define the code field marked SS.

There are 4 types of supervisory frames corresponding to the 4 possible value of the S bits in the control field.

- 1) SS 00 \Rightarrow Corresponds to receive ready RR frames which are used to acknowledge frames when no I-frame are available to piggyback the acknowledgement.
- 2) SS 01 \Rightarrow Corresponds to reject (REJ) frames which are used by the receiver to send a CNAK when error has occurred.
- 3) SS 10 \Rightarrow Correspond to receive not ready (RNR) frame and it is used for flow control.
- 4) SS 11 \Rightarrow Corresponds to selective repeat frame which indicates to transmitter that it should retransmit the frames indicated in the N(R) sub field.

③ The U Frame -



- \rightarrow 32 - Function
- \rightarrow Initialization
- \rightarrow Status reporting
- \rightarrow Resetting

\rightarrow Management information is main function

DATA LINK LAYER IN THE INTERNET

① **SLIP**

② **PPP**

Serial line Internet Protocol (1989)

Point to point

Flag	Address	Control	Protocol	Data and padding	FCS	Flag
8 bit	8 bit	8 bit	1 to 2 Byte 8 to 16		2 or 4 bytes	1 byte

PPP

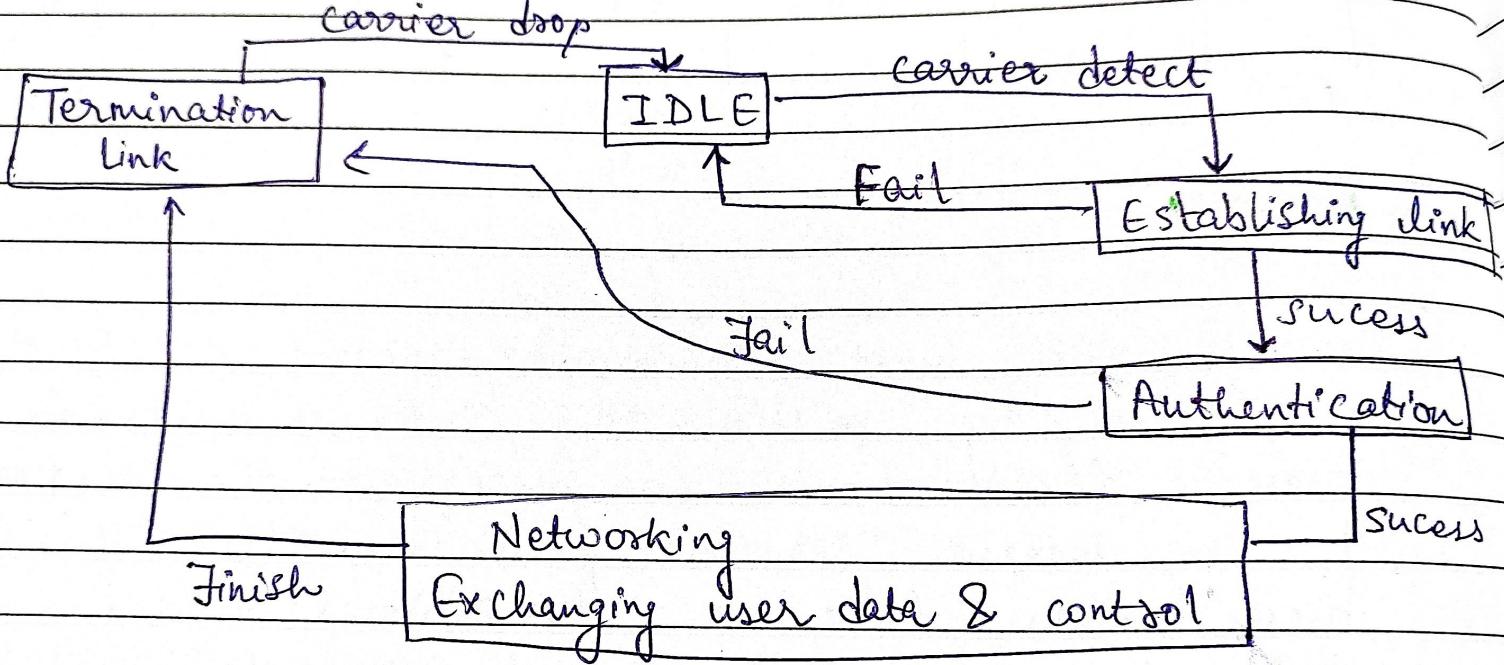
One of the most common protocol is PPP is used by millions of internet users to connect their home computers to the server of an internet service provider. Most of these users have a traditional model and they are connected to the internet through a telephone line or a TV cable. The PPP is used for controlling and managing the data transfer.

Service provided by PPP -

- 1) defines the format of the frames
- 2) it defines how the link b/w 2 devices is to be established and how the data exchange should take place.

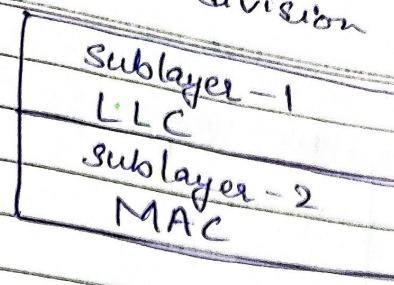
- 3) It decides the encapsulation of network layer data into the data link frame.
- 4) it defines the way in which the two devices can authenticate each other.

Transition State Diagram of PPP



- * FDMA - Frequency division multiple access.
- * TDMA - Time division multiple access.
- * CDMA - Code division multiple access.

DD1



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logical link control

Medium access control

~~BB~~ ~~BB~~

- ① Static channel Allocation
- ② Dynamic channel Allocation

have dedicated communication path

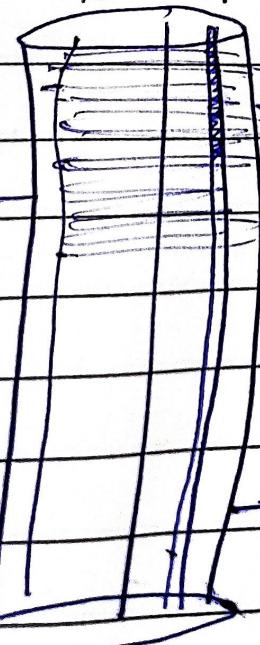
- Frequency division multiplexing (FDM)
- Time division multiplexing (TDM)
- Code division multiplexing
- Wave division multiplexing (WDM)

static

ttt \rightarrow FDMA -

$F_{10} - F_1$

(10 - 200 freq. for this eg.).



$S_1 - 10 - F_1$ [Freq. = 100, Time = 20]

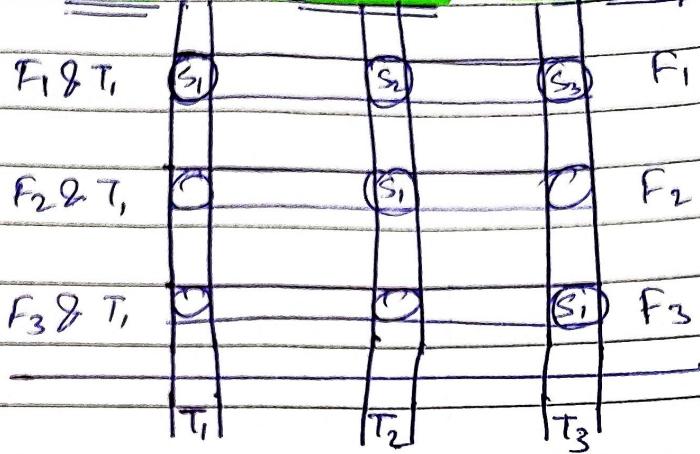
Freq.
15
1413

S_{10}

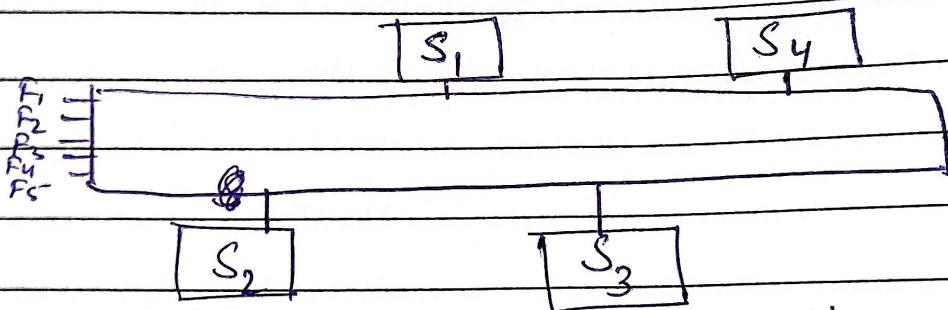
In FDMA, we have 100% Time.

+++ TDMA (we have 100% Frequency but limited time)

+++ Code Division Multiplexing



+++ CODE DIVISION MULTIPLEXING



Static Channel allocation.

S_1 use F_1 line

S_2 use F_2 line & so on - - -

Multiple Access Method

random Access

PURE ALOHA

SLOTTED ALOHA

CSMA
(carrier multiple sense access)

CSMA / CD
(collision detection)

CSMA / CA
(collision avoidance)

Collision Free Protocol
(Bit Map protocol)

Control access

performance
- 18%.
- 36%.

Token passing
polling

channelization
FDMA
TDMA
CDMA

PURE ALOHA

Frames are transmitted at arbitrary time.

$$\text{Throughput (S)} = G \times e^{-2G}$$

The max utilization is about 18.4%.

Global time is not required.

SLOTTED ALOHA

Time is divided up into discrete slots, the frame is sent at start of slot.

$$\text{Throughput (S)} = G \times e^{-G}$$

The max utilization is about 36.8%.

It requires Global

~~CSMA (Carrier Sense Multiple Access)~~

→ Non-Persistent

→ I-Persistent

→ P-Persistent

PURE ALOHA

† Simple to implement.

† Cannot use for satellite due to very low utilization.

SLOTTED ALOHA

time for synchronization as it is divided up into discrete slot.

Implementation is complex due to synchronization of all node or station.

It is used in broadcast satellite.