

	Data Port	Clock Port	Rise	Fall	Clock Edge	Clock Reference
1	ClockDIV	CLK1	5.366	5.345	Rise	CLK1
2	Instr[*]	CLK1	7.938	7.903	Rise	CLK1
1	Instr[0]	CLK1	8.074	8.055	Rise	CLK1
2	Instr[1]	CLK1	8.498	8.510	Rise	CLK1
3	Instr[2]	CLK1	8.387	8.336	Rise	CLK1
4	Instr[3]	CLK1	8.192	8.156	Rise	CLK1
5	Instr[4]	CLK1	8.087	8.072	Rise	CLK1
6	Instr[5]	CLK1	8.185	8.152	Rise	CLK1
7	Instr[6]	CLK1	7.938	7.903	Rise	CLK1
8	Instr[7]	CLK1	8.699	8.689	Rise	CLK1
9	Instr[8]	CLK1	9.302	9.278	Rise	CLK1
10	Instr[9]	CLK1	8.763	8.745	Rise	CLK1
11	Instr[10]	CLK1	9.913	9.917	Rise	CLK1
12	Instr[11]	CLK1	9.462	9.444	Rise	CLK1
13	Instr[12]	CLK1	9.142	9.122	Rise	CLK1
14	Instr[13]	CLK1	9.275	9.345	Rise	CLK1
15	Instr[14]	CLK1	8.839	8.829	Rise	CLK1
16	Instr[15]	CLK1	9.058	9.055	Rise	CLK1
17	Instr[16]	CLK1	9.104	9.154	Rise	CLK1
18	Instr[17]	CLK1	9.045	9.086	Rise	CLK1
19	Instr[18]	CLK1	9.477	9.486	Rise	CLK1
20	Instr[19]	CLK1	9.011	9.028	Rise	CLK1
21	Instr[20]	CLK1	9.729	9.740	Rise	CLK1
22	Instr[21]	CLK1	9.399	9.472	Rise	CLK1
23	Instr[22]	CLK1	8.971	9.058	Rise	CLK1
24	Instr[23]	CLK1	9.278	9.324	Rise	CLK1
25	Instr[24]	CLK1	10.374	10.437	Rise	CLK1
26	Instr[25]	CLK1	9.545	9.496	Rise	CLK1
27	Instr[26]	CLK1	8.962	8.943	Rise	CLK1
28	Instr[27]	CLK1	9.724	9.692	Rise	CLK1
29	Instr[28]	CLK1	9.648	9.726	Rise	CLK1
30	Instr[29]	CLK1	8.211	8.184	Rise	CLK1
31	Instr[30]	CLK1	8.379	8.387	Rise	CLK1
32	Instr[31]	CLK1	8.414	8.393	Rise	CLK1
3	Regout[*]	CLK1	10.699	10.636	Rise	CLK1
1	Regout[0]	CLK1	13.034	12.888	Rise	CLK1
2	Regout[1]	CLK1	13.213	13.124	Rise	CLK1
3	Regout[2]	CLK1	13.571	13.433	Rise	CLK1
4	Regout[3]	CLK1	13.689	13.543	Rise	CLK1

	<b>Data Port</b>	<b>Clock Port</b>	<b>Rise</b>	<b>Fall</b>	<b>Clock Edge</b>	<b>Clock Reference</b>
5	Regout[4]	CLK1	12.537	12.435	Rise	CLK1
6	Regout[5]	CLK1	12.639	12.587	Rise	CLK1
7	Regout[6]	CLK1	12.614	12.531	Rise	CLK1
8	Regout[7]	CLK1	12.556	12.472	Rise	CLK1
9	Regout[8]	CLK1	12.628	12.555	Rise	CLK1
10	Regout[9]	CLK1	10.699	10.636	Rise	CLK1
11	Regout[10]	CLK1	12.816	12.750	Rise	CLK1
12	Regout[11]	CLK1	12.444	12.372	Rise	CLK1
13	Regout[12]	CLK1	13.862	13.761	Rise	CLK1
14	Regout[13]	CLK1	13.568	13.439	Rise	CLK1
15	Regout[14]	CLK1	12.444	12.393	Rise	CLK1
16	Regout[15]	CLK1	13.266	13.155	Rise	CLK1
17	Regout[16]	CLK1	13.484	13.361	Rise	CLK1
18	Regout[17]	CLK1	11.845	11.810	Rise	CLK1
19	Regout[18]	CLK1	13.680	13.527	Rise	CLK1
20	Regout[19]	CLK1	11.794	11.743	Rise	CLK1
21	Regout[20]	CLK1	13.638	13.521	Rise	CLK1
22	Regout[21]	CLK1	13.692	13.613	Rise	CLK1
23	Regout[22]	CLK1	14.177	14.067	Rise	CLK1
24	Regout[23]	CLK1	13.802	13.687	Rise	CLK1
25	Regout[24]	CLK1	13.010	12.851	Rise	CLK1
26	Regout[25]	CLK1	12.912	12.733	Rise	CLK1
27	Regout[26]	CLK1	13.124	12.936	Rise	CLK1
28	Regout[27]	CLK1	13.212	13.037	Rise	CLK1
29	Regout[28]	CLK1	13.094	12.926	Rise	CLK1
30	Regout[29]	CLK1	13.808	13.685	Rise	CLK1
31	Regout[30]	CLK1	12.542	12.468	Rise	CLK1
32	Regout[31]	CLK1	14.328	14.305	Rise	CLK1