

	Data Port	Clock Port	Rise	Fall	Clock Edge	Clock Reference
1	ClockDIV	CLK1	5.542	5.520	Rise	CLK1
2	Instr[*]	CLK1	10.705	10.768	Rise	CLK1
1	Instr[0]	CLK1	8.362	8.343	Rise	CLK1
2	Instr[1]	CLK1	8.798	8.812	Rise	CLK1
3	Instr[2]	CLK1	8.686	8.635	Rise	CLK1
4	Instr[3]	CLK1	8.483	8.447	Rise	CLK1
5	Instr[4]	CLK1	8.376	8.361	Rise	CLK1
6	Instr[5]	CLK1	8.475	8.442	Rise	CLK1
7	Instr[6]	CLK1	8.218	8.182	Rise	CLK1
8	Instr[7]	CLK1	9.009	9.000	Rise	CLK1
9	Instr[8]	CLK1	9.639	9.615	Rise	CLK1
10	Instr[9]	CLK1	9.077	9.059	Rise	CLK1
11	Instr[10]	CLK1	10.277	10.282	Rise	CLK1
12	Instr[11]	CLK1	9.804	9.787	Rise	CLK1
13	Instr[12]	CLK1	9.474	9.454	Rise	CLK1
14	Instr[13]	CLK1	9.612	9.687	Rise	CLK1
15	Instr[14]	CLK1	9.156	9.148	Rise	CLK1
16	Instr[15]	CLK1	9.384	9.382	Rise	CLK1
17	Instr[16]	CLK1	9.431	9.484	Rise	CLK1
18	Instr[17]	CLK1	9.369	9.412	Rise	CLK1
19	Instr[18]	CLK1	9.822	9.832	Rise	CLK1
20	Instr[19]	CLK1	9.333	9.352	Rise	CLK1
21	Instr[20]	CLK1	10.081	10.094	Rise	CLK1
22	Instr[21]	CLK1	9.739	9.817	Rise	CLK1
23	Instr[22]	CLK1	9.292	9.384	Rise	CLK1
24	Instr[23]	CLK1	9.614	9.663	Rise	CLK1
25	Instr[24]	CLK1	10.705	10.768	Rise	CLK1
26	Instr[25]	CLK1	9.894	9.844	Rise	CLK1
27	Instr[26]	CLK1	9.284	9.266	Rise	CLK1
28	Instr[27]	CLK1	10.076	10.044	Rise	CLK1
29	Instr[28]	CLK1	9.948	10.026	Rise	CLK1
30	Instr[29]	CLK1	8.499	8.473	Rise	CLK1
31	Instr[30]	CLK1	8.680	8.688	Rise	CLK1
32	Instr[31]	CLK1	8.713	8.693	Rise	CLK1
3	Regout[*]	CLK1	16.484	16.483	Rise	CLK1
1	Regout[0]	CLK1	15.069	14.932	Rise	CLK1
2	Regout[1]	CLK1	15.254	15.177	Rise	CLK1
3	Regout[2]	CLK1	15.630	15.502	Rise	CLK1
4	Regout[3]	CLK1	15.752	15.616	Rise	CLK1

	Data Port	Clock Port	Rise	Fall	Clock Edge	Clock Reference
5	Regout[4]	CLK1	14.548	14.458	Rise	CLK1
6	Regout[5]	CLK1	14.726	14.695	Rise	CLK1
7	Regout[6]	CLK1	14.630	14.560	Rise	CLK1
8	Regout[7]	CLK1	14.632	14.561	Rise	CLK1
9	Regout[8]	CLK1	14.644	14.584	Rise	CLK1
10	Regout[9]	CLK1	12.700	12.651	Rise	CLK1
11	Regout[10]	CLK1	14.875	14.827	Rise	CLK1
12	Regout[11]	CLK1	14.520	14.461	Rise	CLK1
13	Regout[12]	CLK1	16.011	15.914	Rise	CLK1
14	Regout[13]	CLK1	15.624	15.505	Rise	CLK1
15	Regout[14]	CLK1	14.453	14.415	Rise	CLK1
16	Regout[15]	CLK1	15.375	15.275	Rise	CLK1
17	Regout[16]	CLK1	15.537	15.423	Rise	CLK1
18	Regout[17]	CLK1	13.864	13.847	Rise	CLK1
19	Regout[18]	CLK1	15.807	15.669	Rise	CLK1
20	Regout[19]	CLK1	13.831	13.805	Rise	CLK1
21	Regout[20]	CLK1	15.697	15.590	Rise	CLK1
22	Regout[21]	CLK1	15.789	15.727	Rise	CLK1
23	Regout[22]	CLK1	16.312	16.219	Rise	CLK1
24	Regout[23]	CLK1	15.923	15.831	Rise	CLK1
25	Regout[24]	CLK1	15.042	14.892	Rise	CLK1
26	Regout[25]	CLK1	14.944	14.771	Rise	CLK1
27	Regout[26]	CLK1	15.230	15.055	Rise	CLK1
28	Regout[27]	CLK1	15.308	15.147	Rise	CLK1
29	Regout[28]	CLK1	15.164	15.019	Rise	CLK1
30	Regout[29]	CLK1	15.939	15.825	Rise	CLK1
31	Regout[30]	CLK1	14.618	14.554	Rise	CLK1
32	Regout[31]	CLK1	16.484	16.483	Rise	CLK1