

	Input Port	Output Port	RR	RF	FR	FF
1	Regin[0]	Regout[0]	10.751	10.775	11.330	11.382
2	Regin[0]	Regout[1]	9.525	9.522	10.065	10.094
3	Regin[0]	Regout[2]	9.190	9.201	9.652	9.654
4	Regin[0]	Regout[3]	8.916	8.936	9.421	9.471
5	Regin[0]	Regout[4]	8.680	8.672	9.213	9.200
6	Regin[0]	Regout[5]	7.689	7.688	8.227	8.167
7	Regin[0]	Regout[6]	9.134	9.098	9.660	9.622
8	Regin[0]	Regout[7]	8.488	8.472	9.010	9.058
9	Regin[0]	Regout[8]	10.302	10.423	10.935	10.905
10	Regin[0]	Regout[9]	8.797	8.759	9.310	9.281
11	Regin[0]	Regout[10]	9.972	10.037	10.384	10.488
12	Regin[0]	Regout[11]	8.771	8.799	9.357	9.326
13	Regin[0]	Regout[12]	9.403	9.398	9.880	9.918
14	Regin[0]	Regout[13]	9.877	9.916	10.371	10.441
15	Regin[0]	Regout[14]	8.716	8.729	9.242	9.255
16	Regin[0]	Regout[15]	8.252	8.258	8.765	8.762
17	Regin[0]	Regout[16]	10.151	10.156	10.624	10.659
18	Regin[0]	Regout[17]	8.576	8.528	9.080	9.096
19	Regin[0]	Regout[18]	9.780	9.886	10.326	10.296
20	Regin[0]	Regout[19]	8.868	8.907	9.487	9.451
21	Regin[0]	Regout[20]	10.133	10.176	10.604	10.647
22	Regin[0]	Regout[21]	8.732	8.742	9.236	9.237
23	Regin[0]	Regout[22]	9.740	9.783	10.223	10.309
24	Regin[0]	Regout[23]	9.588	9.608	10.099	10.110
25	Regin[0]	Regout[24]	8.877	8.839	9.322	9.314
26	Regin[0]	Regout[25]	8.783	8.768	9.262	9.238
27	Regin[0]	Regout[26]	8.515	8.521	9.019	8.968
28	Regin[0]	Regout[27]	8.996	8.969	9.471	9.453
29	Regin[0]	Regout[28]	8.045	8.040	8.496	8.482
30	Regin[0]	Regout[29]	8.872	8.958	9.466	9.416
31	Regin[0]	Regout[30]	10.202	10.231	10.725	10.737
32	Regin[0]	Regout[31]	10.516	10.665	11.055	11.128
33	Regin[1]	Regout[0]	8.730	8.753	9.267	9.281
34	Regin[1]	Regout[1]	10.372	10.396	10.942	10.936
35	Regin[1]	Regout[2]	8.628	8.687	9.198	9.181
36	Regin[1]	Regout[3]	8.680	8.701	9.154	9.166
37	Regin[1]	Regout[4]	8.917	8.893	9.459	9.478
38	Regin[1]	Regout[5]	8.507	8.496	9.076	9.056
39	Regin[1]	Regout[6]	8.899	8.860	9.443	9.468

	Input Port	Output Port	RR	RF	FR	FF
40	Regin[1]	Regout[7]	8.630	8.624	9.169	9.163
41	Regin[1]	Regout[8]	8.840	8.871	9.386	9.447
42	Regin[1]	Regout[9]	8.388	8.345	8.877	8.825
43	Regin[1]	Regout[10]	8.494	8.475	9.034	9.045
44	Regin[1]	Regout[11]	8.825	8.843	9.370	9.403
45	Regin[1]	Regout[12]	8.639	8.658	9.115	9.125
46	Regin[1]	Regout[13]	9.979	10.031	10.496	10.518
47	Regin[1]	Regout[14]	8.727	8.713	9.267	9.285
48	Regin[1]	Regout[15]	9.393	9.421	9.930	9.958
49	Regin[1]	Regout[16]	9.170	9.188	9.730	9.718
50	Regin[1]	Regout[17]	9.064	9.026	9.591	9.553
51	Regin[1]	Regout[18]	8.880	8.862	9.341	9.353
52	Regin[1]	Regout[19]	9.708	9.742	10.283	10.281
53	Regin[1]	Regout[20]	9.318	9.328	9.777	9.819
54	Regin[1]	Regout[21]	10.014	10.142	10.613	10.594
55	Regin[1]	Regout[22]	9.231	9.229	9.725	9.723
56	Regin[1]	Regout[23]	9.265	9.320	9.843	9.762
57	Regin[1]	Regout[24]	8.469	8.397	8.972	8.943
58	Regin[1]	Regout[25]	8.573	8.677	9.146	9.114
59	Regin[1]	Regout[26]	7.807	7.830	8.371	8.258
60	Regin[1]	Regout[27]	7.956	7.920	8.438	8.441
61	Regin[1]	Regout[28]	8.068	8.095	8.568	8.565
62	Regin[1]	Regout[29]	9.695	9.709	10.194	10.247
63	Regin[1]	Regout[30]	8.508	8.567	9.124	9.047
64	Regin[1]	Regout[31]	10.957	11.052	11.457	11.631
65	Regin[2]	Regout[0]	7.214	7.294	7.268	7.289
66	Regin[2]	Regout[1]	7.764	7.830	7.865	7.901
67	Regin[2]	Regout[2]	6.722	6.698	6.835	6.807
68	Regin[2]	Regout[3]	7.032	7.077	7.120	7.151
69	Regin[2]	Regout[4]	7.640	7.635	7.743	7.784
70	Regin[2]	Regout[5]	6.281	6.317	6.411	6.371
71	Regin[2]	Regout[6]	7.446	7.451	7.551	7.616
72	Regin[2]	Regout[7]	6.747	6.742	6.821	6.880
73	Regin[2]	Regout[8]	7.665	7.699	7.728	7.753
74	Regin[2]	Regout[9]	6.419	6.430	6.559	6.494
75	Regin[2]	Regout[10]	7.545	7.532	7.620	7.598
76	Regin[2]	Regout[11]	6.602	6.630	6.676	6.734
77	Regin[2]	Regout[12]	6.940	6.972	7.053	7.115
78	Regin[2]	Regout[13]	6.111	6.116	6.244	6.240

	Input Port	Output Port	RR	RF	FR	FF
79	Regin[2]	Regout[14]	7.263	7.271	7.371	7.384
80	Regin[2]	Regout[15]	6.213	6.212	6.264	6.293
81	Regin[2]	Regout[16]	7.322	7.300	7.426	7.399
82	Regin[2]	Regout[17]	6.125	6.133	6.203	6.241
83	Regin[2]	Regout[18]	6.410	6.408	6.502	6.500
84	Regin[2]	Regout[19]	6.352	6.393	6.495	6.527
85	Regin[2]	Regout[20]	6.905	6.948	7.004	7.047
86	Regin[2]	Regout[21]	7.154	7.250	7.321	7.270
87	Regin[2]	Regout[22]	6.311	6.309	6.433	6.422
88	Regin[2]	Regout[23]	6.153	6.140	6.247	6.266
89	Regin[2]	Regout[24]	6.758	6.743	6.824	6.839
90	Regin[2]	Regout[25]	5.762	5.769	5.851	5.849
91	Regin[2]	Regout[26]	6.710	6.729	6.807	6.812
92	Regin[2]	Regout[27]	5.823	5.817	5.922	5.889
93	Regin[2]	Regout[28]	6.175	6.199	6.250	6.265
94	Regin[2]	Regout[29]	7.115	7.092	7.230	7.198
95	Regin[2]	Regout[30]	6.832	6.833	6.959	6.951
96	Regin[2]	Regout[31]	7.296	7.443	7.355	7.493
97	Regin[3]	Regout[0]	6.967	6.961	7.016	7.070
98	Regin[3]	Regout[1]	8.599	8.623	8.715	8.739
99	Regin[3]	Regout[2]	6.008	5.983	6.129	6.134
100	Regin[3]	Regout[3]	6.996	7.033	7.036	7.073
101	Regin[3]	Regout[4]	6.858	6.923	6.968	7.003
102	Regin[3]	Regout[5]	6.923	6.902	7.028	7.032
103	Regin[3]	Regout[6]	7.746	7.717	7.920	7.891
104	Regin[3]	Regout[7]	6.731	6.728	6.879	6.875
105	Regin[3]	Regout[8]	6.650	6.745	6.824	6.843
106	Regin[3]	Regout[9]	6.246	6.197	6.382	6.363
107	Regin[3]	Regout[10]	6.967	6.929	7.045	7.071
108	Regin[3]	Regout[11]	6.620	6.640	6.703	6.736
109	Regin[3]	Regout[12]	6.000	6.080	6.182	6.126
110	Regin[3]	Regout[13]	7.636	7.646	7.691	7.722
111	Regin[3]	Regout[14]	6.278	6.287	6.378	6.378
112	Regin[3]	Regout[15]	6.533	6.570	6.599	6.664
113	Regin[3]	Regout[16]	6.875	6.852	6.930	6.937
114	Regin[3]	Regout[17]	6.465	6.442	6.640	6.647
115	Regin[3]	Regout[18]	6.704	6.701	6.834	6.861
116	Regin[3]	Regout[19]	6.495	6.541	6.603	6.652
117	Regin[3]	Regout[20]	5.856	5.875	5.945	5.996

	Input Port	Output Port	RR	RF	FR	FF
118	Regin[3]	Regout[21]	7.772	7.804	7.946	7.978
119	Regin[3]	Regout[22]	6.431	6.416	6.480	6.504
120	Regin[3]	Regout[23]	6.552	6.573	6.670	6.719
121	Regin[3]	Regout[24]	6.629	6.663	6.792	6.690
122	Regin[3]	Regout[25]	6.335	6.343	6.481	6.517
123	Regin[3]	Regout[26]	6.216	6.255	6.366	6.269
124	Regin[3]	Regout[27]	6.530	6.511	6.673	6.646
125	Regin[3]	Regout[28]	6.702	6.690	6.806	6.824
126	Regin[3]	Regout[29]	7.542	7.587	7.712	7.698
127	Regin[3]	Regout[30]	7.517	7.486	7.539	7.568
128	Regin[3]	Regout[31]	7.613	7.711	7.723	7.821
129	Regin[4]	Regout[0]	8.252	8.236	8.809	8.824
130	Regin[4]	Regout[1]	8.247	8.236	8.731	8.752
131	Regin[4]	Regout[2]	7.710	7.661	8.156	8.202
132	Regin[4]	Regout[3]	7.331	7.427	7.846	7.791
133	Regin[4]	Regout[4]	7.475	7.430	7.958	7.944
134	Regin[4]	Regout[5]	7.640	7.606	8.152	8.149
135	Regin[4]	Regout[6]	7.415	7.381	7.878	7.876
136	Regin[4]	Regout[7]	7.290	7.229	7.738	7.727
137	Regin[4]	Regout[8]	7.562	7.545	8.043	8.057
138	Regin[4]	Regout[9]	6.841	6.767	7.209	7.185
139	Regin[4]	Regout[10]	7.516	7.538	8.033	7.974
140	Regin[4]	Regout[11]	7.176	7.124	7.622	7.620
141	Regin[4]	Regout[12]	8.193	8.179	8.745	8.722
142	Regin[4]	Regout[13]	9.364	9.399	9.958	9.909
143	Regin[4]	Regout[14]	7.663	7.626	8.169	8.164
144	Regin[4]	Regout[15]	8.291	8.266	8.811	8.836
145	Regin[4]	Regout[16]	8.446	8.432	8.971	8.985
146	Regin[4]	Regout[17]	7.101	7.101	7.577	7.496
147	Regin[4]	Regout[18]	8.501	8.477	9.058	9.065
148	Regin[4]	Regout[19]	7.594	7.548	8.045	8.046
149	Regin[4]	Regout[20]	8.469	8.443	8.980	8.986
150	Regin[4]	Regout[21]	8.678	8.753	9.270	9.264
151	Regin[4]	Regout[22]	8.753	8.745	9.295	9.317
152	Regin[4]	Regout[23]	8.295	8.261	8.791	8.804
153	Regin[4]	Regout[24]	7.548	7.584	8.081	7.966
154	Regin[4]	Regout[25]	7.576	7.536	8.088	8.080
155	Regin[4]	Regout[26]	7.455	7.399	7.901	7.876
156	Regin[4]	Regout[27]	7.671	7.637	8.179	8.175

	Input Port	Output Port	RR	RF	FR	FF
157	Regin[4]	Regout[28]	7.198	7.158	7.651	7.602
158	Regin[4]	Regout[29]	8.020	8.011	8.448	8.469
159	Regin[4]	Regout[30]	7.141	7.106	7.607	7.602
160	Regin[4]	Regout[31]	9.758	9.843	10.303	10.419