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|------------------------------------|-------------------------------------------------|
| Flow Status                        | Successful - Mon Jun 23 10:54:21 2025           |
| Quartus Prime Version              | 24.1std.0 Build 1077 03/04/2025 SC Lite Edition |
| Revision Name                      | TopDE                                           |
| Top-level Entity Name              | TopDE                                           |
| Family                             | Cyclone IV E                                    |
| Total logic elements               | 1,957 / 6,272 ( 31 % )                          |
| Total combinational functions      | 1,858 / 6,272 ( 30 % )                          |
| Dedicated logic registers          | 1,168 / 6,272 ( 19 % )                          |
| Total registers                    | 1168                                            |
| Total pins                         | 75 / 180 ( 42 % )                               |
| Total virtual pins                 | 0                                               |
| Total memory bits                  | 32,768 / 276,480 ( 12 % )                       |
| Embedded Multiplier 9-bit elements | 0 / 30 ( 0 % )                                  |
| Total PLLs                         | 0 / 2 ( 0 % )                                   |
| Device                             | EP4CE6F17C6                                     |
| Timing Models                      | Final                                           |