

	Input Port	Output Port	RR	RF	FR	FF
1	Regin[1]	Regout[13]	16.645	16.722	17.311	17.388
2	Regin[1]	Regout[22]	15.991	16.038	16.576	16.623
3	Regin[0]	Regout[13]	15.303	15.380	15.944	16.021
4	Regin[0]	Regout[31]	15.280	15.428	15.701	15.849
5	Regin[0]	Regout[10]	15.093	15.184	15.595	15.686
6	Regin[1]	Regout[1]	15.034	15.121	15.559	15.646
7	Regin[1]	Regout[31]	14.861	15.009	15.455	15.603
8	Regin[0]	Regout[22]	14.787	14.834	15.324	15.371
9	Regin[0]	Regout[19]	14.561	14.629	15.079	15.148
10	Regin[1]	Regout[15]	14.506	14.530	15.081	15.105
11	Regin[1]	Regout[6]	14.421	14.631	15.000	15.210
12	Regin[0]	Regout[8]	14.406	14.457	14.917	14.968
13	Regin[0]	Regout[21]	14.304	14.330	14.728	14.754
14	Regin[0]	Regout[6]	14.286	14.496	14.882	15.092
15	Regin[1]	Regout[14]	14.167	14.247	14.753	14.833
16	Regin[1]	Regout[19]	14.118	14.187	14.648	14.712
17	Regin[1]	Regout[8]	14.058	14.109	14.556	14.607
18	Regin[1]	Regout[10]	14.025	14.116	14.533	14.624
19	Regin[0]	Regout[2]	13.846	13.814	14.377	14.345
20	Regin[0]	Regout[18]	13.843	13.845	14.373	14.375
21	Regin[1]	Regout[27]	13.748	13.752	14.427	14.431
22	Regin[1]	Regout[16]	13.692	13.739	14.376	14.436
23	Regin[1]	Regout[18]	13.691	13.693	14.211	14.213
24	Regin[0]	Regout[7]	13.686	13.707	14.194	14.215
25	Regin[1]	Regout[0]	13.683	13.716	14.203	14.256
26	Regin[0]	Regout[1]	13.661	13.771	14.114	14.224
27	Regin[0]	Regout[0]	13.592	13.616	14.091	14.111
28	Regin[0]	Regout[29]	13.575	13.629	14.071	14.125
29	Regin[0]	Regout[27]	13.447	13.451	13.950	13.954
30	Regin[1]	Regout[21]	13.424	13.450	13.918	13.944
31	Regin[0]	Regout[16]	13.400	13.452	13.909	13.969
32	Regin[0]	Regout[5]	13.351	13.343	13.875	13.867
33	Regin[1]	Regout[29]	13.251	13.305	13.763	13.817
34	Regin[2]	Regout[2]	13.207	13.310	13.317	13.420
35	Regin[1]	Regout[5]	13.137	13.129	13.661	13.653
36	Regin[1]	Regout[7]	13.064	13.085	13.576	13.597
37	Regin[0]	Regout[15]	12.987	13.011	13.574	13.598
38	Regin[0]	Regout[4]	12.977	12.991	13.472	13.495
39	Regin[3]	Regout[2]	12.975	13.078	13.155	13.258

	Input Port	Output Port	RR	RF	FR	FF
40	Regin[0]	Regout[14]	12.967	13.047	13.505	13.585
41	Regin[0]	Regout[12]	12.866	12.887	13.407	13.428
42	Regin[1]	Regout[2]	12.833	12.801	13.402	13.370
43	Regin[0]	Regout[17]	12.770	12.871	13.242	13.304
44	Regin[0]	Regout[25]	12.741	12.788	13.342	13.390
45	Regin[1]	Regout[20]	12.717	12.770	13.345	13.398
46	Regin[1]	Regout[17]	12.689	12.790	13.283	13.384
47	Regin[2]	Regout[28]	12.555	12.728	12.614	12.787
48	Regin[0]	Regout[30]	12.531	12.528	13.058	13.055
49	Regin[1]	Regout[4]	12.530	12.525	13.025	13.020
50	Regin[0]	Regout[9]	12.524	12.509	13.019	13.004
51	Regin[2]	Regout[21]	12.505	12.594	12.585	12.674
52	Regin[1]	Regout[12]	12.486	12.507	13.082	13.103
53	Regin[1]	Regout[9]	12.482	12.467	12.948	12.933
54	Regin[0]	Regout[11]	12.476	12.521	12.970	13.011
55	Regin[1]	Regout[25]	12.400	12.448	13.053	13.101
56	Regin[0]	Regout[23]	12.292	12.358	12.823	12.889
57	Regin[2]	Regout[29]	12.231	12.411	12.393	12.573
58	Regin[3]	Regout[29]	12.212	12.392	12.339	12.519
59	Regin[1]	Regout[3]	12.206	12.287	12.744	12.825
60	Regin[1]	Regout[11]	12.204	12.236	12.771	12.794
61	Regin[0]	Regout[3]	12.196	12.277	12.673	12.754
62	Regin[0]	Regout[20]	12.123	12.223	12.720	12.820
63	Regin[2]	Regout[4]	11.979	11.964	12.094	12.079
64	Regin[1]	Regout[30]	11.898	11.895	12.453	12.450
65	Regin[3]	Regout[28]	11.874	12.059	11.921	12.115
66	Regin[2]	Regout[22]	11.868	11.911	11.963	12.006
67	Regin[0]	Regout[24]	11.834	11.800	12.312	12.278
68	Regin[3]	Regout[21]	11.747	11.836	11.865	11.954
69	Regin[1]	Regout[23]	11.736	11.802	12.316	12.382
70	Regin[2]	Regout[12]	11.722	11.775	11.855	11.908
71	Regin[2]	Regout[19]	11.634	11.676	11.636	11.678
72	Regin[3]	Regout[22]	11.629	11.672	11.747	11.790
73	Regin[2]	Regout[3]	11.600	11.666	11.687	11.753
74	Regin[2]	Regout[20]	11.576	11.673	11.677	11.765
75	Regin[1]	Regout[24]	11.550	11.516	12.082	12.048
76	Regin[2]	Regout[23]	11.479	11.531	11.557	11.609
77	Regin[3]	Regout[12]	11.421	11.474	11.529	11.582
78	Regin[3]	Regout[3]	11.346	11.412	11.458	11.524

	Input Port	Output Port	RR	RF	FR	FF
79	Regin[3]	Regout[27]	11.336	11.350	11.463	11.477
80	Regin[2]	Regout[16]	11.319	11.352	11.432	11.461
81	Regin[3]	Regout[16]	11.292	11.325	11.433	11.434
82	Regin[3]	Regout[20]	11.206	11.312	11.336	11.380
83	Regin[2]	Regout[13]	11.154	11.192	11.255	11.293
84	Regin[1]	Regout[28]	11.024	11.072	11.577	11.628
85	Regin[3]	Regout[4]	10.997	10.982	11.106	11.091
86	Regin[0]	Regout[28]	10.984	11.026	11.541	11.583
87	Regin[0]	Regout[26]	10.958	10.961	11.445	11.448
88	Regin[2]	Regout[18]	10.903	10.878	11.022	10.997
89	Regin[2]	Regout[0]	10.893	10.941	11.015	11.054
90	Regin[3]	Regout[31]	10.889	11.017	10.968	11.096
91	Regin[3]	Regout[23]	10.869	10.921	10.988	11.040
92	Regin[2]	Regout[1]	10.855	10.950	10.916	11.011
93	Regin[2]	Regout[27]	10.755	10.769	10.916	10.930
94	Regin[3]	Regout[19]	10.742	10.784	10.833	10.875
95	Regin[3]	Regout[6]	10.698	10.908	10.751	10.943
96	Regin[2]	Regout[6]	10.694	10.904	10.805	11.015
97	Regin[2]	Regout[26]	10.684	10.732	10.824	10.863
98	Regin[2]	Regout[31]	10.628	10.756	10.696	10.824
99	Regin[3]	Regout[25]	10.615	10.622	10.673	10.689
100	Regin[1]	Regout[26]	10.590	10.593	11.087	11.087
101	Regin[2]	Regout[15]	10.572	10.655	10.640	10.723
102	Regin[3]	Regout[15]	10.513	10.596	10.617	10.700
103	Regin[3]	Regout[18]	10.469	10.444	10.641	10.616
104	Regin[3]	Regout[0]	10.458	10.511	10.633	10.672
105	Regin[3]	Regout[13]	10.437	10.475	10.609	10.647
106	Regin[2]	Regout[25]	10.375	10.391	10.437	10.453
107	Regin[2]	Regout[9]	10.278	10.297	10.375	10.394
108	Regin[2]	Regout[24]	10.278	10.266	10.357	10.354
109	Regin[2]	Regout[10]	10.195	10.194	10.355	10.354
110	Regin[2]	Regout[8]	10.190	10.254	10.280	10.344
111	Regin[4]	Regout[31]	10.113	10.203	10.675	10.798
112	Regin[2]	Regout[30]	10.100	10.081	10.199	10.180
113	Regin[3]	Regout[1]	10.050	10.162	10.234	10.245
114	Regin[3]	Regout[8]	10.040	10.104	10.105	10.169
115	Regin[3]	Regout[9]	10.036	10.055	10.167	10.186
116	Regin[2]	Regout[14]	10.014	10.096	10.113	10.195
117	Regin[3]	Regout[24]	9.950	9.947	9.996	9.993

	Input Port	Output Port	RR	RF	FR	FF
118	Regin[3]	Regout[26]	9.883	9.830	9.979	9.935
119	Regin[3]	Regout[5]	9.724	9.725	9.832	9.824
120	Regin[4]	Regout[13]	9.703	9.741	10.316	10.267
121	Regin[3]	Regout[17]	9.703	9.657	9.807	9.761
122	Regin[2]	Regout[5]	9.355	9.353	9.538	9.536
123	Regin[3]	Regout[30]	9.293	9.274	9.403	9.384
124	Regin[2]	Regout[7]	9.266	9.287	9.353	9.374
125	Regin[2]	Regout[17]	9.208	9.162	9.334	9.288
126	Regin[3]	Regout[14]	9.203	9.285	9.314	9.396
127	Regin[3]	Regout[10]	9.182	9.179	9.271	9.292
128	Regin[4]	Regout[22]	9.065	9.057	9.624	9.648
129	Regin[4]	Regout[21]	8.987	9.067	9.600	9.594
130	Regin[3]	Regout[11]	8.939	8.964	9.059	9.082
131	Regin[3]	Regout[7]	8.900	8.921	8.972	8.993
132	Regin[2]	Regout[11]	8.817	8.842	8.919	8.944
133	Regin[4]	Regout[18]	8.802	8.777	9.377	9.385
134	Regin[4]	Regout[20]	8.771	8.745	9.298	9.306
135	Regin[4]	Regout[16]	8.747	8.733	9.289	9.303
136	Regin[4]	Regout[23]	8.589	8.555	9.099	9.115
137	Regin[4]	Regout[15]	8.586	8.561	9.121	9.149
138	Regin[4]	Regout[0]	8.545	8.530	9.120	9.136
139	Regin[4]	Regout[1]	8.539	8.529	9.038	9.062
140	Regin[4]	Regout[12]	8.483	8.471	9.053	9.032
141	Regin[4]	Regout[29]	8.304	8.295	8.744	8.767
142	Regin[4]	Regout[2]	7.982	7.932	8.439	8.488
143	Regin[4]	Regout[27]	7.938	7.903	8.462	8.459
144	Regin[4]	Regout[14]	7.931	7.893	8.453	8.449
145	Regin[4]	Regout[5]	7.908	7.874	8.436	8.435
146	Regin[4]	Regout[19]	7.859	7.812	8.322	8.325
147	Regin[4]	Regout[25]	7.843	7.801	8.371	8.363
148	Regin[4]	Regout[8]	7.826	7.810	8.321	8.336
149	Regin[4]	Regout[24]	7.809	7.847	8.360	8.240
150	Regin[4]	Regout[10]	7.776	7.801	8.309	8.248
151	Regin[4]	Regout[4]	7.734	7.689	8.231	8.217
152	Regin[4]	Regout[26]	7.713	7.655	8.172	8.147
153	Regin[4]	Regout[6]	7.672	7.638	8.149	8.149
154	Regin[4]	Regout[3]	7.585	7.686	8.116	8.059
155	Regin[4]	Regout[7]	7.541	7.479	8.001	7.992
156	Regin[4]	Regout[28]	7.447	7.407	7.914	7.865

	Input Port	Output Port	RR	RF	FR	FF
157	Regin[4]	Regout[11]	7.427	7.374	7.885	7.885
158	Regin[4]	Regout[30]	7.386	7.349	7.866	7.861
159	Regin[4]	Regout[17]	7.344	7.346	7.835	7.751
160	Regin[4]	Regout[9]	7.075	7.000	7.451	7.429