

root
: Root

system
: System

system_port

membus
: SystemXBar

cpu_side_ports

mem_side_ports

I2cache
: L2Cache

mem_side

cpu_side

mem_ctrl
: MemCtrl

port

I2bus
: L2XBar

mem_side_ports

cpu_side_ports

cpu
: RiscvTimingSimpleCPU

dcache_port

icache_port

dcache
: L1DCache

mem_side

cpu_side

icache
: L1ICache

mem_side

cpu_side