



Curriculum vitae

PERSONAL INFORMATION

Santino Sottile

📍 17, via Schillaci, 90147 Palermo (Italy)

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✉ santinosottile80@gmail.com

Sex Male | Date of birth 21 Jun 1980

PREFERRED JOB

Software tester / Junior Web Developer

WORK EXPERIENCE

- 4 Dec 2017–Present **Technical assistant for electronics, optoelectronics and telecommunications**
ITALIAN MINISTRY OF DEFENCE – SERIMANT BARRACKS
35 Via Generale Cantore Antonio, 90141 Palermo (Pa) (Italy)
- 18 Sep 2017–25 Nov 2017 **Electrical engineering / electronics teacher**
I.I.S.S. Guglielmo Marconi
60 Via Giuseppe Verdi, 24044 Dalmine (Italy)
- 19 Dec 2016–30 Jun 2017 **Computer lab technical assistant**
I.I.S.S. EINAUDI – CASAREGIS - GALILEI
5 Piazza Raffaele Sopranis, 16126 Genova (Italy)
- 1 Feb 2016–20 Jul 2016 **Trainee**
Italtel S.p.A.
SS 113 Bivio Foresta, 90044 Carini (Pa) (Italy)
<https://www.italtel.com/>
Development and design of a remote control infrastructure for different types of devices.
- 1 May 2002–31 Jul 2002 **Technician in the spare parts service area**
Technical training centers Mercedes-Benz Italia S.p.A.

EDUCATION AND TRAINING

- 1 Oct 2010–21 Jul 2016 **Degree in automation engineering obtained with a score of 110/110**
Università degli Studi di Palermo, Palermo (Italy)
Degree thesis carried out in collaboration with the Italtel S.p.A. di Carini (PA), concerning the design of an interface between different communication protocols, within a control system. The degree course provides skills regarding the control of linear and non-linear systems, within which electrical machines and drives are used, in the context of industrial automation considering both the hardware aspect and the software management of the generic infrastructure control.
- 21 Jul 2014–1 Aug 2014 **Certificate of participation calculation course with Matlab Simulink**
Università degli Studi di Palermo, Palermo (Italy)
Introduction to scientific and parallel computing, for the optimization of the computational performance of the computer

1 Sep 2002–23 Mar 2010

Degree in Electronic Engineering obtained with a score of 98/110

Università degli Studi di Palermo, Palermo (Italy)

Degree thesis carried out in collaboration with the Enel S.p.A. of Catania, concerning the study of the remote control of the energy distribution and production network in an isolated system; The degree course includes the acquisition of skills in the field of analogical and digital electronics, digital systems electronics, automatic design of electronic circuits, measurements and instrumentation for electronics, automation measurements and instruments, sensors for the automation, optoelectronics, electromagnetic fields, automatic controls, logic networks, numerical calculation and electrical engineering.

1 May 2007–31 Jul 2007

01/05/2007–31/07/2007 Trinity College London - GRADE 4 English knowledge certification

Trinity College London, Palermo (Italy)

13 Sep 1994–12 Jul 1999

Diploma of chief technical expert obtained with the vote of 80/100. Electronic specialization and telecommunications.

Istituto Tecnico Industriale Statale "Alessandro Volta", Palermo (Italy)

10 Jan 1996–31 May 1996

Training course on electrical systems

Istituto Tecnico Industriale Statale "Alessandro Volta", Palermo (Italy)

PERSONAL SKILLS

Mother tongue(s)

Italian

Foreign language(s)

English

UNDERSTANDING		SPEAKING		WRITING
Listening	Reading	Spoken interaction	Spoken production	
A2	A2	A2	A2	A2
Trinity College London GRADE 4				

Levels: A1 and A2: Basic user - B1 and B2: Independent user - C1 and C2: Proficient user
Common European Framework of Reference for Languages

Job-related skills

Knowledge of the basic concepts of Python programming, JavaScript and HTML and CSS. Professional skills Knowledge of electrical machines and drives, robotics and industrial automation, good general knowledge of digital and analogue electronics. Knowledge of the simulation through Matlab / Simulink. Knowledge of programming language C. Knowledge of programming in LabVIEW and related simulation, management of DAQ boards (AD / DA signal management, harmonic analysis, sampling, aliasing) LabVIEW Real Time, Hardware in the loop. Good ability to use electronic laboratory measurement equipment. Excellent mastery in programming and designing digital circuits using VHDL.

Digital skills

Good command of Windows and Linux Ubuntu operating systems. Ability to use Office applications.

ADDITIONAL INFORMATION

Presentations

Passion for music and singing, reading, cinema and volleyball. After-school volunteering in the parish area.