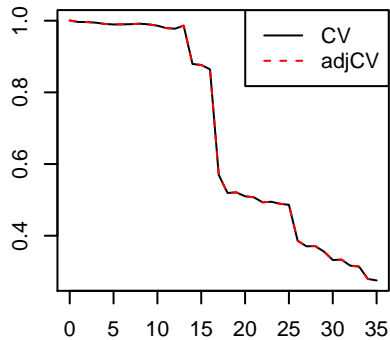
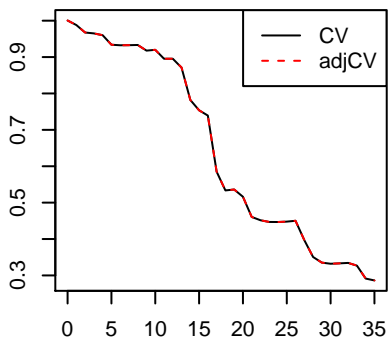


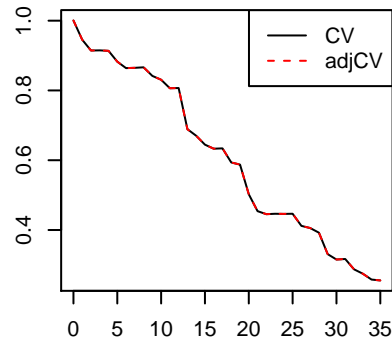
f0\_0



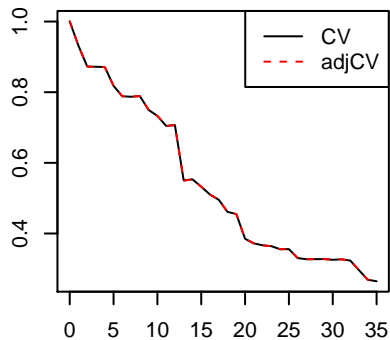
f0\_1



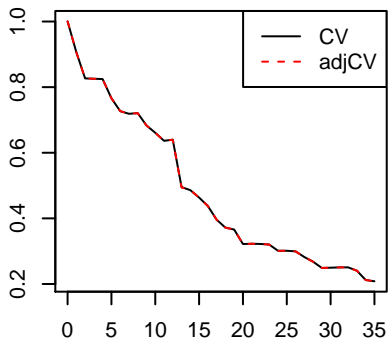
f0\_2



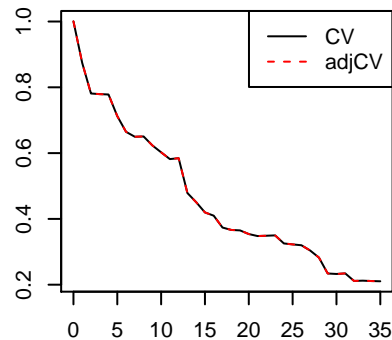
f0\_3



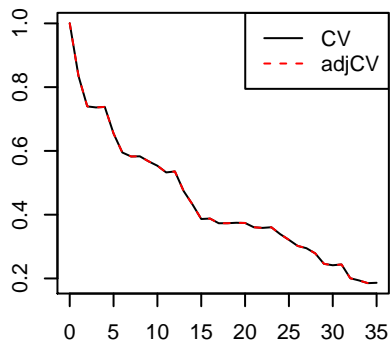
f0\_4



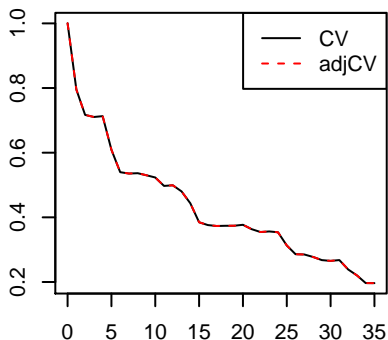
f0\_5



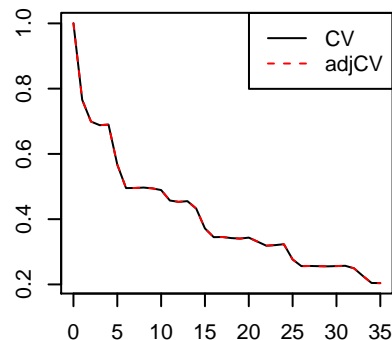
f0\_6



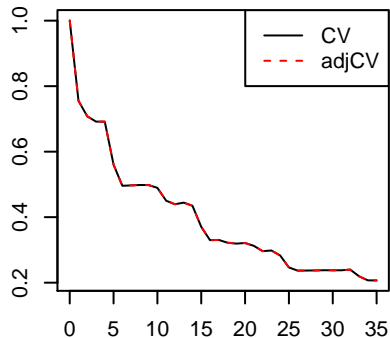
f0\_7



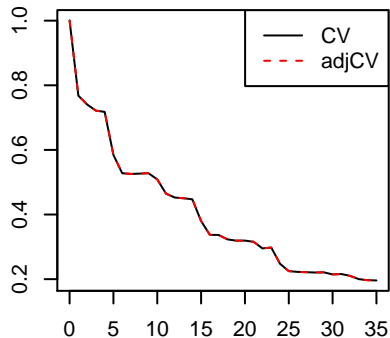
f0\_8



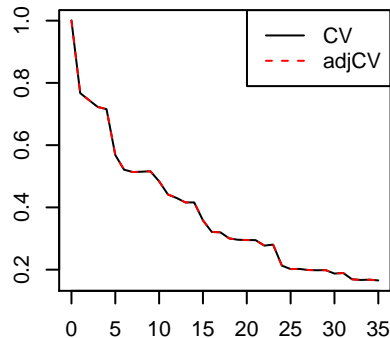
f0\_9



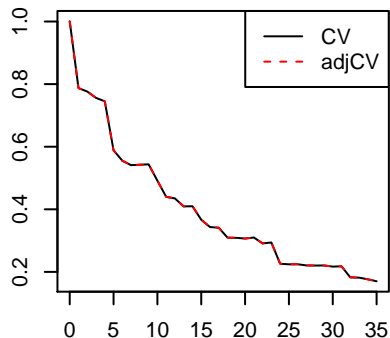
f0\_10



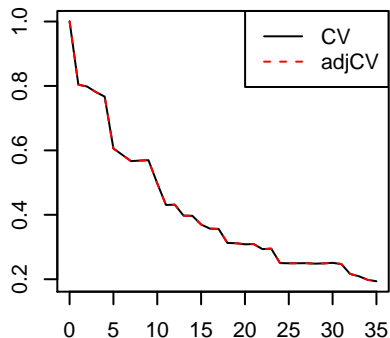
f0\_11



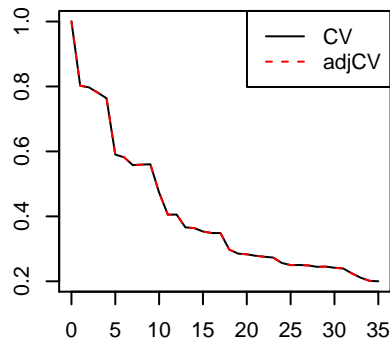
f0\_12



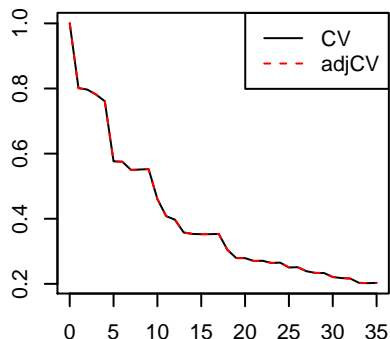
f0\_13



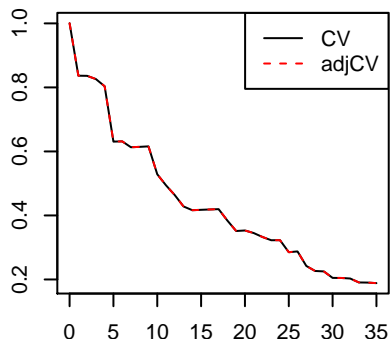
f0\_14



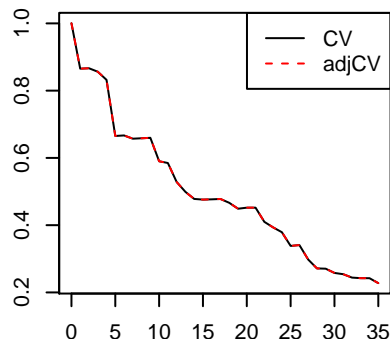
f0\_15

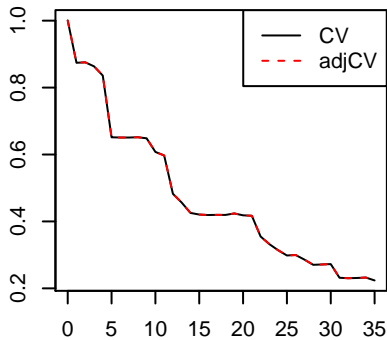
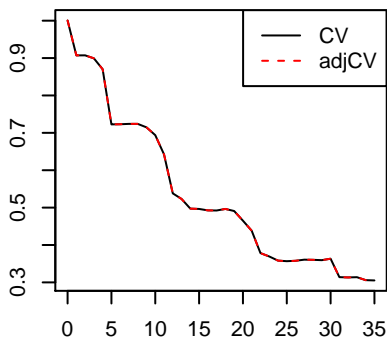
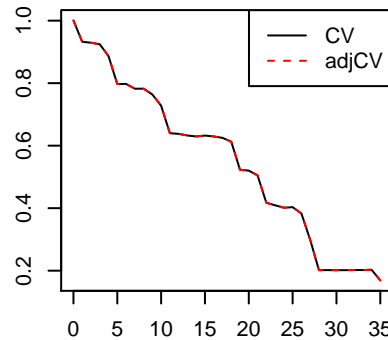
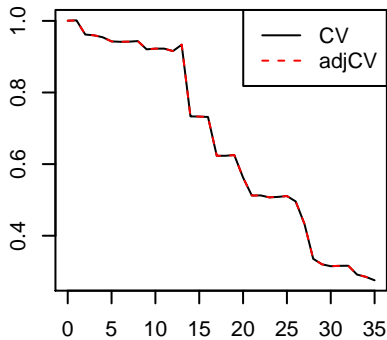
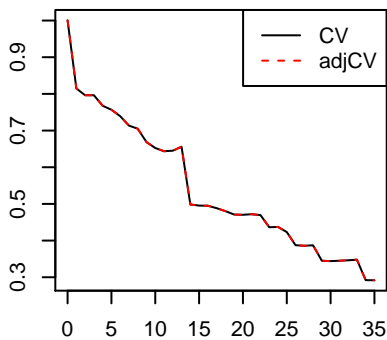
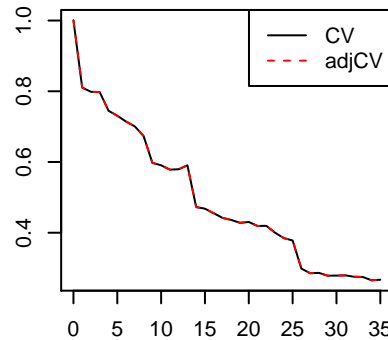
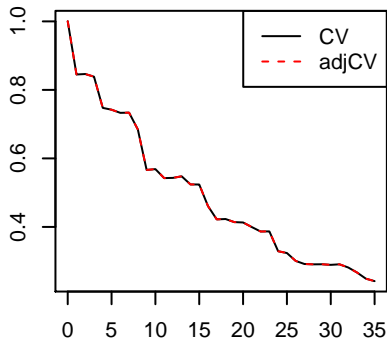
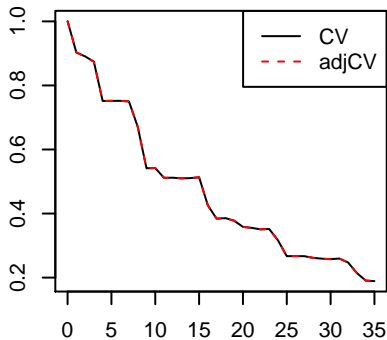
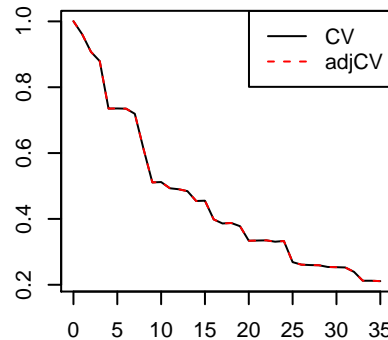


f0\_16

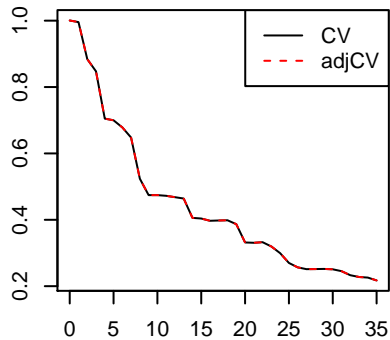


f0\_17

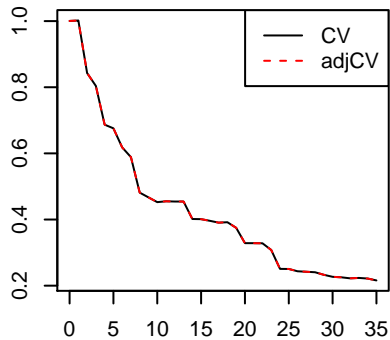


**f0\_18****f0\_19****f0\_20****f1\_0****f1\_1****f1\_2****f1\_3****f1\_4****f1\_5**

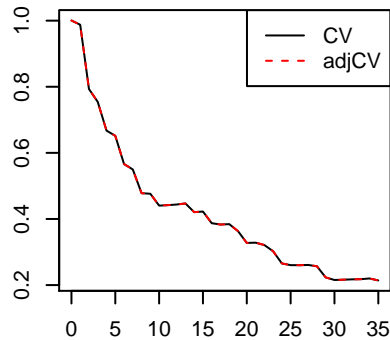
f1\_6



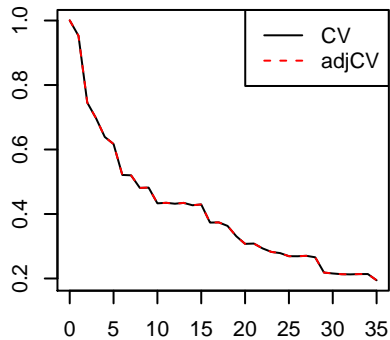
f1\_7



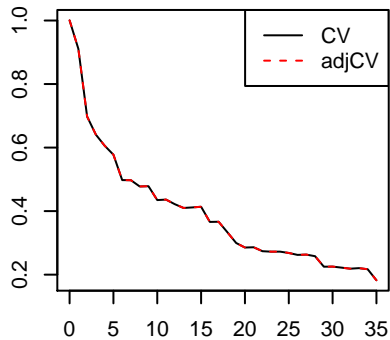
f1\_8



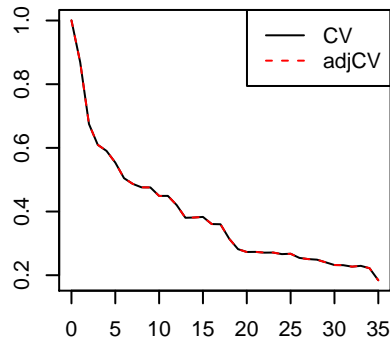
f1\_9



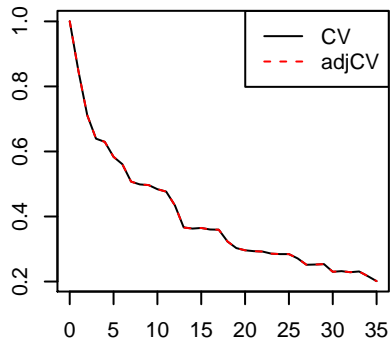
f1\_10



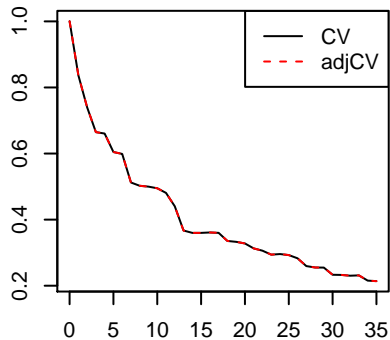
f1\_11



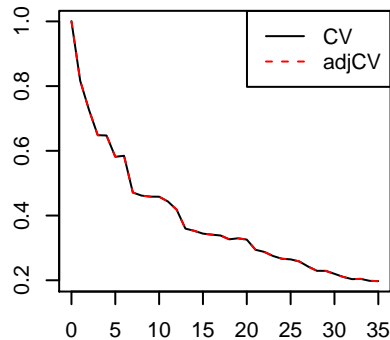
f1\_12

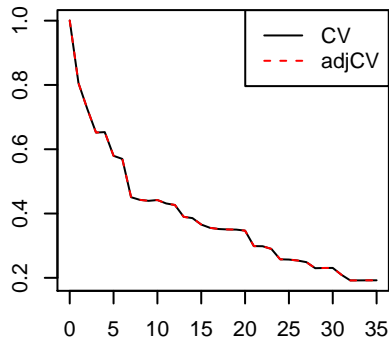
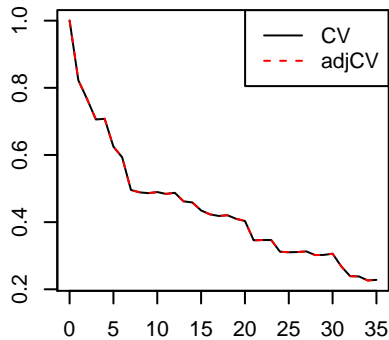
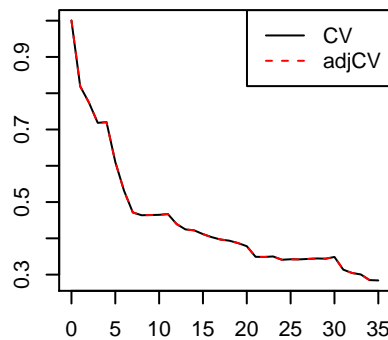
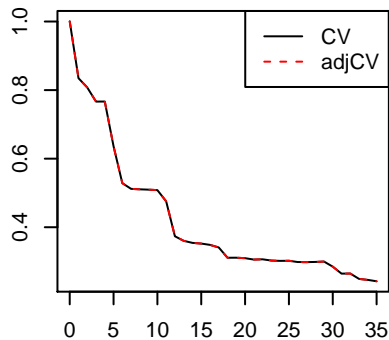
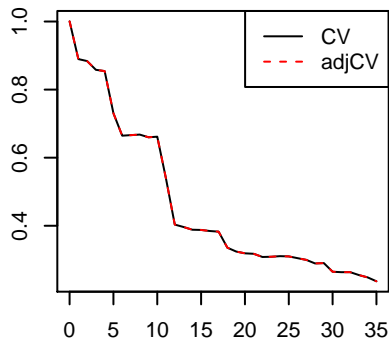
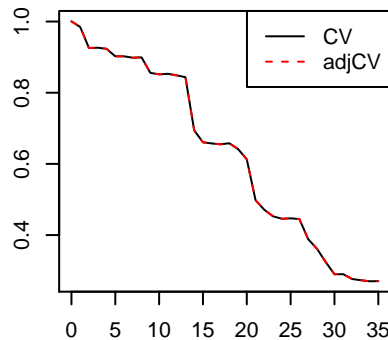
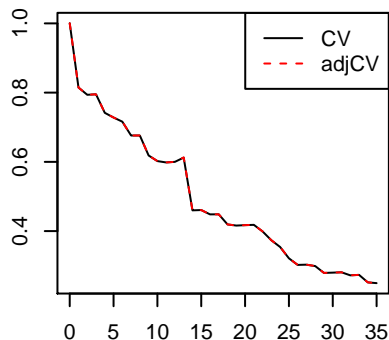
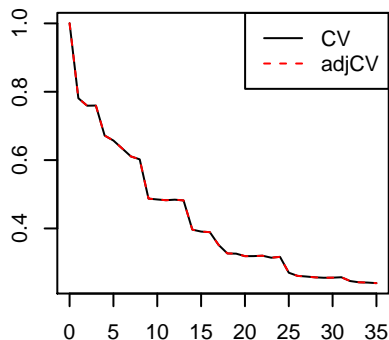
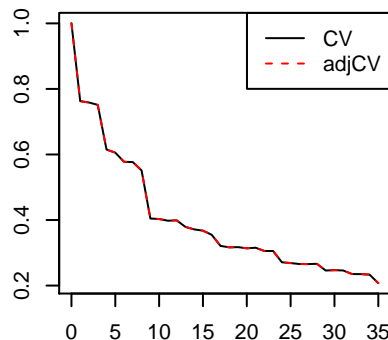


f1\_13



f1\_14

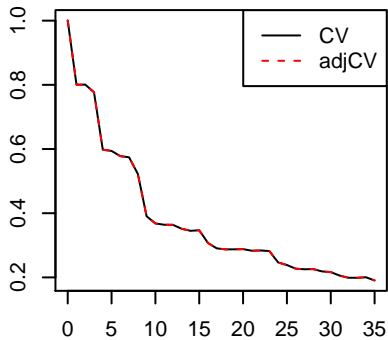


**f1\_15****f1\_16****f1\_17****f1\_18****f1\_19****f2\_0****f2\_1****f2\_2****f2\_3**

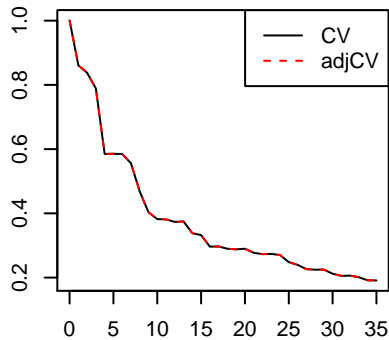
number of components

RMSEP

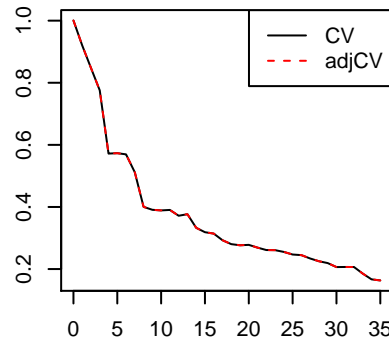
f2\_4



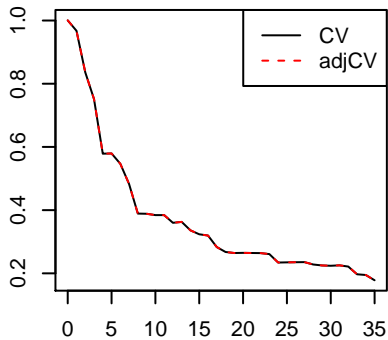
f2\_5



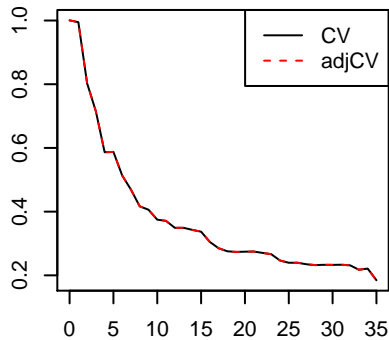
f2\_6



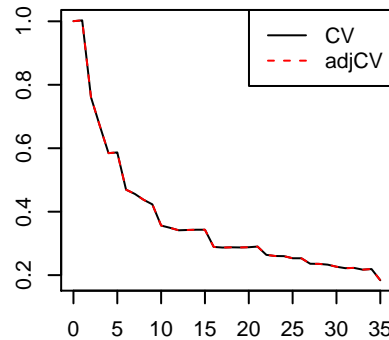
f2\_7



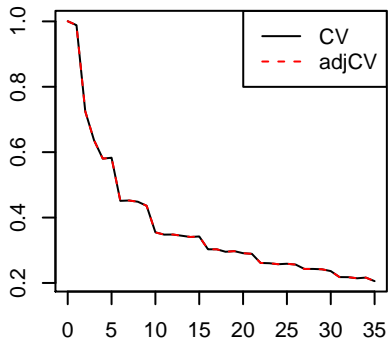
f2\_8



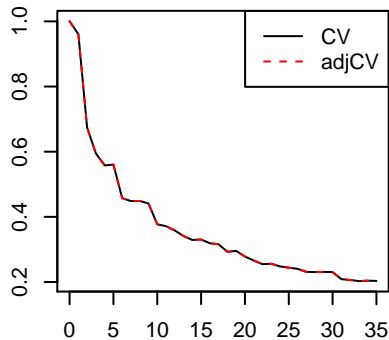
f2\_9



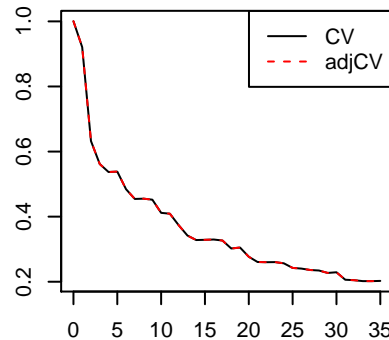
f2\_10



f2\_11



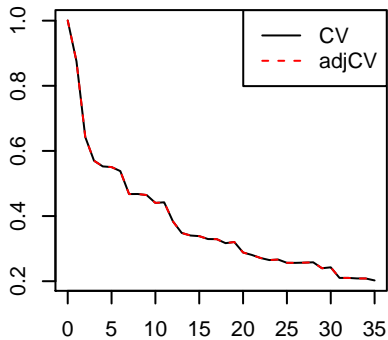
f2\_12



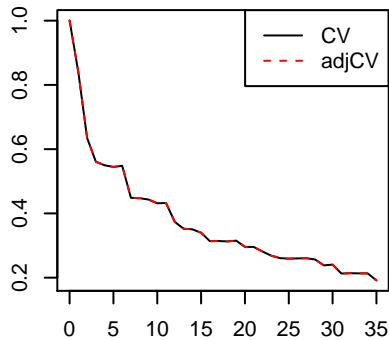
number of components

RMSEP

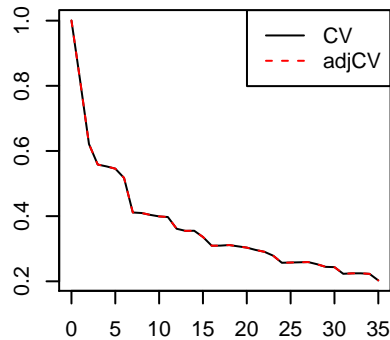
f2\_13



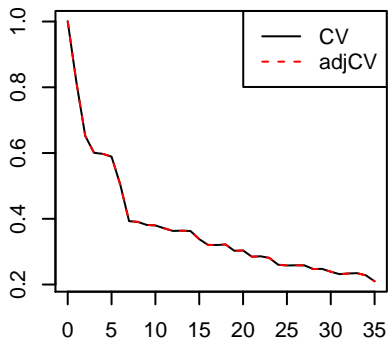
f2\_14



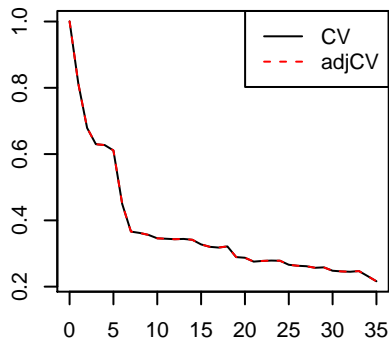
f2\_15



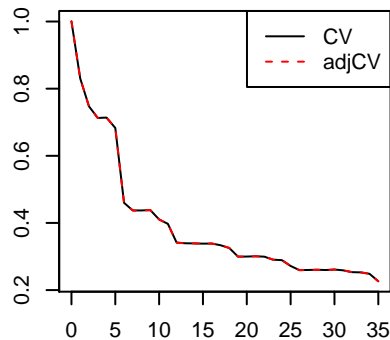
f2\_16



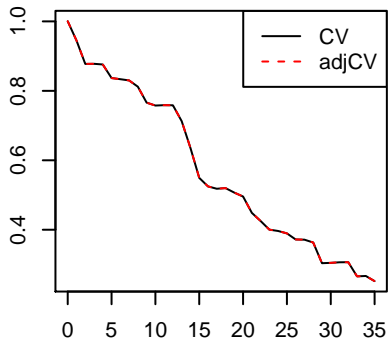
f2\_17



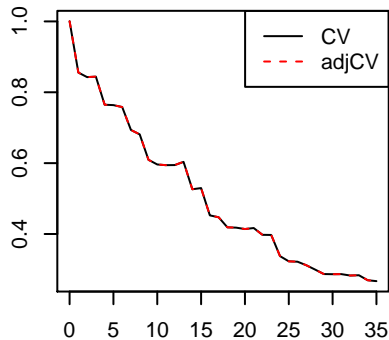
f2\_18



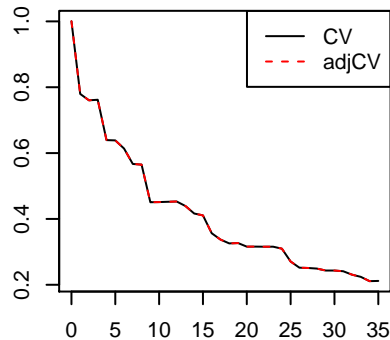
f3\_0



f3\_1

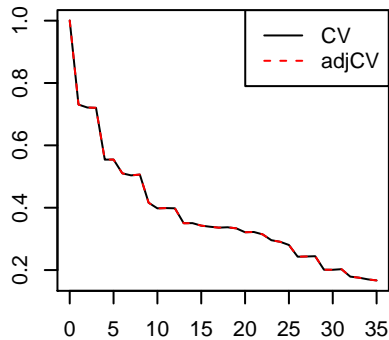


f3\_2

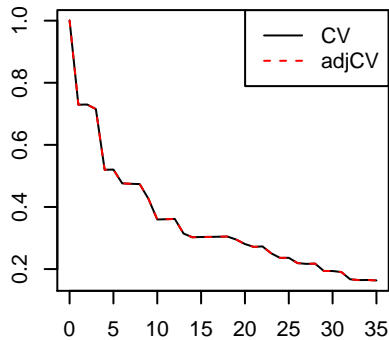


number of components

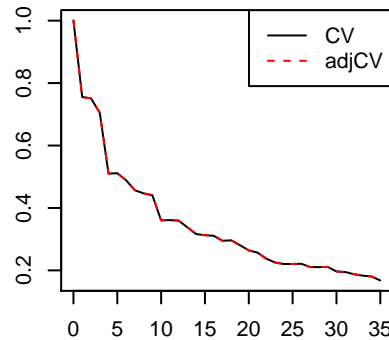
f3\_3



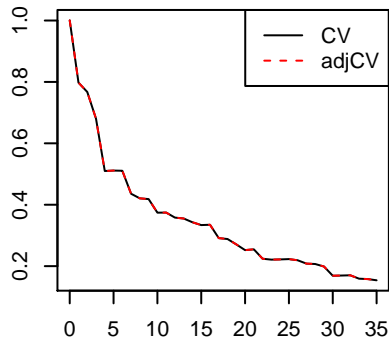
f3\_4



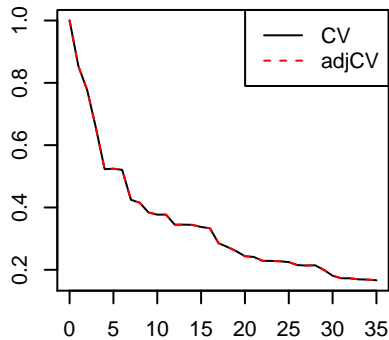
f3\_5



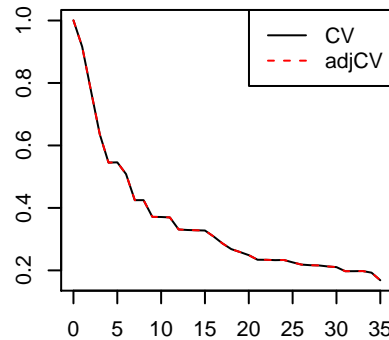
f3\_6



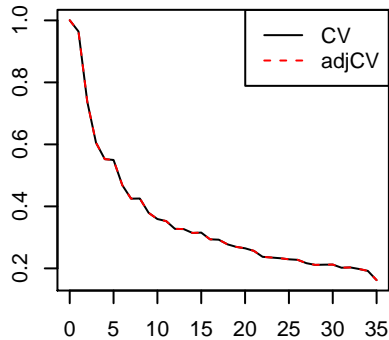
f3\_7



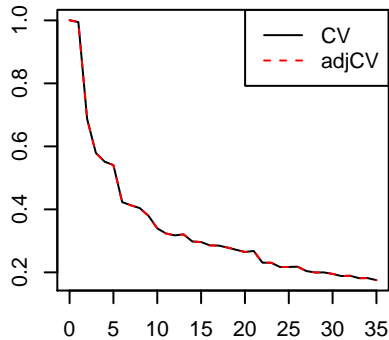
f3\_8



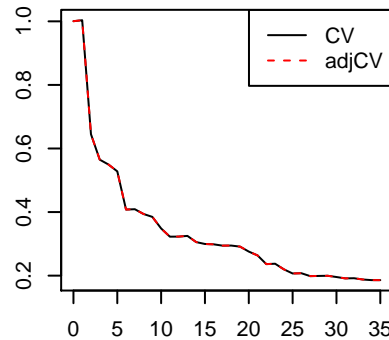
f3\_9



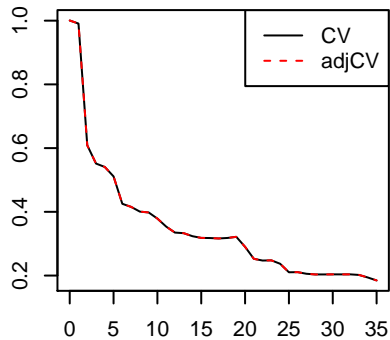
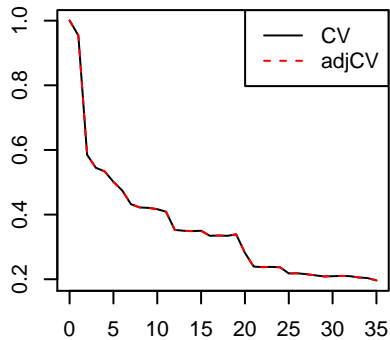
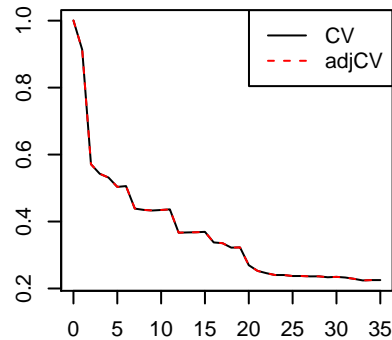
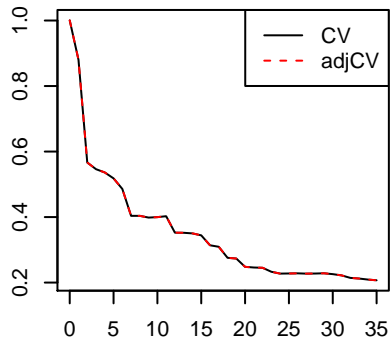
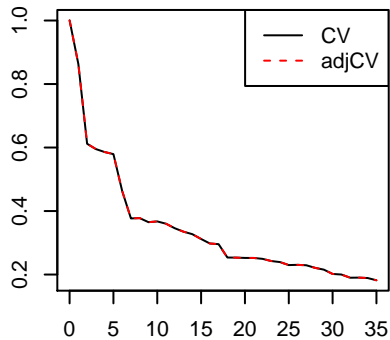
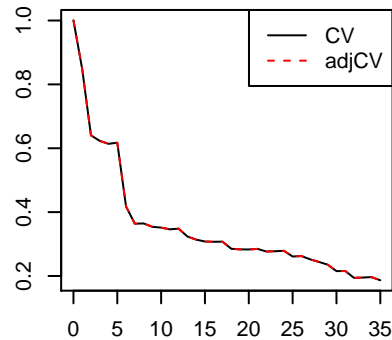
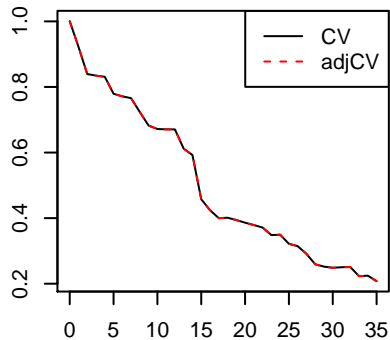
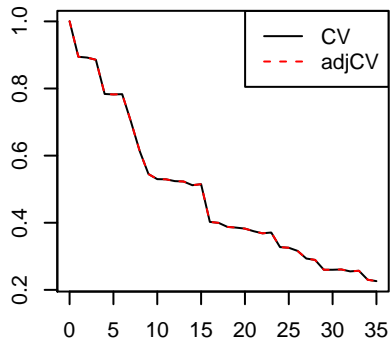
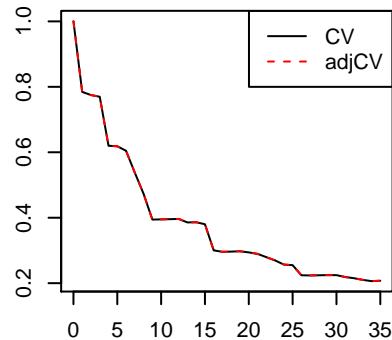
f3\_10



f3\_11

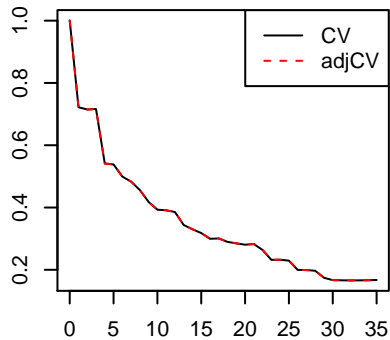




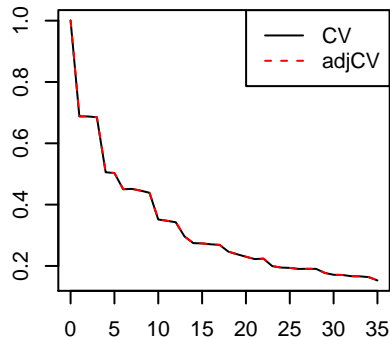
**f3\_12****f3\_13****f3\_14****f3\_15****f3\_16****f3\_17****f4\_0****f4\_1****f4\_2**

RMSEP

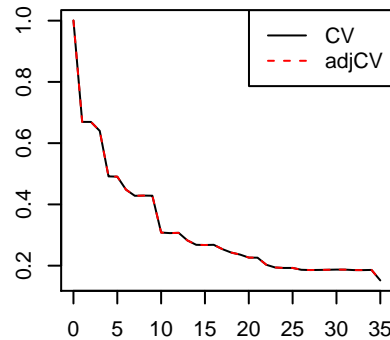
f4\_3



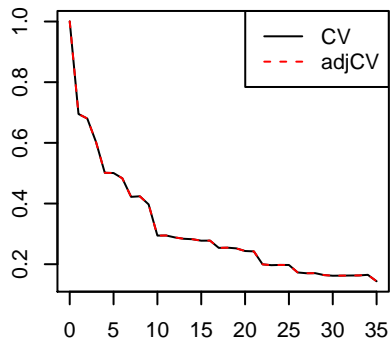
f4\_4



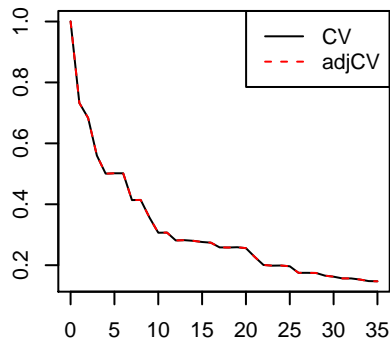
f4\_5



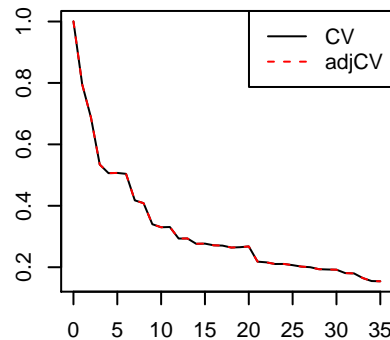
f4\_6



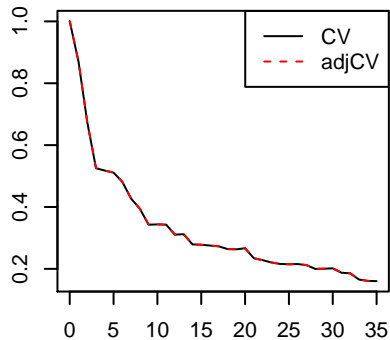
f4\_7



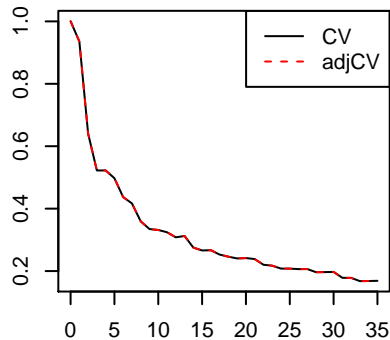
f4\_8



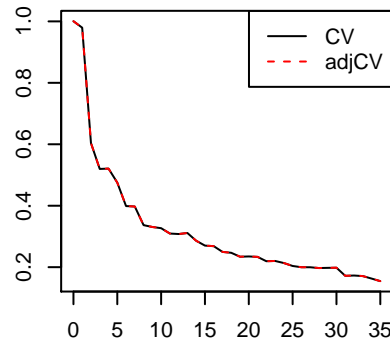
f4\_9



f4\_10



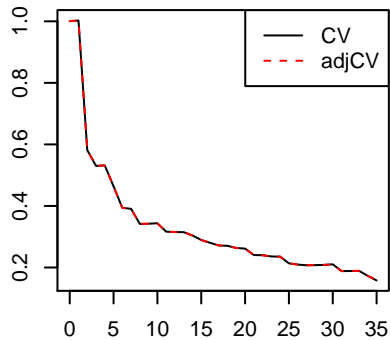
f4\_11



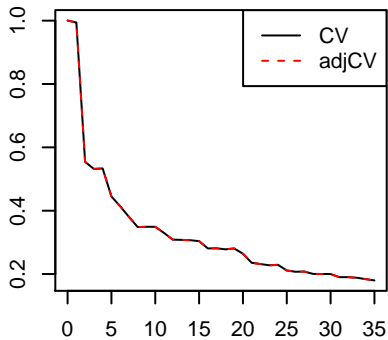
number of components

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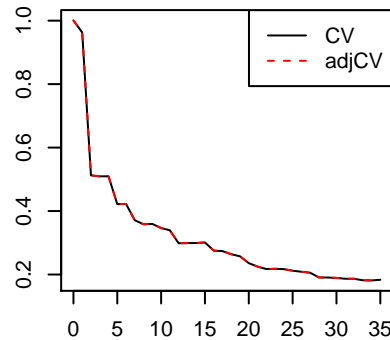
f4\_12



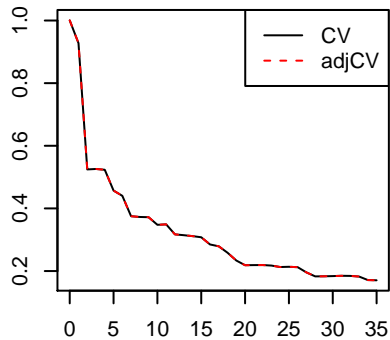
f4\_13



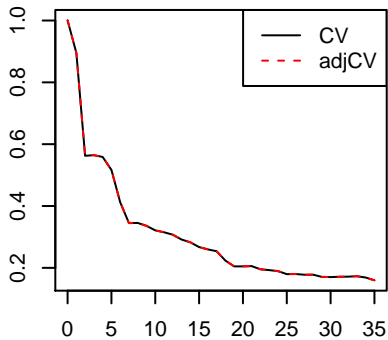
f4\_14



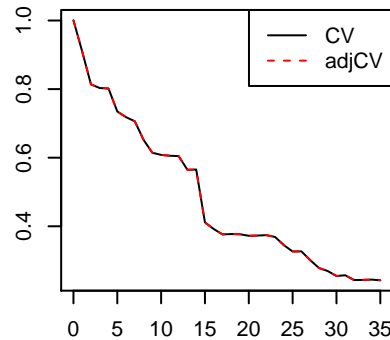
f4\_15



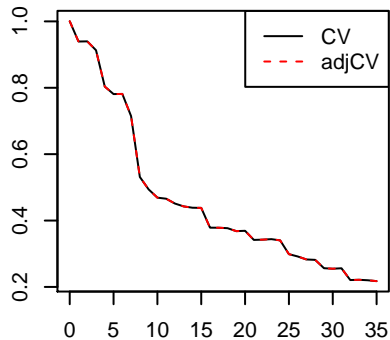
f4\_16



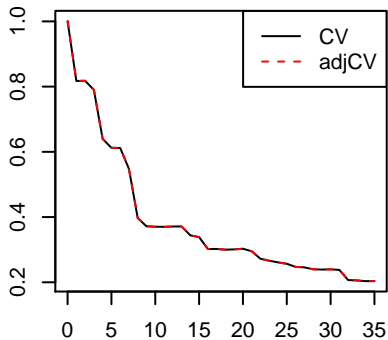
f5\_0



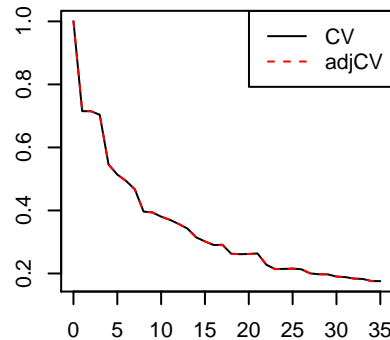
f5\_1



f5\_2



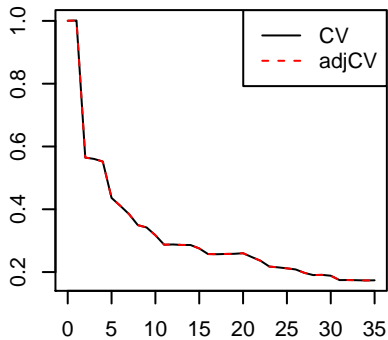
f5\_3



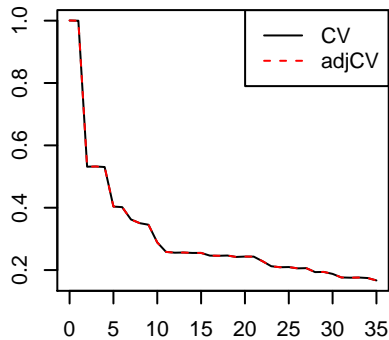
number of components



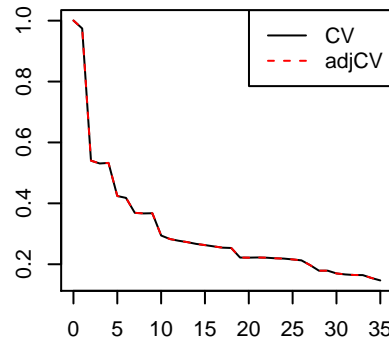
f5\_13



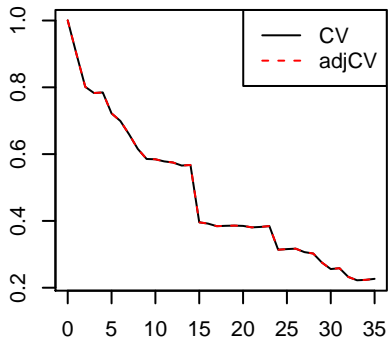
f5\_14



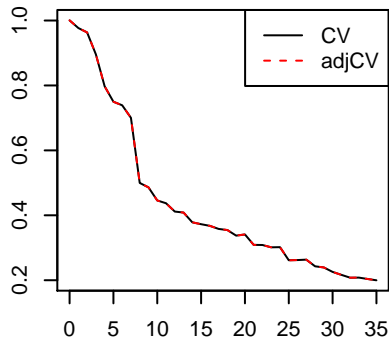
f5\_15



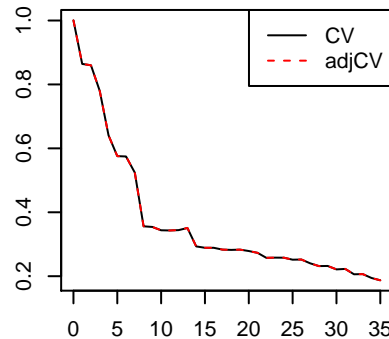
f6\_0



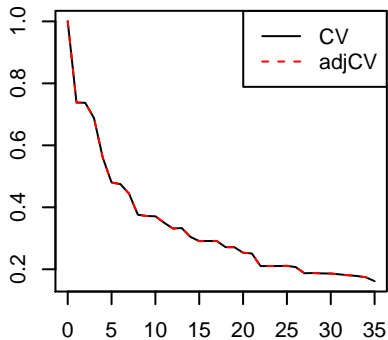
f6\_1



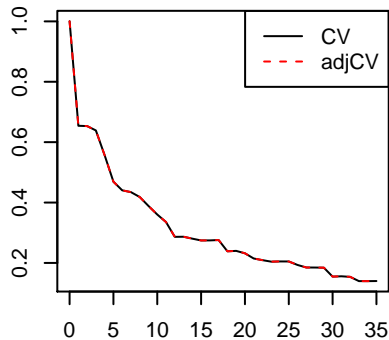
f6\_2



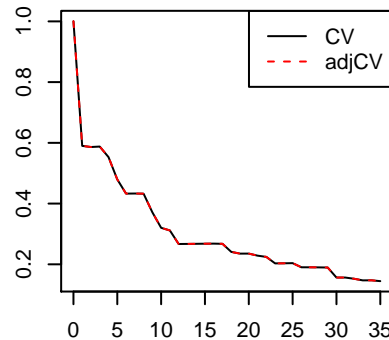
f6\_3



f6\_4

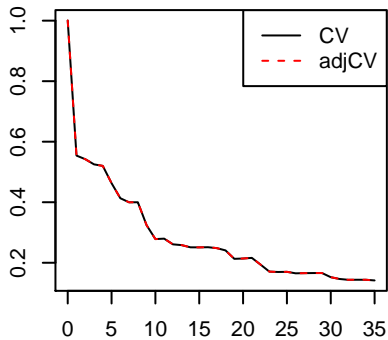


f6\_5

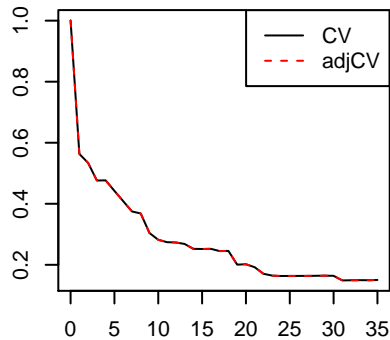


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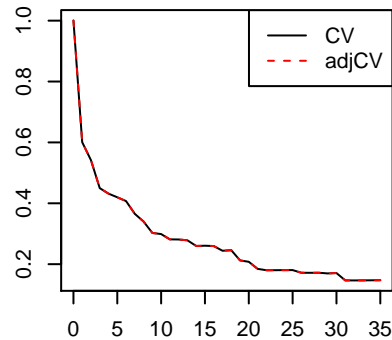
f6\_6



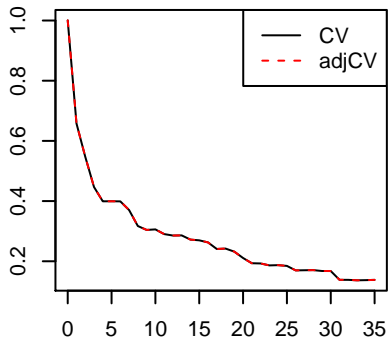
f6\_7



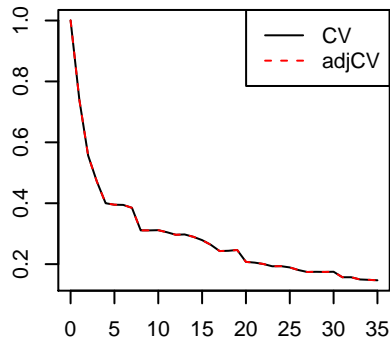
f6\_8



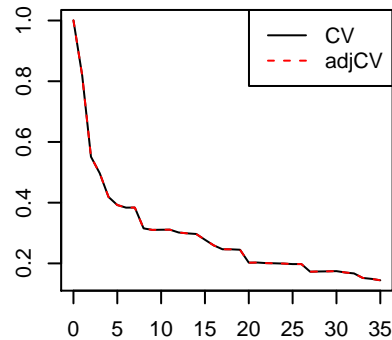
f6\_9



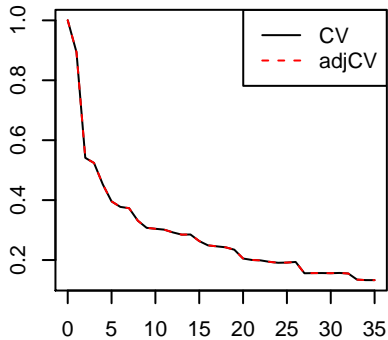
f6\_10



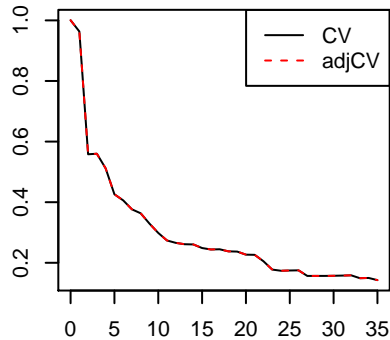
f6\_11



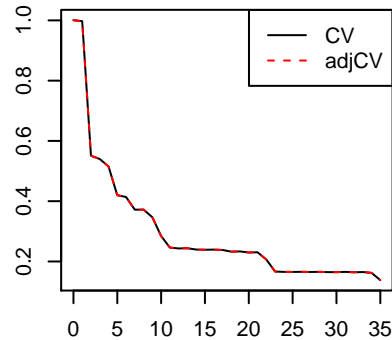
f6\_12



f6\_13

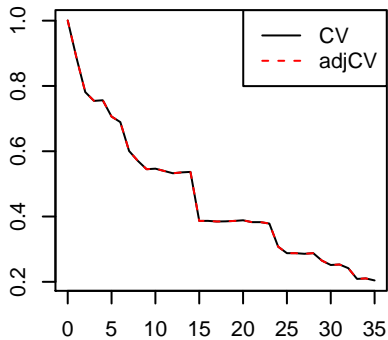


f6\_14

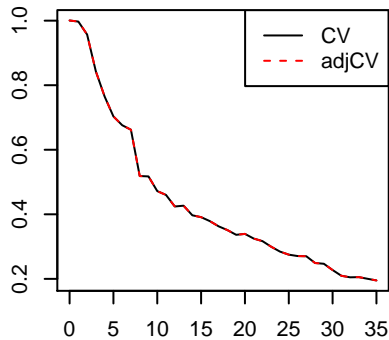


number of components

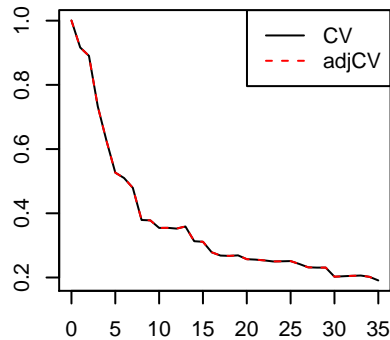
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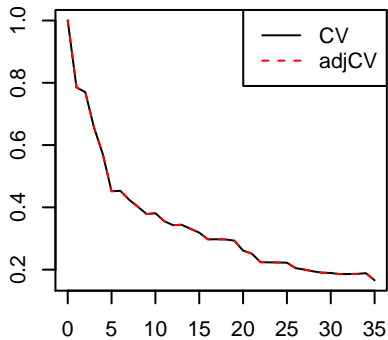
f7\_1



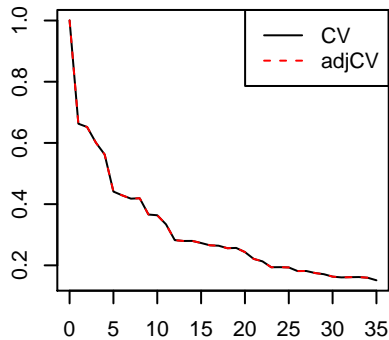
f7\_2



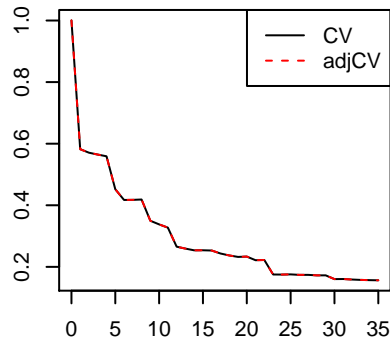
f7\_3



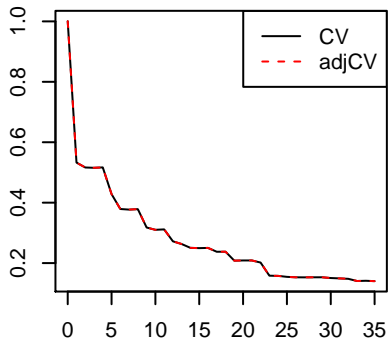
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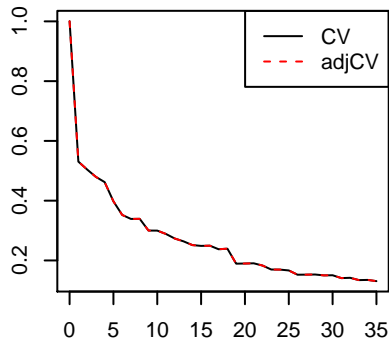
f7\_5



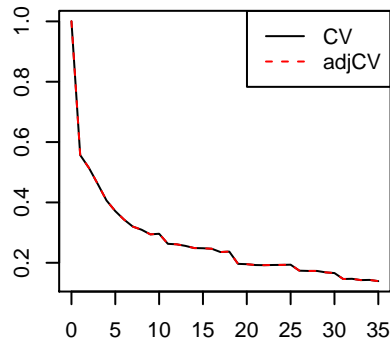
f7\_6



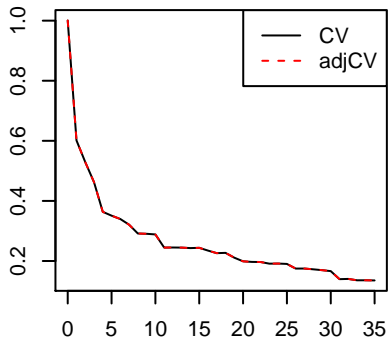
f7\_7



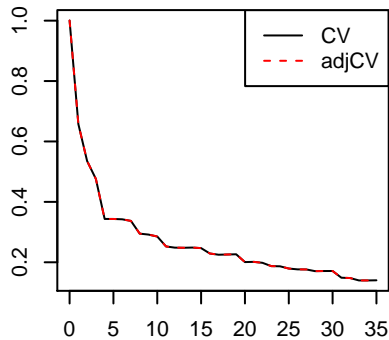
f7\_8



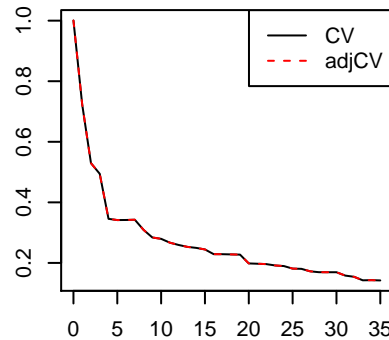
f7\_9



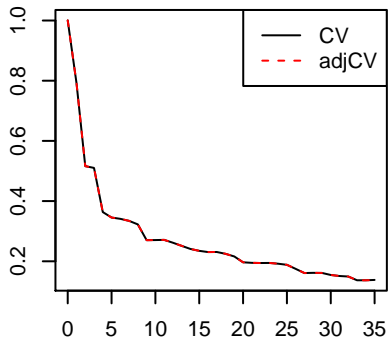
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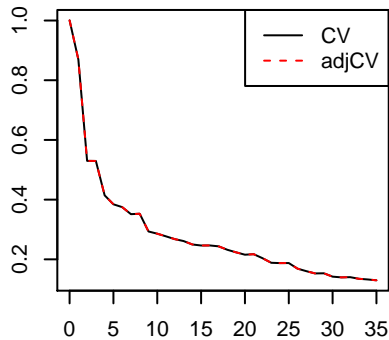
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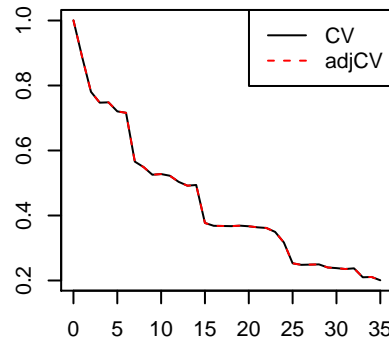
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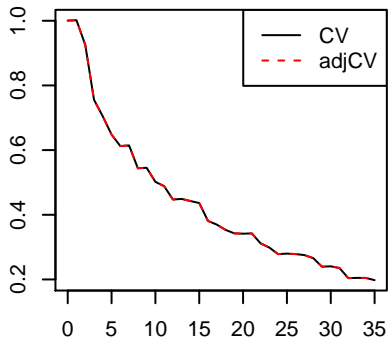
f7\_13



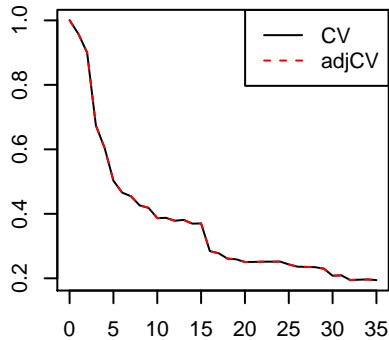
f8\_0



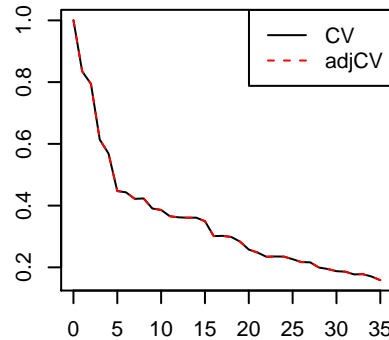
f8\_1



f8\_2



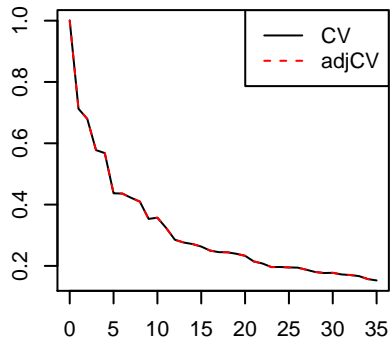
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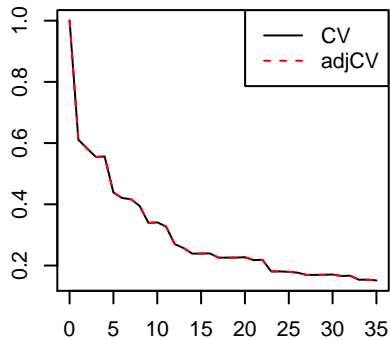


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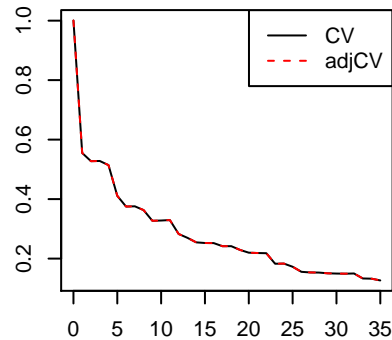
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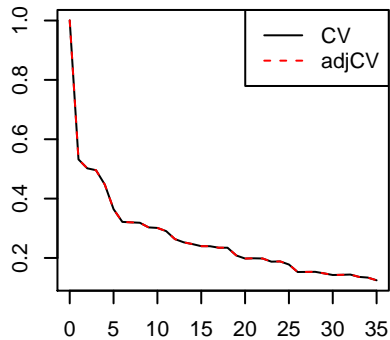
f8\_5



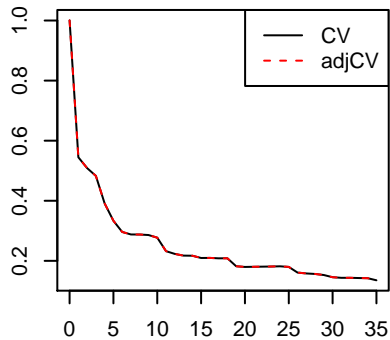
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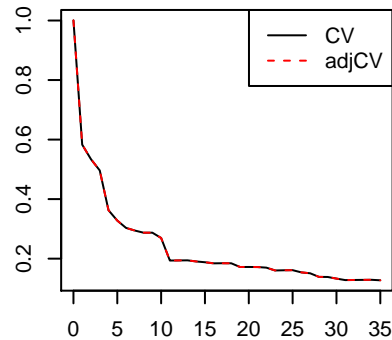
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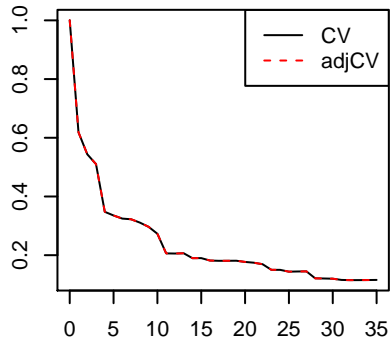
f8\_8



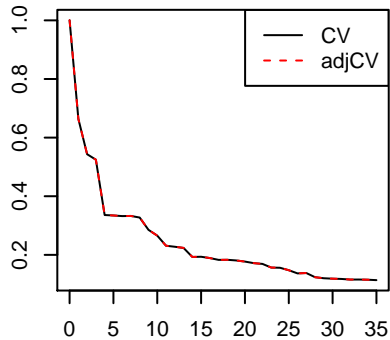
f8\_9



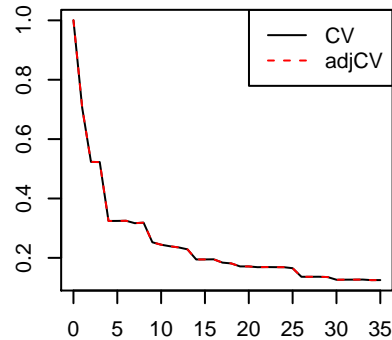
f8\_10



f8\_11



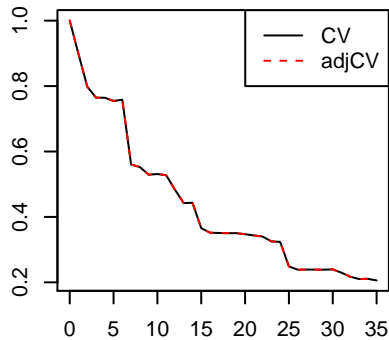
f8\_12



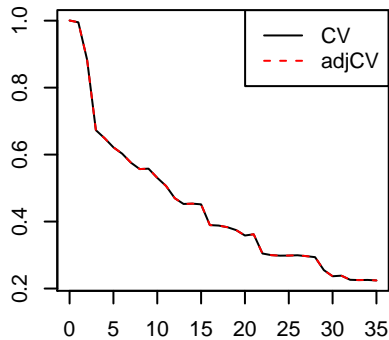
number of components

RMSEP

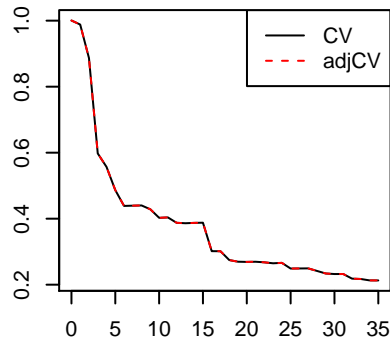
f9\_0



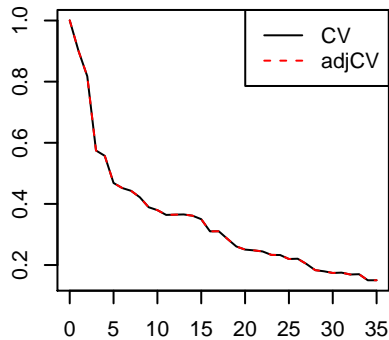
f9\_1



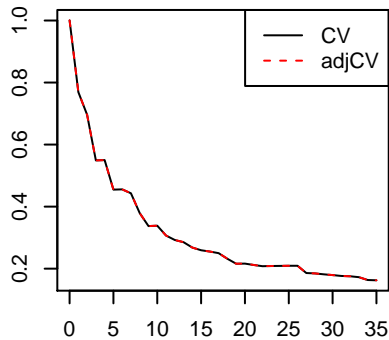
f9\_2



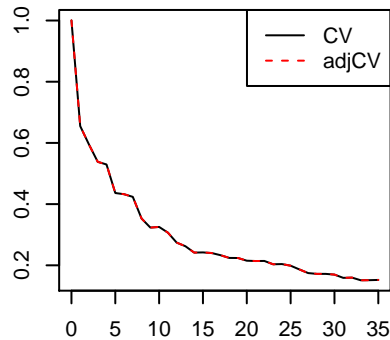
f9\_3



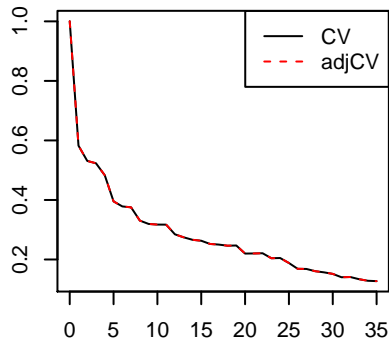
f9\_4



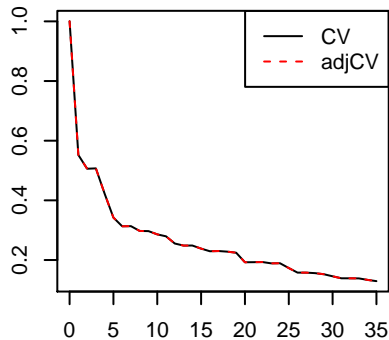
f9\_5



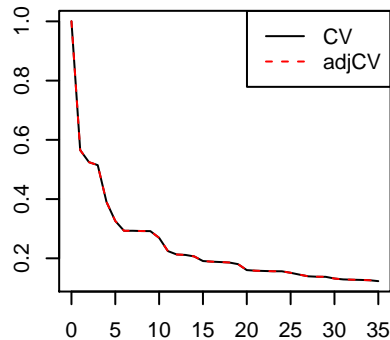
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f9\_7



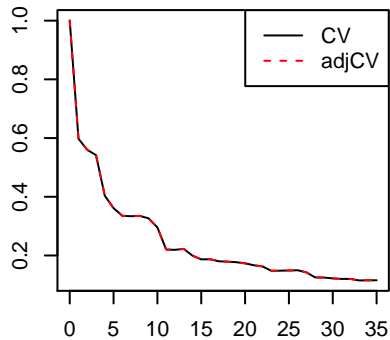
f9\_8



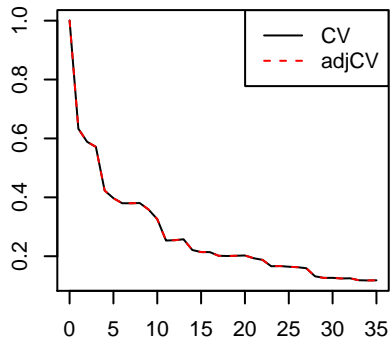
number of components

RMSEP

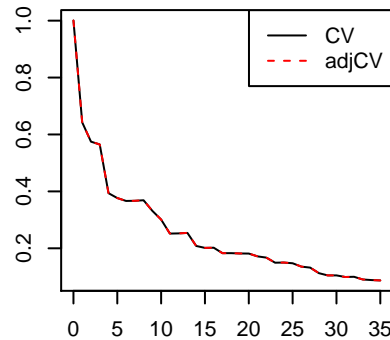
f9\_9



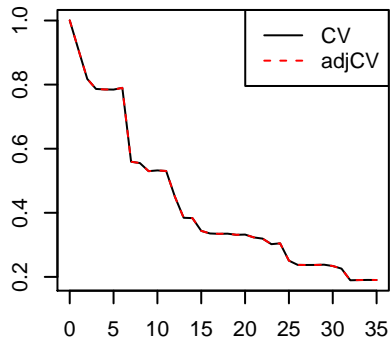
f9\_10



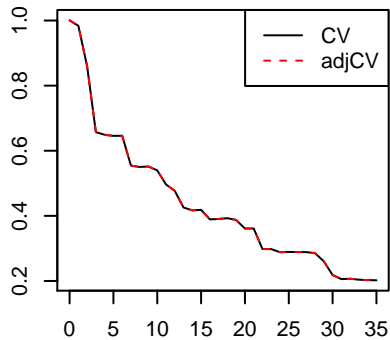
f9\_11



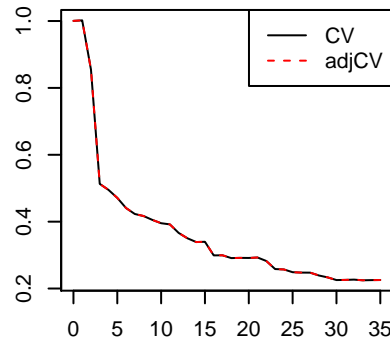
f10\_0



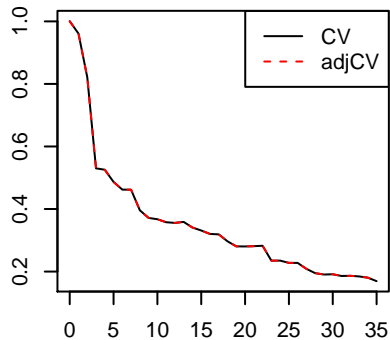
f10\_1



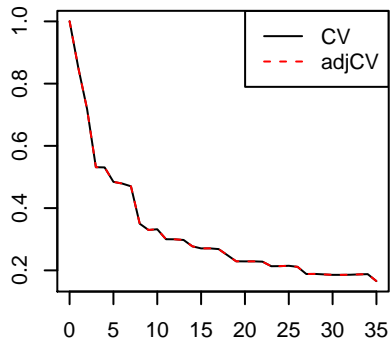
f10\_2



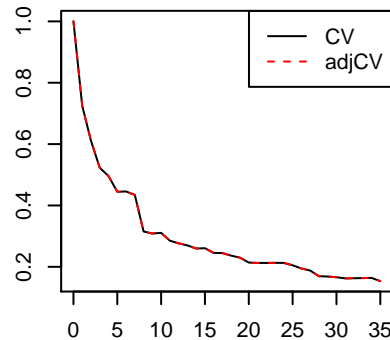
f10\_3



f10\_4



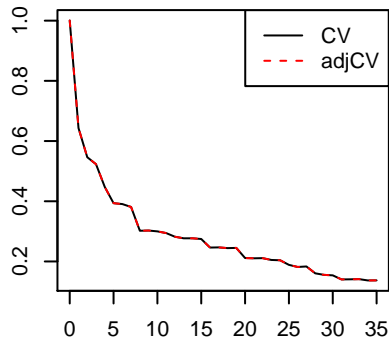
f10\_5



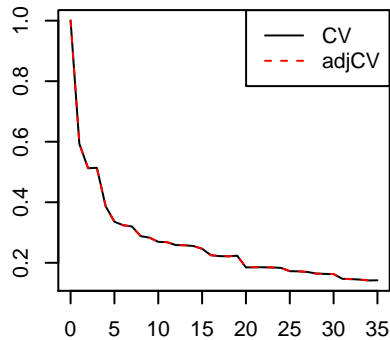
number of components

RMSEP

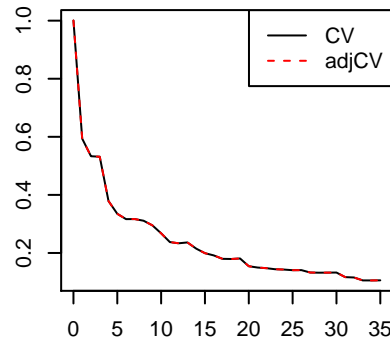
f10\_6



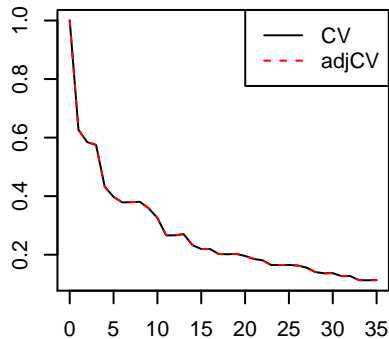
f10\_7



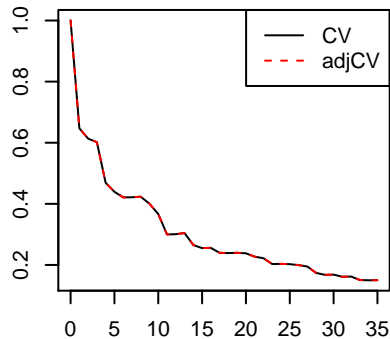
f10\_8



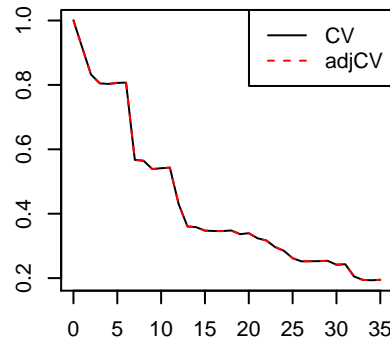
f10\_9



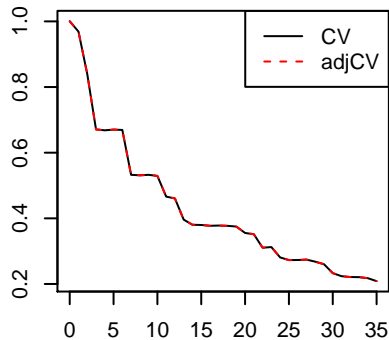
f10\_10



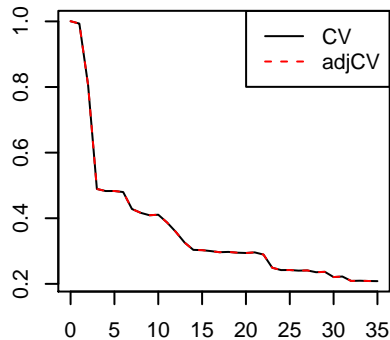
f11\_0



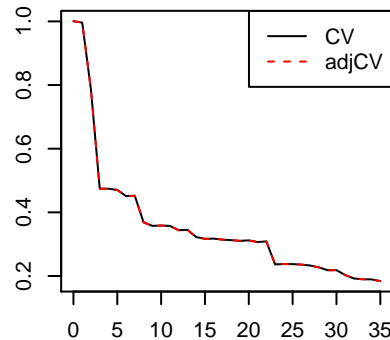
f11\_1



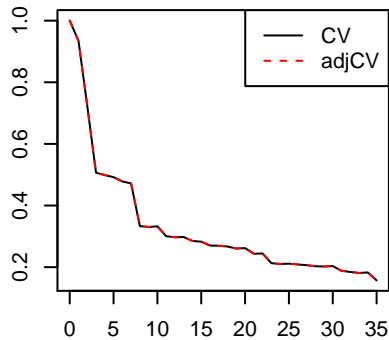
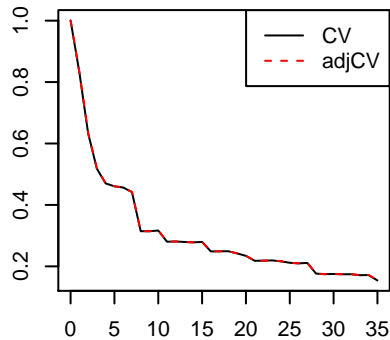
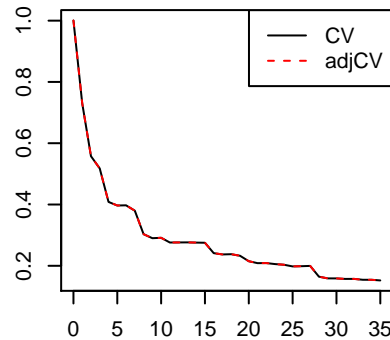
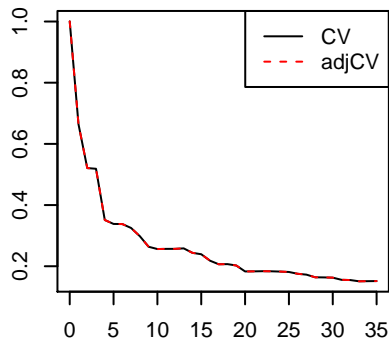
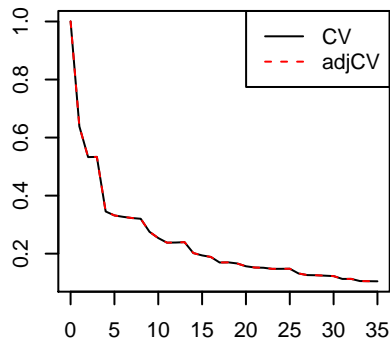
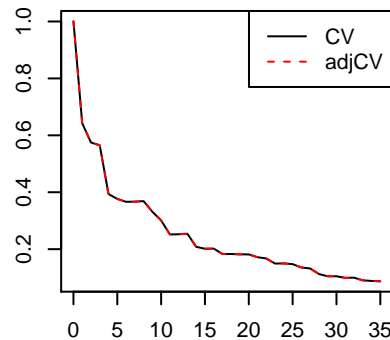
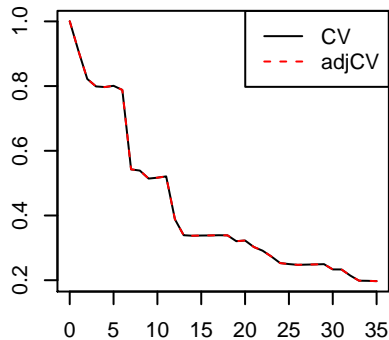
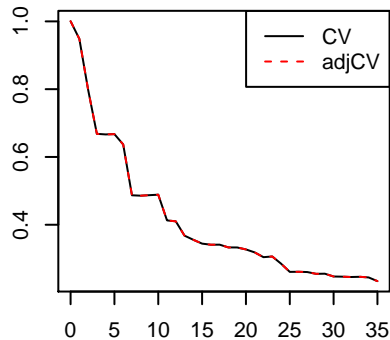
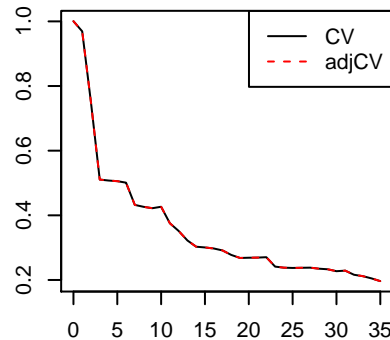
f11\_2



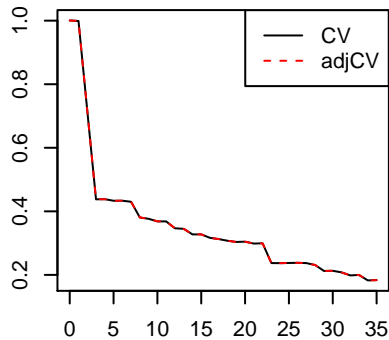
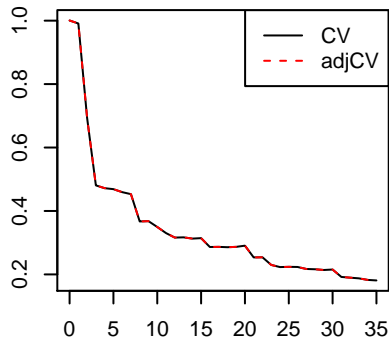
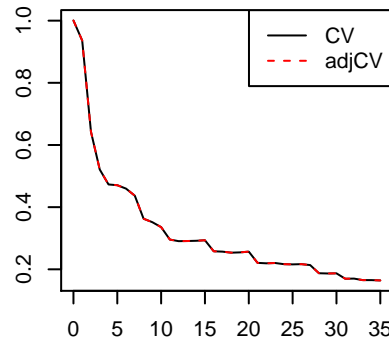
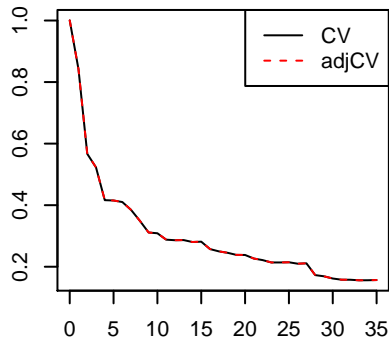
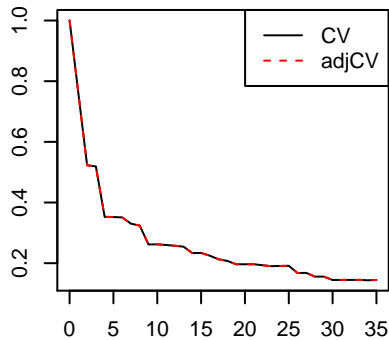
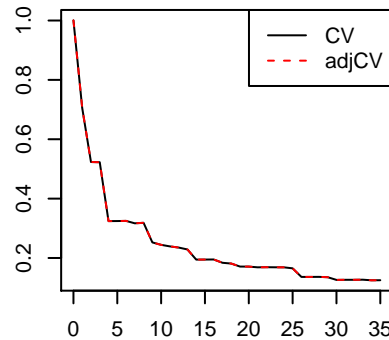
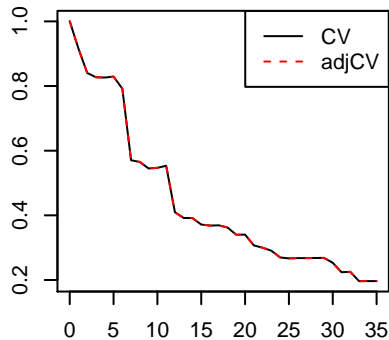
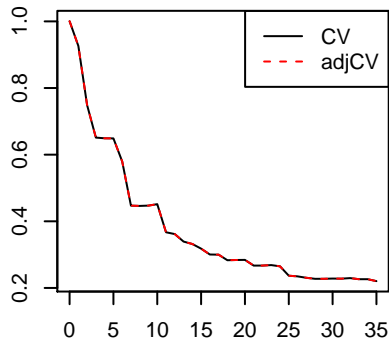
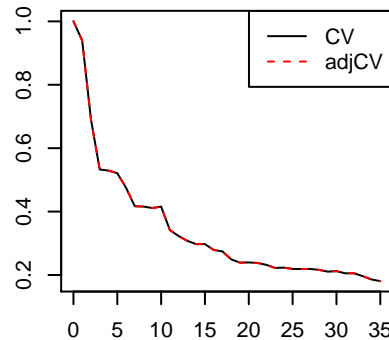
f11\_3



number of components

**f11\_4****f11\_5****f11\_6****f11\_7****f11\_8****f11\_9****f12\_0****f12\_1****f12\_2**

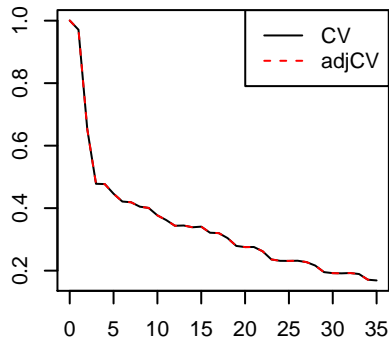
number of components

**f12\_3****f12\_4****f12\_5****f12\_6****f12\_7****f12\_8****f13\_0****f13\_1****f13\_2**

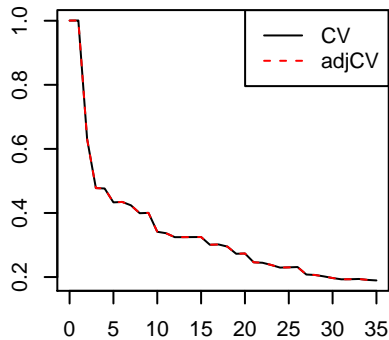
number of components

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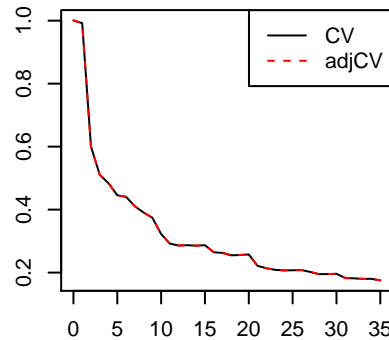
f13\_3



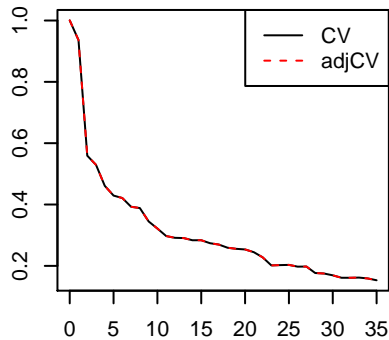
f13\_4



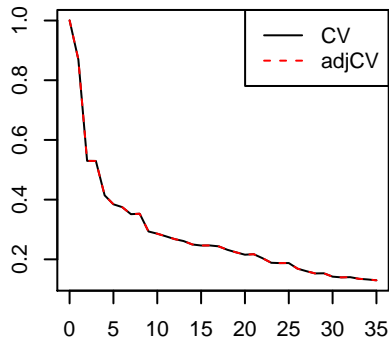
f13\_5



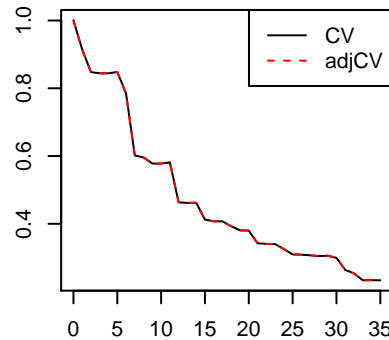
f13\_6



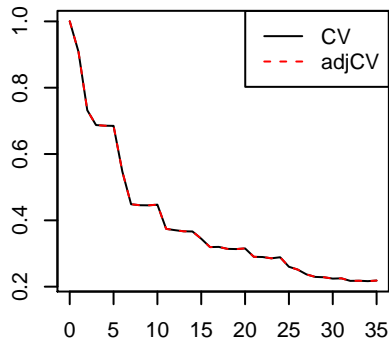
f13\_7



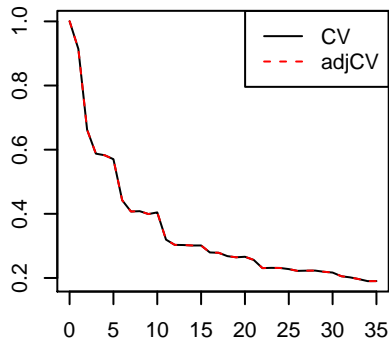
f14\_0



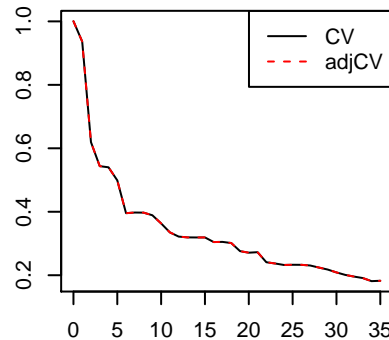
f14\_1



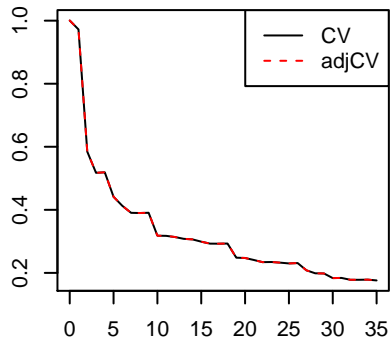
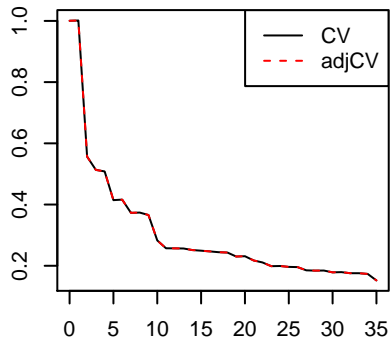
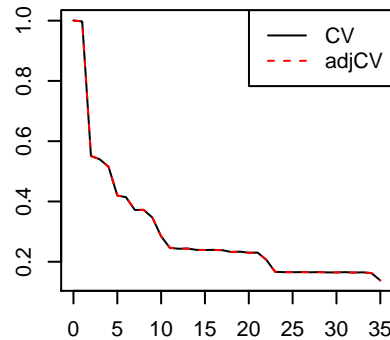
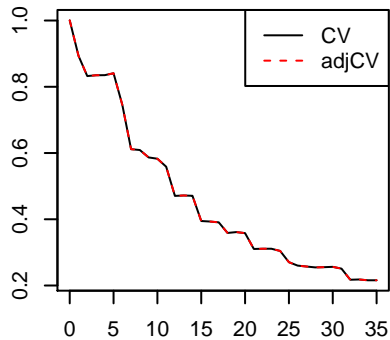
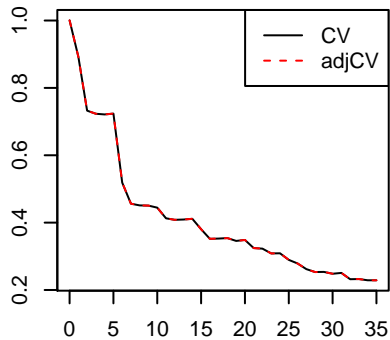
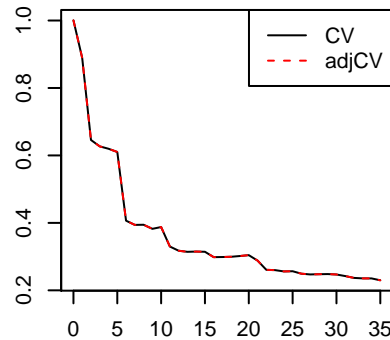
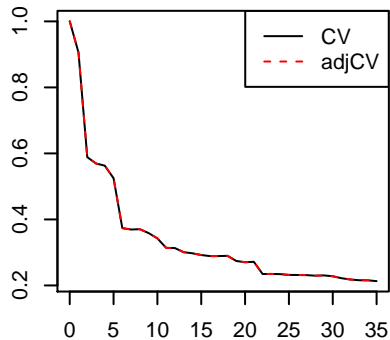
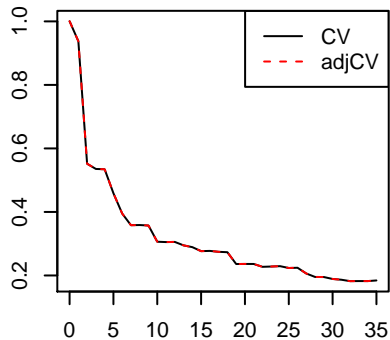
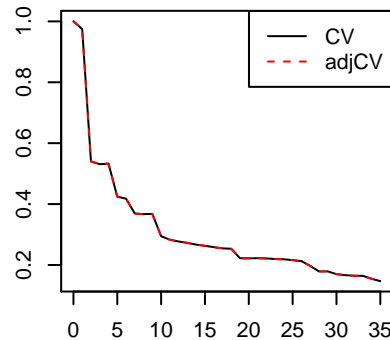
f14\_2



f14\_3



number of components

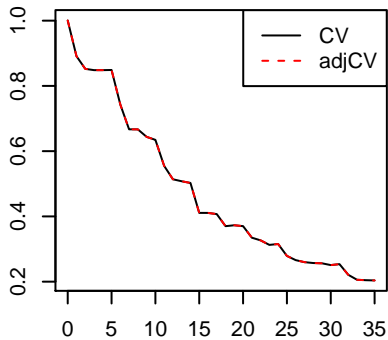
**f14\_4****f14\_5****f14\_6****f15\_0****f15\_1****f15\_2****f15\_3****f15\_4****f15\_5**

number of components

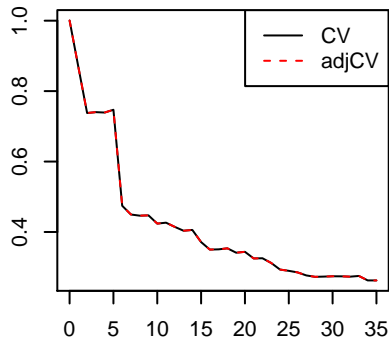


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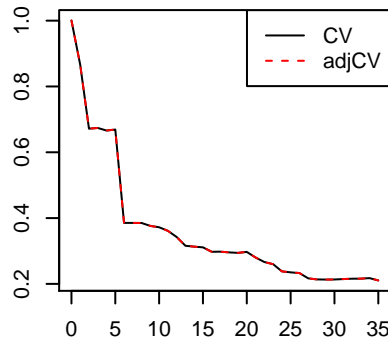
f16\_0



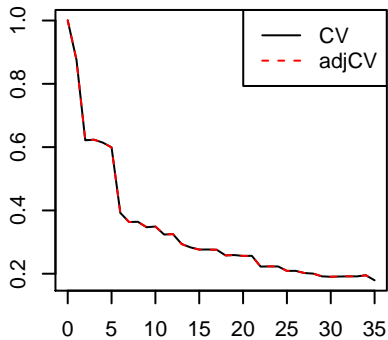
f16\_1



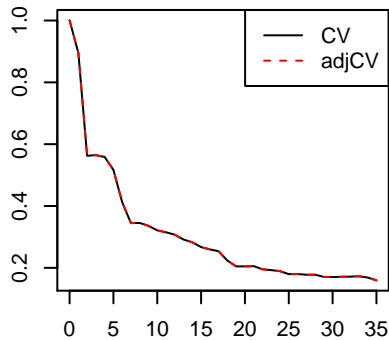
f16\_2



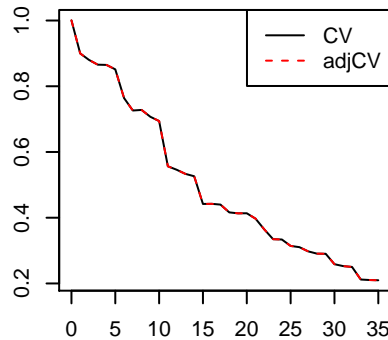
f16\_3



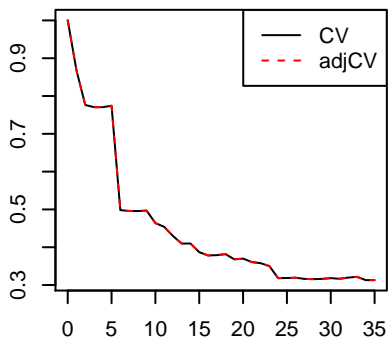
f16\_4



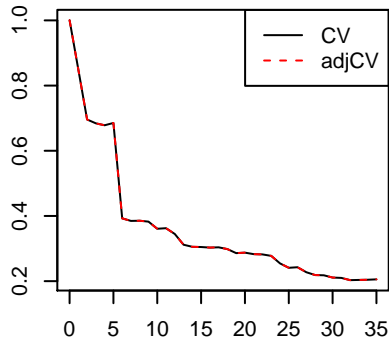
f17\_0



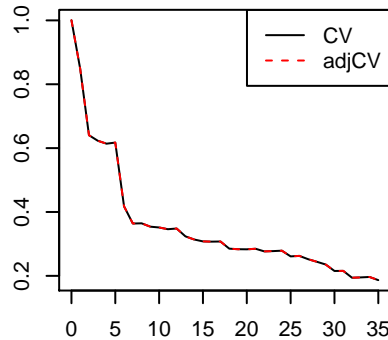
f17\_1



f17\_2



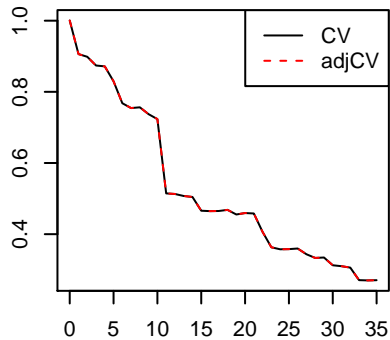
f17\_3



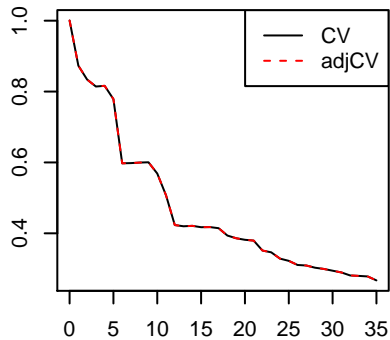
number of components

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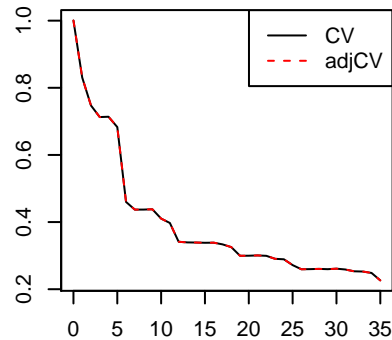
**f18\_0**



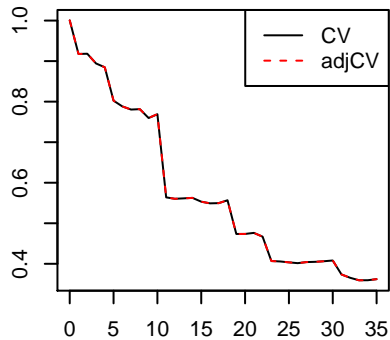
**f18\_1**



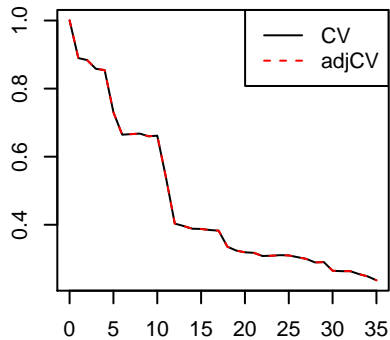
**f18\_2**



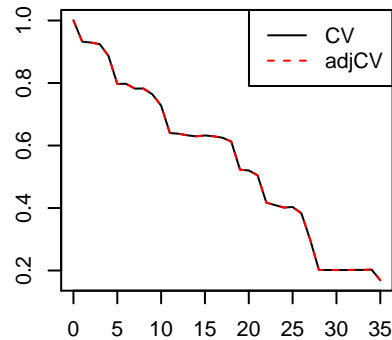
**f19\_0**



**f19\_1**



**f20\_0**



number of components