LECTURE 19 - LED SWITCH LED CONTROL USING SWITCH - GPID : LPC2148 In To avoid floating set values
pull down assistance Input [smitch]

	*	STEPS :	FOR LED	CONTROL	USING SWITCH: Soutput [450]							
	0	O Configure functionality of PO-D and PO-1 as GPID Part										
		, 0	PIN	SELO =	0x00000000;							
	0	Configure Direction of POO as Input Pin 100DIR II = OXFFFFFFFFF; the other bits										
	3	Configure Dilection of Poil as output Pin 100DIR 11-0x00000002; the other bits										
	(F)	theck	y Swi	tch con	rected to POD is Pressed							
7		if (CIDPINO & OXOI) ==1)										
	1	If Pressed, Switch ON IED connected to POI										
2		10SETO 1= 0x00000002;										
	B	Else Switch OFF LED connected to PO.1										
•		10 CLR 0 1 = 0x00000002;										
	,											
	*	STEP 1 - PINSELO = 0x00000000;										
	, -	PINSEL Registers: - Especial Function Register (SFE)										
	-	PINSELO- POO to PO-15										
		PINSELI - PO.16 to PO.31										
	1	PINSEL 2 - PI-16 to PI-31										
		13. 26:301 Bolds 8 271151 1154 8 8										
	->	PINSELO Register:										
		Table - Pin function Select register o (PINSTIO - address										
		0x to02 (000) bit description										
		Bit	Symbal		Aunction	Reset						
		1:0	PO·D	00	GPJO Port 00	0						
				01	TXD (VARTO)							
				10	PWMI							
				11	Reserved							
		3:2	Poil	00	GPIO Pout 0.0	0						
				01	RKD (VARTO)							
		,		10	PWm3							
				11	EINTO							

	7								J		1
\	*	STEP	- 10001	RR= 0	X FFFF	FFFE;	a de la companya del companya de la companya del companya de la co			7 4	
				_ 0		-					
	1 1/4	Bit 31	1	Bit H	git,	16	Bit :	e Bit.	B	ito	
	1. 1				1				-	2	
	*	PD-31)	80.17	PO-1	6	P0.2	Po.1	6	PO.0	
					4						
		Table -	GPIO PO	ret 1 D	inection	r negis	ter (101	DIR - adl	dress	5	
		OX EOOS	2 8018) b	it des	criptio	n					
-		-0		e e e e e e e e e e e e e e e e e e e	- Contraction of the Contraction	0 00	PROMISE AL BURNING MORE			Reset	-
		Bit	Symbol	Value		uption	A A		0	Reset	
		31:0	PIXDIR		į.	-	ection c		_	ř	200
	1				ž.		IR contre		_Bit	000	0
	1'		2007				controls			7,1	
					Contr	oleed pir	n is inpe	xt.			
	ie L			and the same	Contro	elled pin	is out	sut.	<u> </u>		
	O C	1 /	04,1 60	W1 - 1 6	100	13 (3)	9. 7.7	1 43 1		J .	*
1	*	STEP 3 -	- LOODIR	1= 0x	00000	002	Y 4 4 4	1 . 35			
+	86	3	01.0	19 W	09	<u> </u>	1 36 19	-0.	00.		, i 1, - t
4	0	3it 3!		But 7	Bet 16	00000	Bit 2	Bet 1	Bet	0	
1	81	· 1/1.	09 31.	ill Pi					, ,	1	
4	91	P0.31	14.000 F	277	PO-16		20.2	PO-1	POST	2	
1	4	1 30	02 6320	1 : 15.	and the same of th	C-31 1	्रे हिंदू	7 90 ¹	- 54	1 . ;	V.
		Clear	Set	Sp	lit a	Bit				29.6	
	0	¥ 11 1 1	HOI	+	131	K.	W. W.	010	SI.	u ^V ;	
	3	101	1 0010	Q	(0010)	97.1					
	1	101	1111	-	0010		d d	and the second			
						/					
	k S7	EP 4-	9f (110)	PNOK	oxol):	271);					
							2 E	, t			
	8.	t 31	B	it M	Bit 16		Bit I	Bitl	BI	£0	
	1	1				_			RI	0	
		V 0.31	••••	DM	10.16	••••	Por	\$0.1	SPECIAL PROPERTY.	.0	
	<i>P</i>	0.31		0 (1	10.0		70.7	***	1.0		

*	STEP 5	- IDSET	o se c	× 00000	0002 ;		. ,	λ,		
	STEP 5 - 10SET 0 1 = 0x000000002;									
	Bit 31	- 20 M. C. 18 18 18 18 18 18 18 18 18 18 18 18 18	Bit 17	Bit 16		Bit 2	Bit 1	Bit o		
		nder and an antique of the place of the plac	and the second second second second second second	Augustus Sangeroopel Charles and Augustus Augustus Charles			1			
	10.31	****	Port	PO 16		P0.2	PO-1	PO.0		
*	STEP 6	,	49							
	STEP 6 - 10CHRO 1= 0x000000002;									
	Bit 31	• 2 0 0	Bit 17	Bél 16	v · · · ·	Bit?	Lit!	Bito		
	Po.3/	A PO V89	. Po. 13	¥0.16		40.21 X	Pa-1	· V Po·D		
- 225	PD-31 - 2000 PO-12 PO-16 PO-16 PO-10 PO-10 PO-10									
*	REGISTER SUMMARY - GPSD:									
	The residence of the state of t									
	15 14	13 12	11 10	9 8	7 6	5 4	3 2	1 0	The second second	
PINSELO	Po-7	PO-6	Po·5	PO 4	P0:3	PO-2	POI	PO.0	1	
v.E.	31 30	29 28	21 26	25 24	23 22	21 20	19 18	17 16	1	
PINSELD	P015	PO-14	PO:13	P012	PO-11	PO-10	PO-9	P0.8		
	15 14	13 12	11 10	9 8	7 6	5 H	3 2	1 0		
PINSEL	P0.23	Po.22	PO.21	PO.20	PO-19	PO 18	PO-17	PD-16		
	31 30	24 28	27 26	25 24	23 22	21 20	19 18	17 16		
PINSELI	Po 31	PO.30	PO-29	PO.28	PO.27	PO.26	Po.25	Po.24		
	.= 14	h- 10			~	-				
PINSELY	15 14	13 12	11 4	3	Walter Street Street			10		
Merr				GPIO TRACE		GP10 IDEBUG		SEL		
	The Mark the form of the									
- 11	,									