LECTURE 22 - UART RX

*	STEPS TO RECEIVE A CHARACTER: (PC TO ARM)							
0	Set PO.O più as TxD & PO.I più as RxD - USARTO)							
	PINSELO = 0x00000005; Il Pin Select 0							
B	Set 8 bits - no Parity - 1 stop bit for Txsion & DLAB=1							
	VOICR - 0x83; 11 Line Control Register							
(3)	Set BAID Rate as 9600 bps 15 MHZ VPB clack (PCIK)							
	VODIN = 97, 11 Divisor Latch ISB							
(9)	Disable Access To Divisor Latches							
	VOLCR = 0x03; 11 DLAB =0 Bit = 7							
6	Wait until VARTO neady with the neceived pata							
	while (! (VOISR & OXOV); Il time status Register - RDR							
6	Read the received Data from VORBR							
A single	data = VORBR; // Receive Buffer Register							
-14	A STATE OF THE PROPERTY OF THE							
*	STEP 5 - while ([(voise & axoi));							
7 7						ç	*v 'v *	
I OXOI	0	0	0	0	0	0	0	RDR
	13. 13%	· · · · · · · · · · · · · · · · · · ·	2	Trinia I	CID	1	0	
	A NAME AND AND A STATE OF A STATE							
+	REGISTER SUMMARY - VART:							
	7	6	5	4	3	2	Colonia A	0
WRBR		i ili	1 (1)		1	Mar In	1 11/1	
VOTHR	Concept of a water of the reserve							
VODIL		210	Same A	142-1-6		1777.6		14
VODEM			>	. 31 C	S 12 12 1	44.4	y shad down	1211
borer	D. T	6	111.30 223			Tx Fifo	Rx Fifo	Rx
	Rx Trigger					Reset	Rst	Enable
VOLLR	DLAB	8et	sticky	Even	Parity	No. of	word fength	
		Break	Parity	Par. Sel	Enable	StopBit	Seko	
VOUSR	Rx	TENT	THRE	BI	FE	PE	· OE	RDR
	PIPOEN				1 100 1	day	3 117	100133
VOFDR	multal				. 4 Km2 2	DivA	ad Val	14
DOTER	TXEN							