

CCA, Artemis Plus

11/1/2023

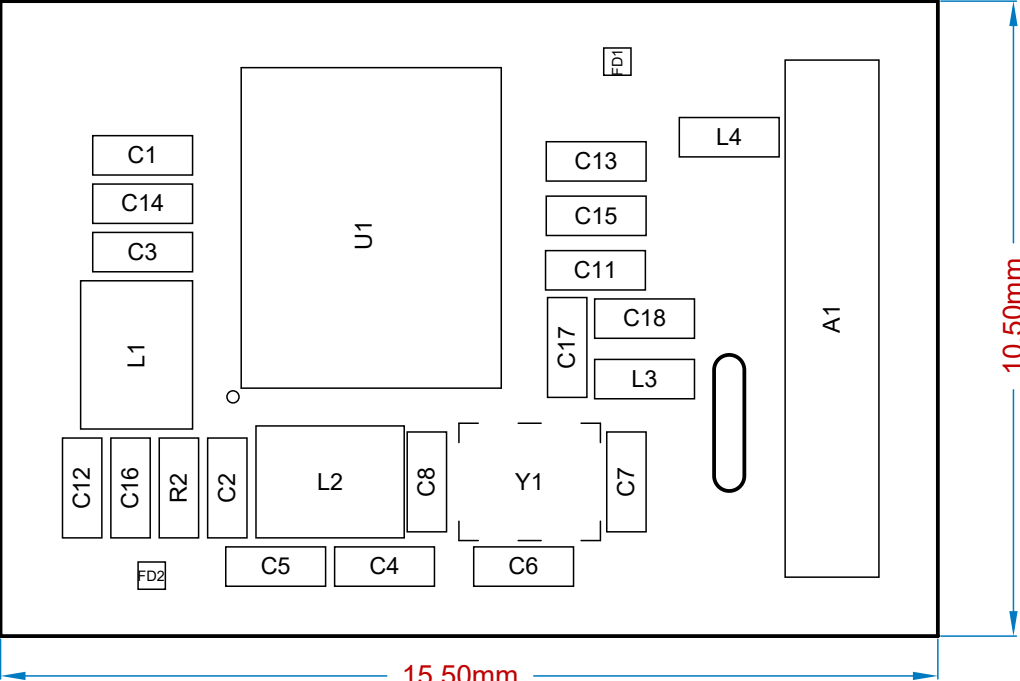
Rev: C

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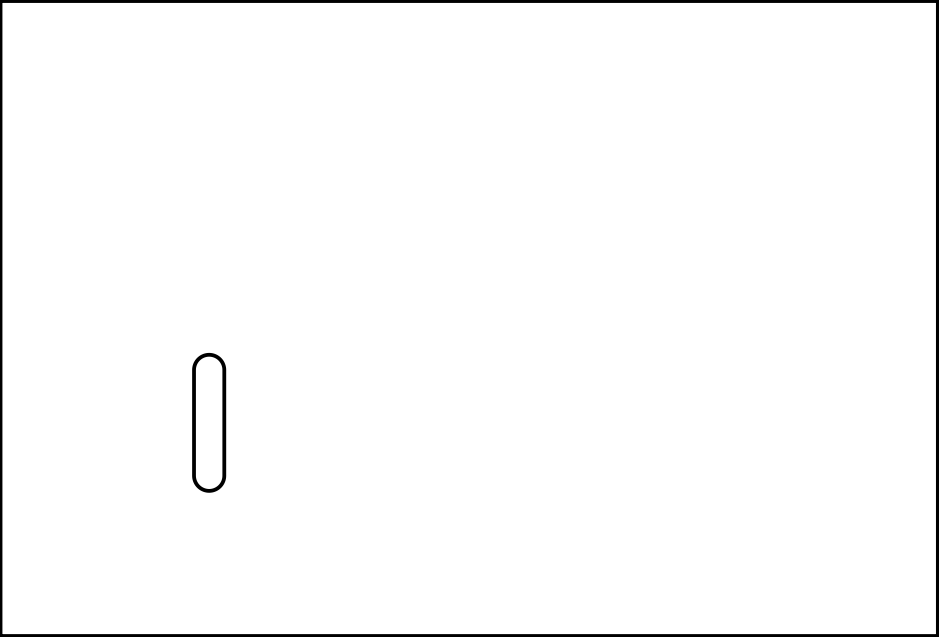
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Modified Design: Sapphire Circuits, LLC J. Mizrahi
Original Design: SparkFun Electronics, N. Seidle

Project: Artemis Plus		
Title: Cover Page		
Size: A	Number: 1012	Revision: C
Date: 11/1/2023	Time: 3:27 PM	Sheet: 1 of 7
File: Artemis_Plus-CCA.PCBDwf		

View from Top side (Scale 8:1)

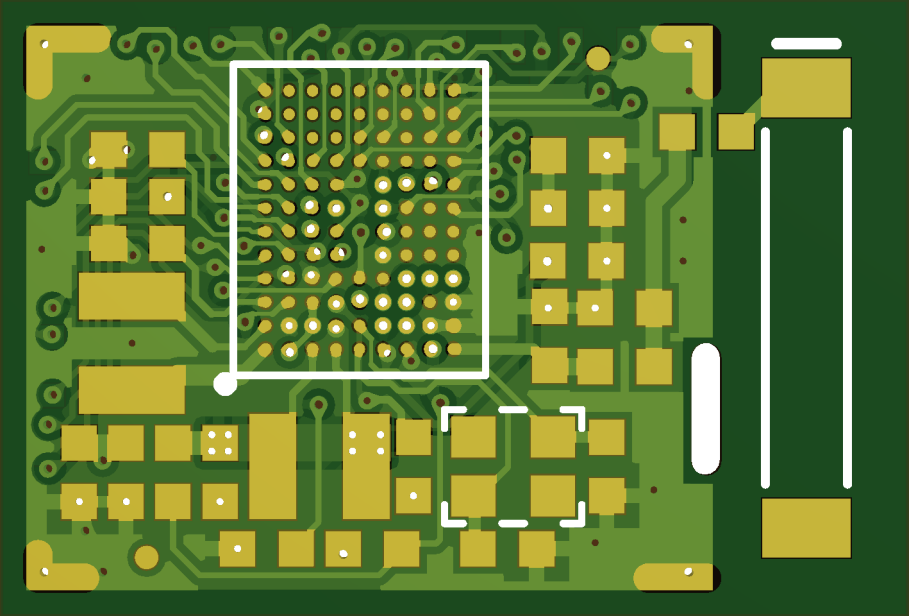


View from Bottom side (Scale 8:1)

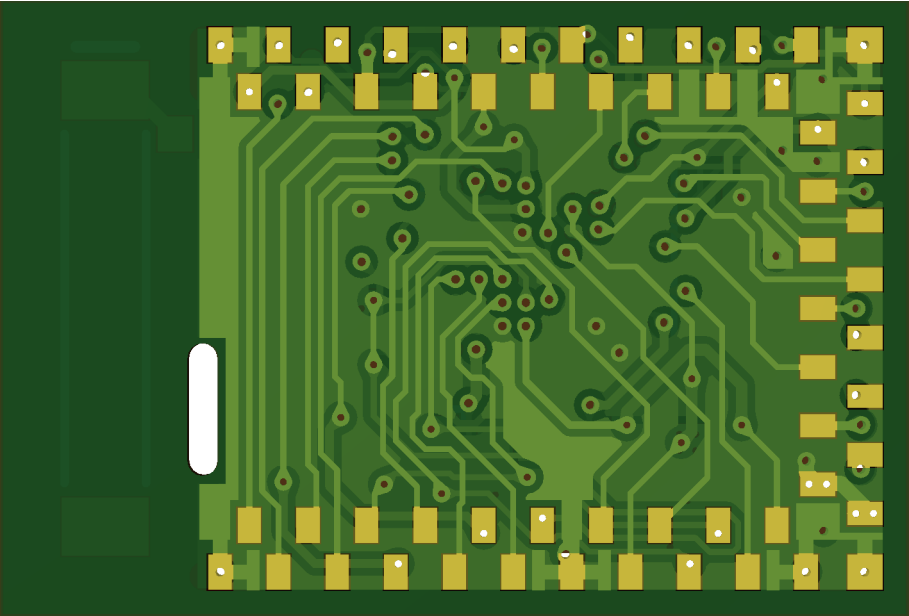


Project: Artemis Plus		
Title: Component Outline View		
Size: A	Number: 1012	Revision: C
Date: 11/1/2023	Time: 3:27 PM	Sheet: 2 of 7
File: Artemis_Plus-CCA.PCBDwf		

Rendered View Top



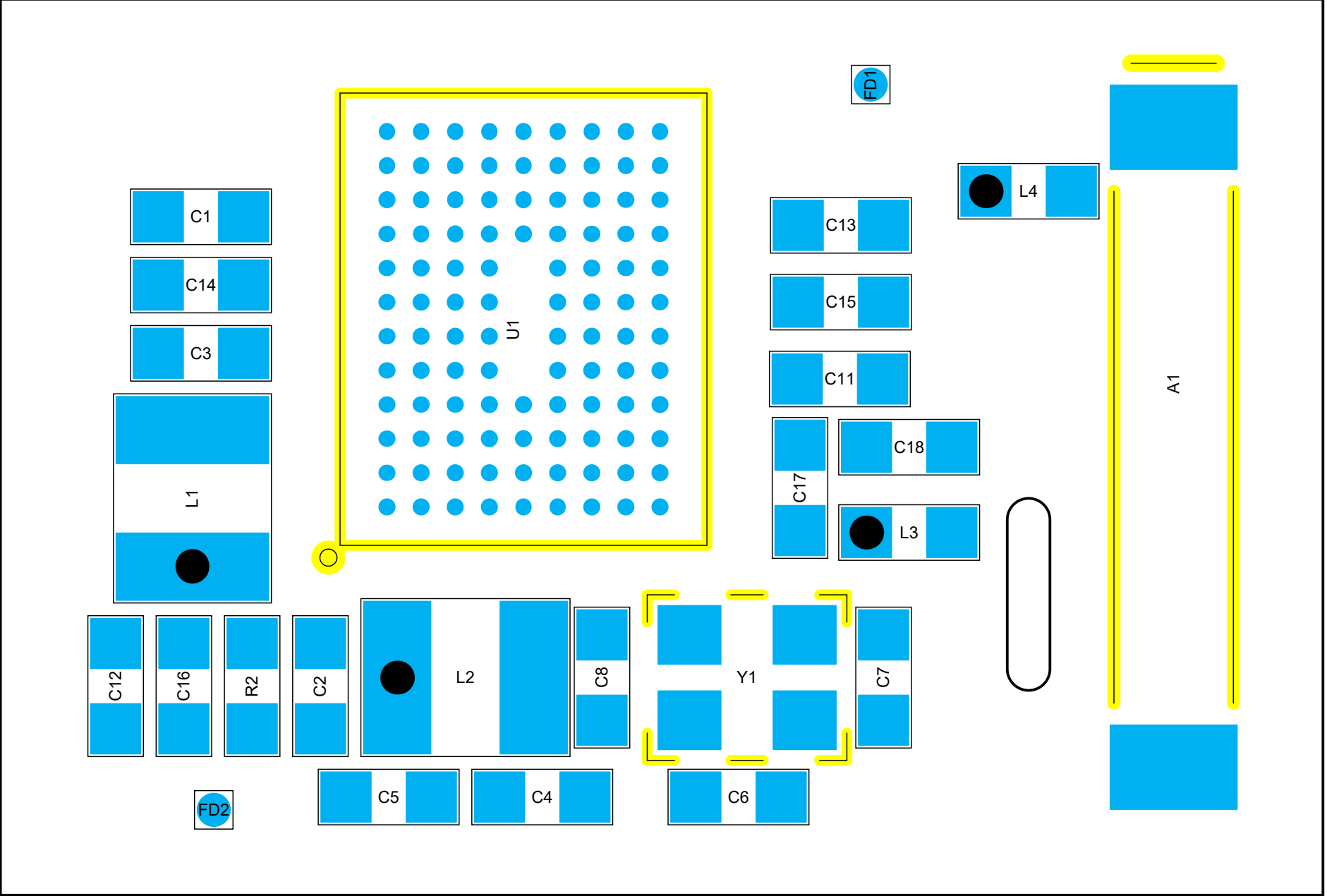
Rendered View Bottom



All vias are to be filled with non-conductive epoxy, capped, and plated
(not shown in rendering)

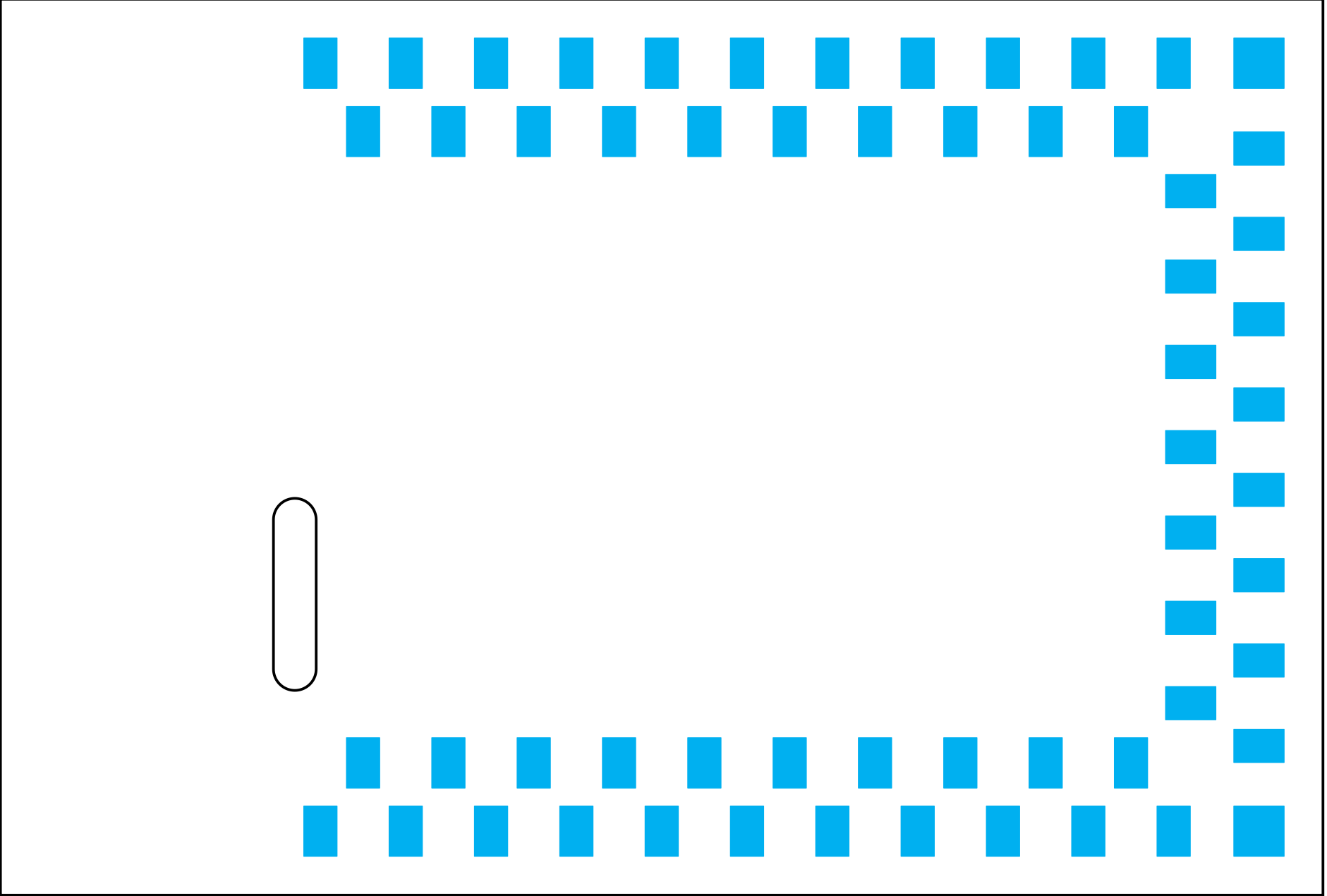
Project: Artemis Plus		
Title: Rendering View		
Size: A	Number: 1012	Revision: C
Date: 11/1/2023	Time: 3:27 PM	Sheet: 3 of 7
File: Artemis_Plus-CCA.PCBdwf		

View from Top side (Scale 16:1)



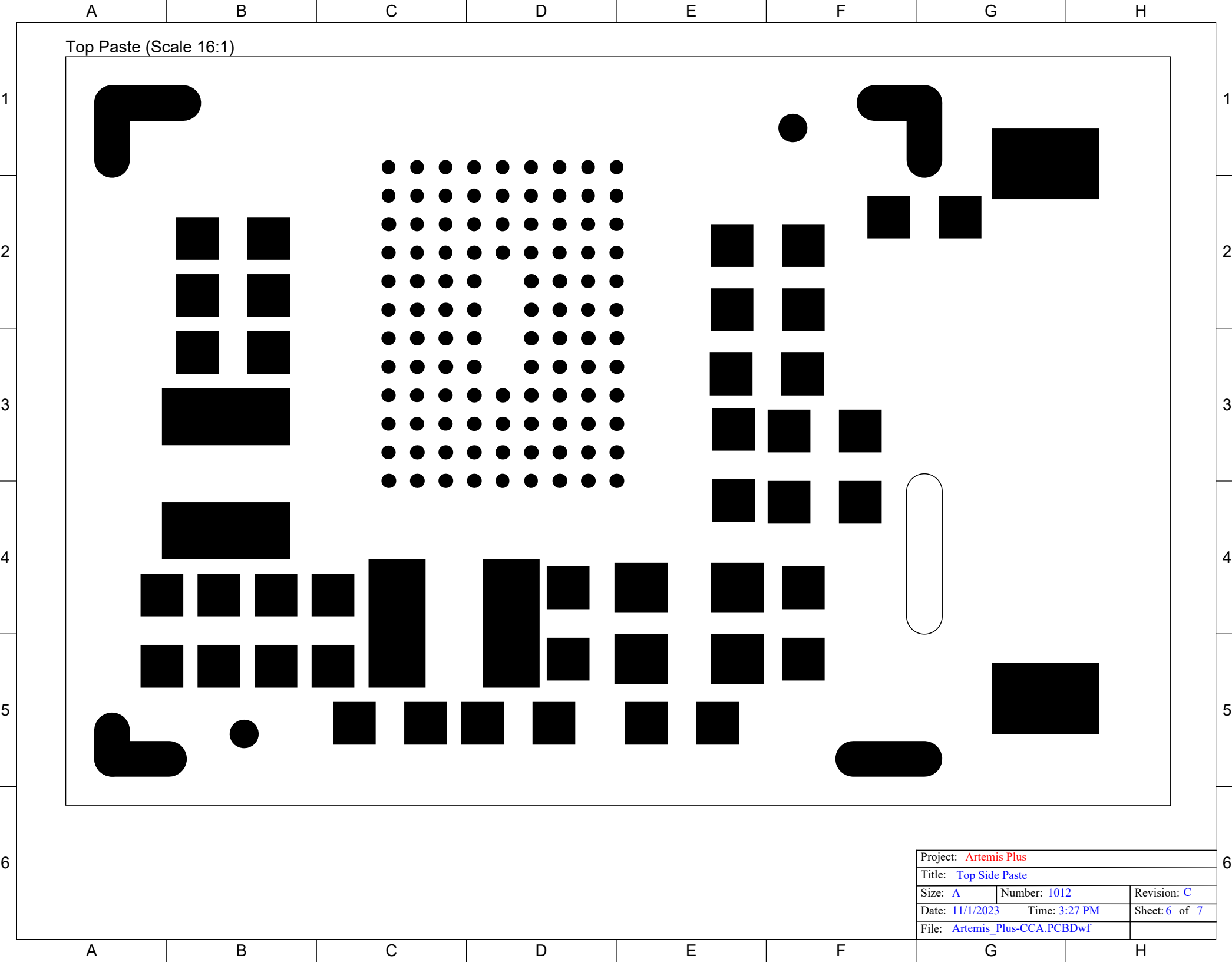
Project: Artemis Plus		
Title: Top Side Detail View		
Size: A	Number: 1012	Revision: C
Date: 11/1/2023	Time: 3:27 PM	Sheet: 4 of 7
File: Artemis_Plus-CCA.PCBdwf		

View from Bottom side (Scale 16:1)



No components on bottom side.

Project: Artemis Plus		
Title: Bottom Side Detail View		
Size: A	Number: 1012	Revision: C
Date: 11/1/2023	Time: 3:27 PM	Sheet: 5 of 7
File: Artemis_Plus-CCA.PCBDwf		



Bottom Paste (Scale 16:1)

Project: Artemis Plus		
Title: Bottom Side Paste		
Size: A	Number: 1012	Revision: C
Date: 11/1/2023	Time: 3:27 PM	Sheet: 7 of 7
File: Artemis_Plus-CCA.PCBDwf		

No paste features on bottom side.

