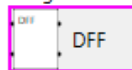
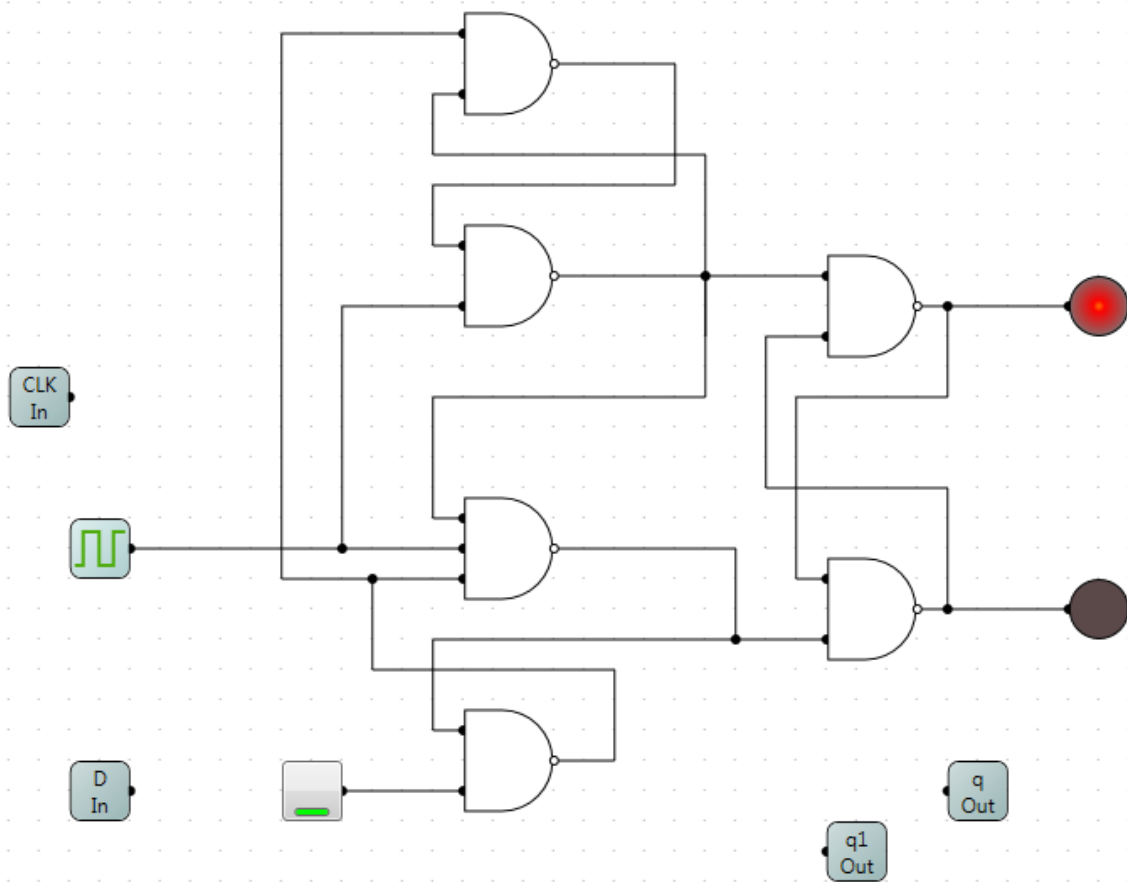


## D Flip Flop

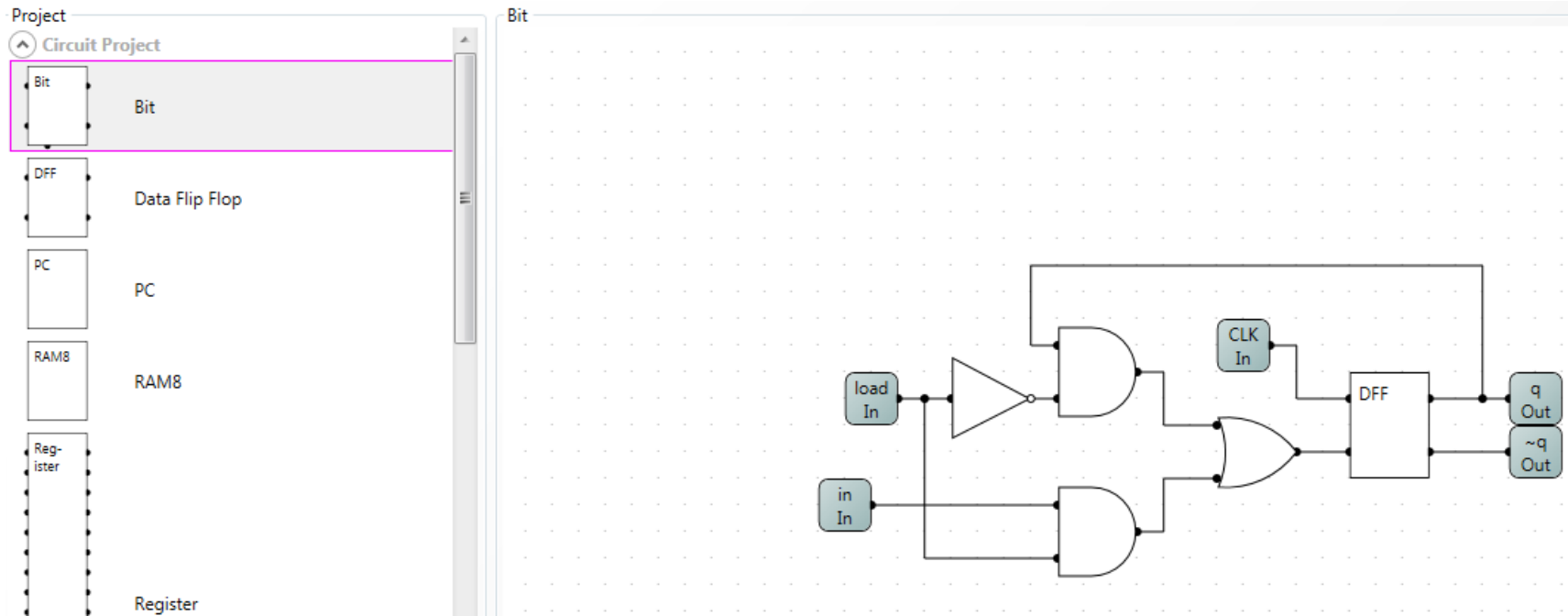
Running Circuit



Data Flip Flop

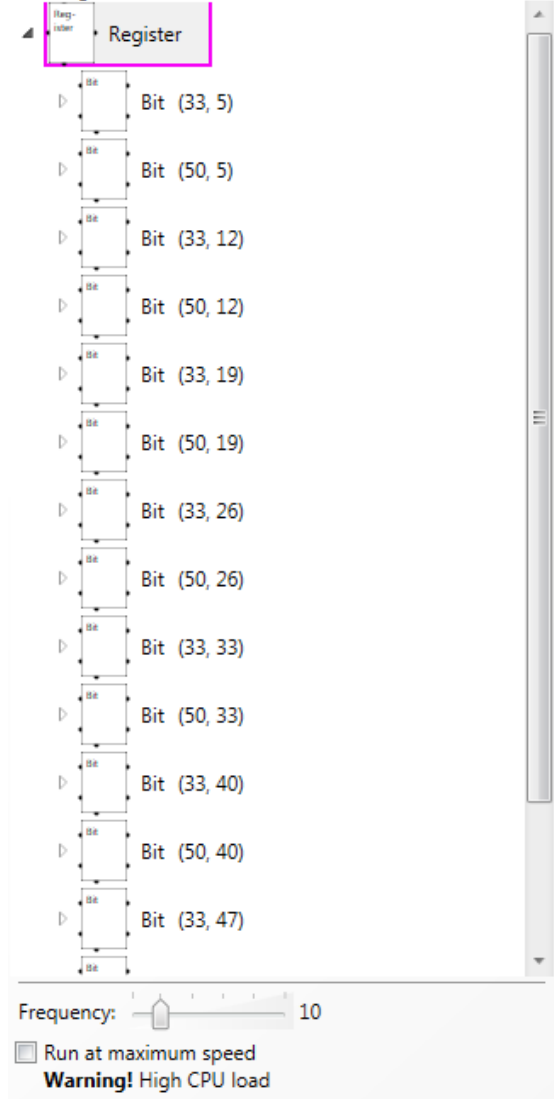


## 1 Bit Register

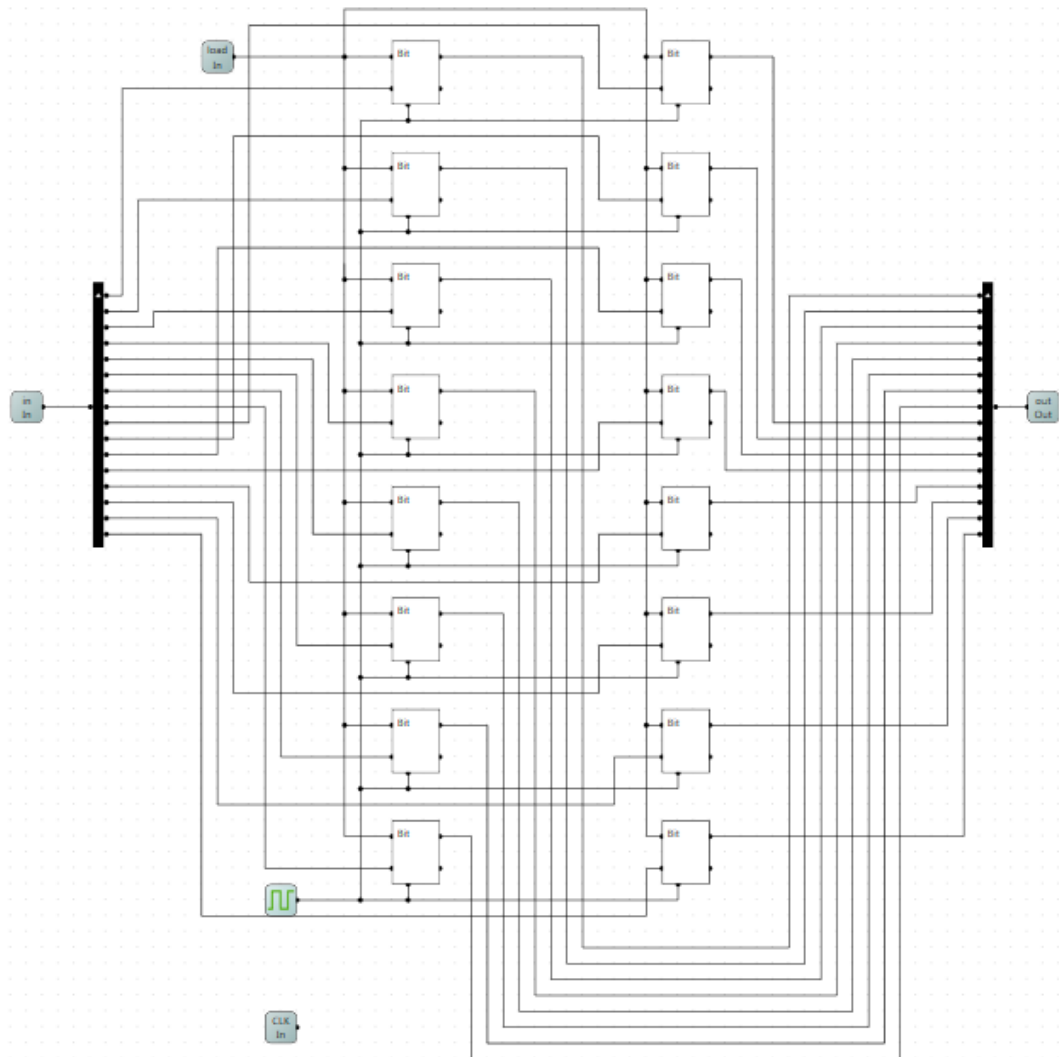


## Register

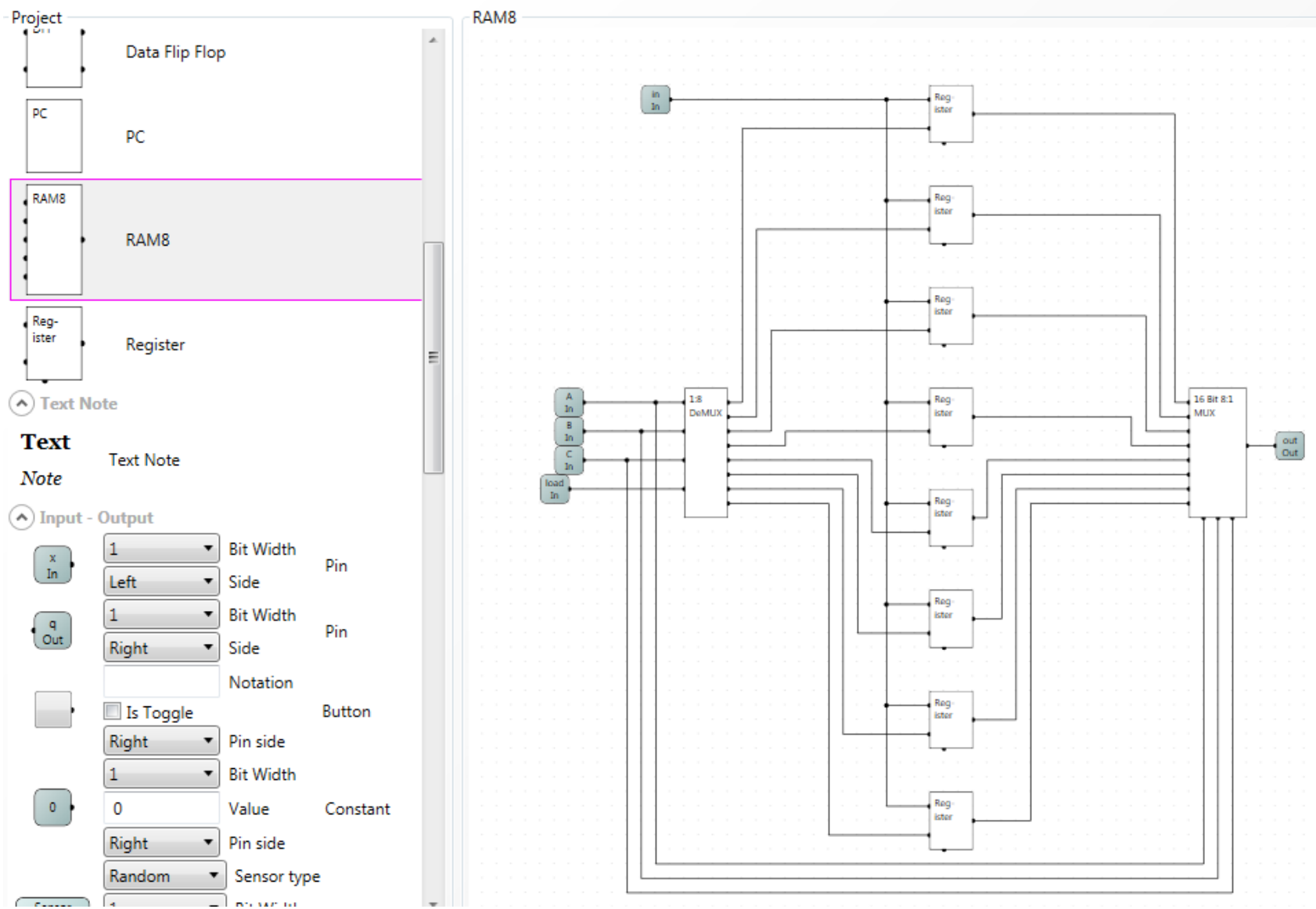
Running Circuit



Register



## RAM 8



## Program Counter

