

Bibliography

- [1] John L Gustafson and Isaac T Yonemoto. “Beating floating point at its own game: Posit arithmetic”. In: *Supercomputing Frontiers and Innovations* 4.2 (2017), p. 71.
- [2] John L Gustafson and Isaac T Yonemoto. “Beating floating point at its own game: Posit arithmetic”. In: *Supercomputing Frontiers and Innovations* 4.2 (2017), pp. 71–86.
- [3] Florent De Dinechin et al. “Posits: the good, the bad and the ugly”. In: *Proceedings of the Conference for Next Generation Arithmetic 2019*. 2019, pp. 1–10.
- [4] Zachariah Carmichael et al. “Deep positron: A deep neural network using the posit number system”. In: *2019 Design, Automation & Test in Europe Conference & Exhibition (DATE)*. IEEE. 2019, pp. 1421–1426.
- [5] M Klöwer, PD Düben, and TN Palmer. “Number formats, error mitigation, and scope for 16-bit arithmetics in weather and climate modeling analyzed with a shallow water model”. In: *Journal of Advances in Modeling Earth Systems* 12.10 (2020), e2020MS002246.
- [6] “IEEE Standard for Floating-Point Arithmetic”. In: *IEEE Std 754-2019 (Revision of IEEE 754-2008)* (2019), pp. 1–84. doi: 10.1109/IEEESTD.2019.8766229.
- [7] Posit Working Group. “Posit Standard Documentation”. In: *Release 3.2-draft* (2018).
- [8] Marco Cococcioni et al. “Fast approximations of activation functions in deep neural networks when using posit arithmetic”. In: *Sensors* 20.5 (2020), p. 1515.
- [9] Raul Murillo, Alberto A Del Barrio, and Guillermo Botella. “Deep PeNSieve: A deep learning framework based on the posit number system”. eng. In: *Digital signal processing* 102 (2020), p. 102762. issn: 1051-2004.

- [10] Seyed Hamed Fatemi Langroudi, Tej Pandit, and Dhireesha Kudithipudi. “Deep learning inference on embedded devices: Fixed-point vs posit”. In: *2018 1st Workshop on Energy Efficient Machine Learning and Cognitive Computing for Embedded Applications (EMC2)*. IEEE. 2018, pp. 19–23.
- [11] Steven WD Chien, Ivy B Peng, and Stefano Markidis. “Posit NPB: Assessing the Precision Improvement in HPC Scientific Applications”. In: *International Conference on Parallel Processing and Applied Mathematics*. Springer. 2019, pp. 301–310.
- [12] Vinay Saxena et al. “Brightening the Optical Flow through Posit Arithmetic”. In: *2021 22nd International Symposium on Quality Electronic Design (ISQED)*. 2021, pp. 463–468. doi: 10.1109/ISQED51717.2021.9424360.
- [13] Johan Besseling and Anders Renström. *A comparative study of IEEE 754 32-bit Float and Posit 32-bit floating point format on precision.: Using numerical methods*. 2020.