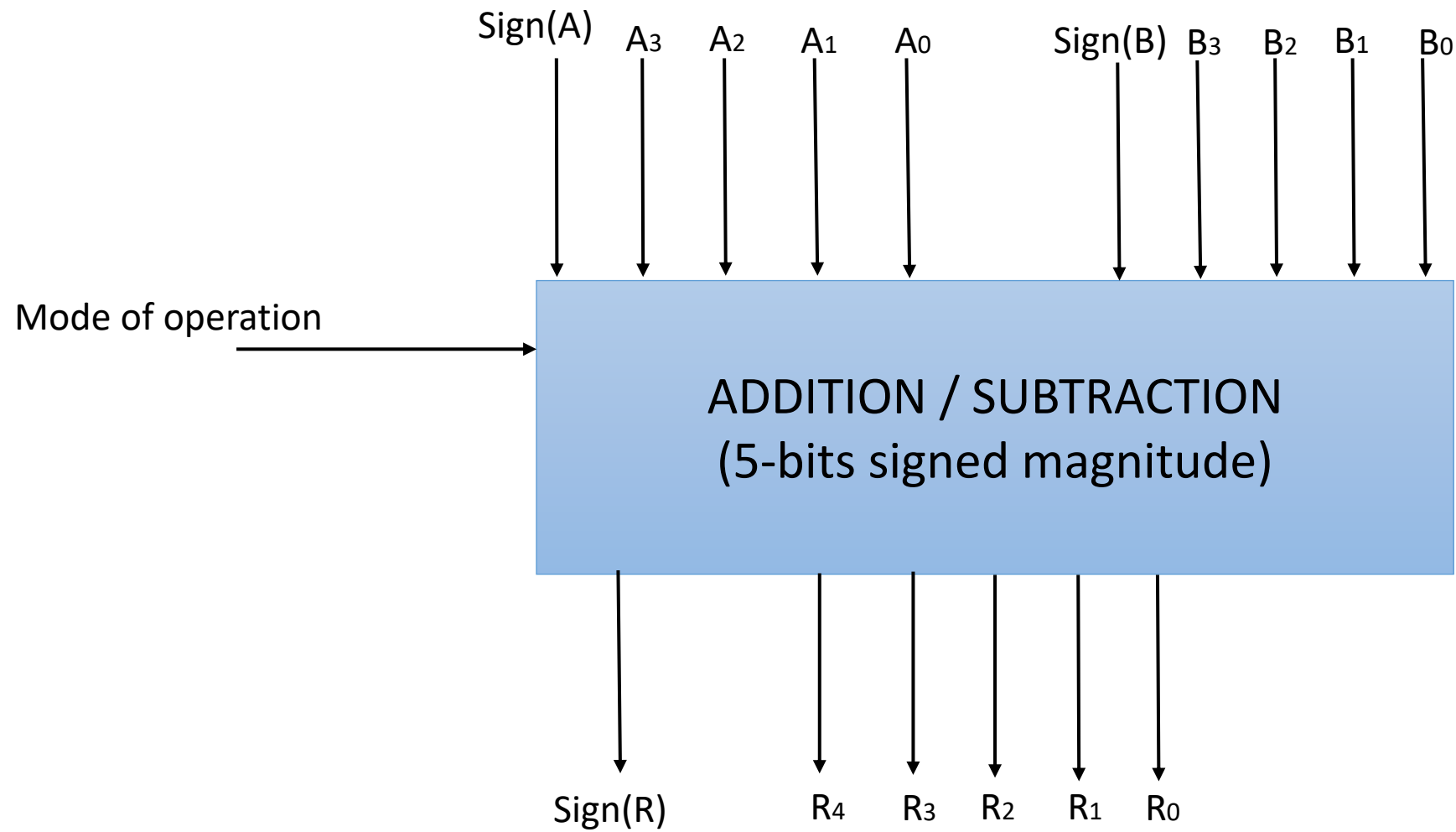
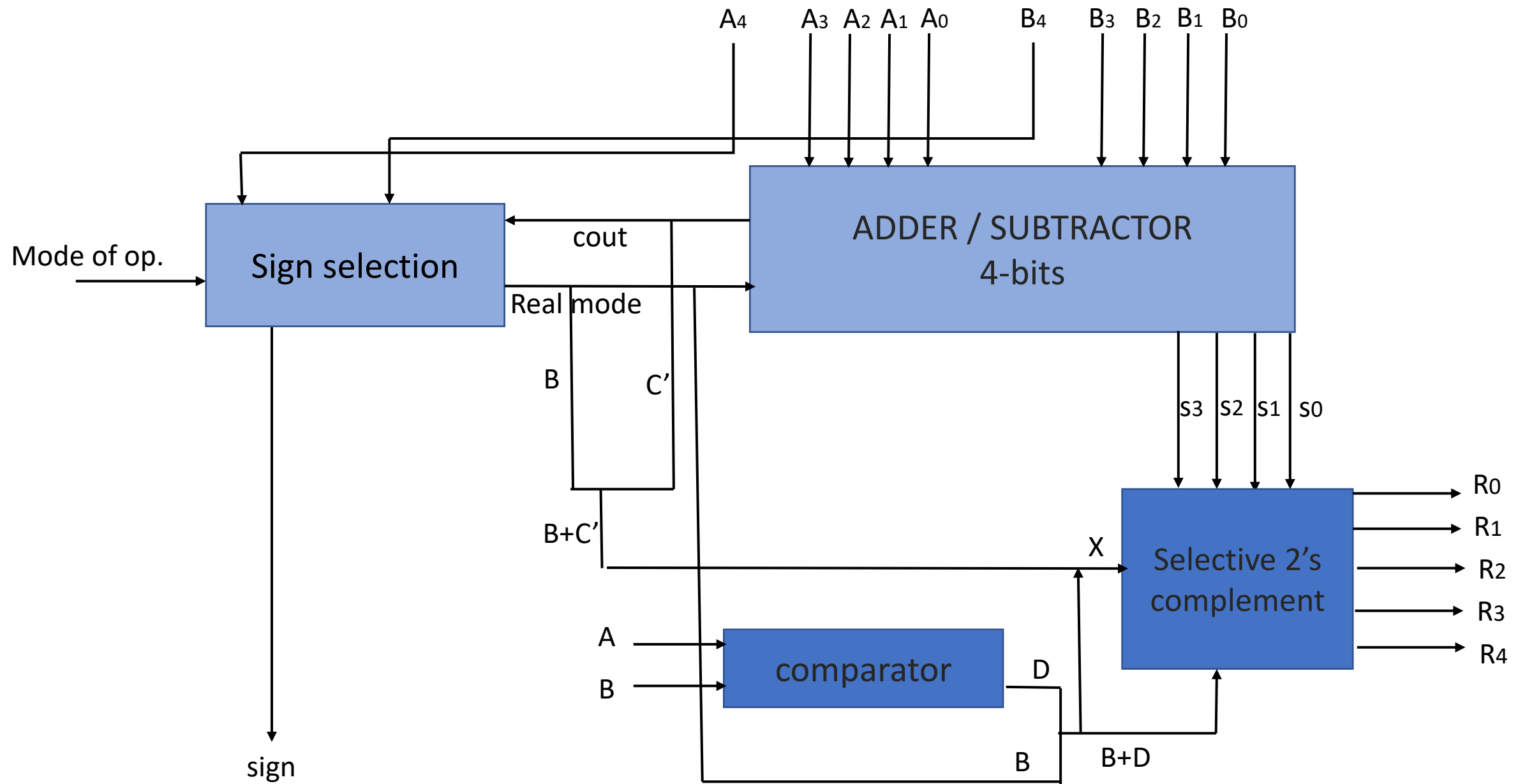
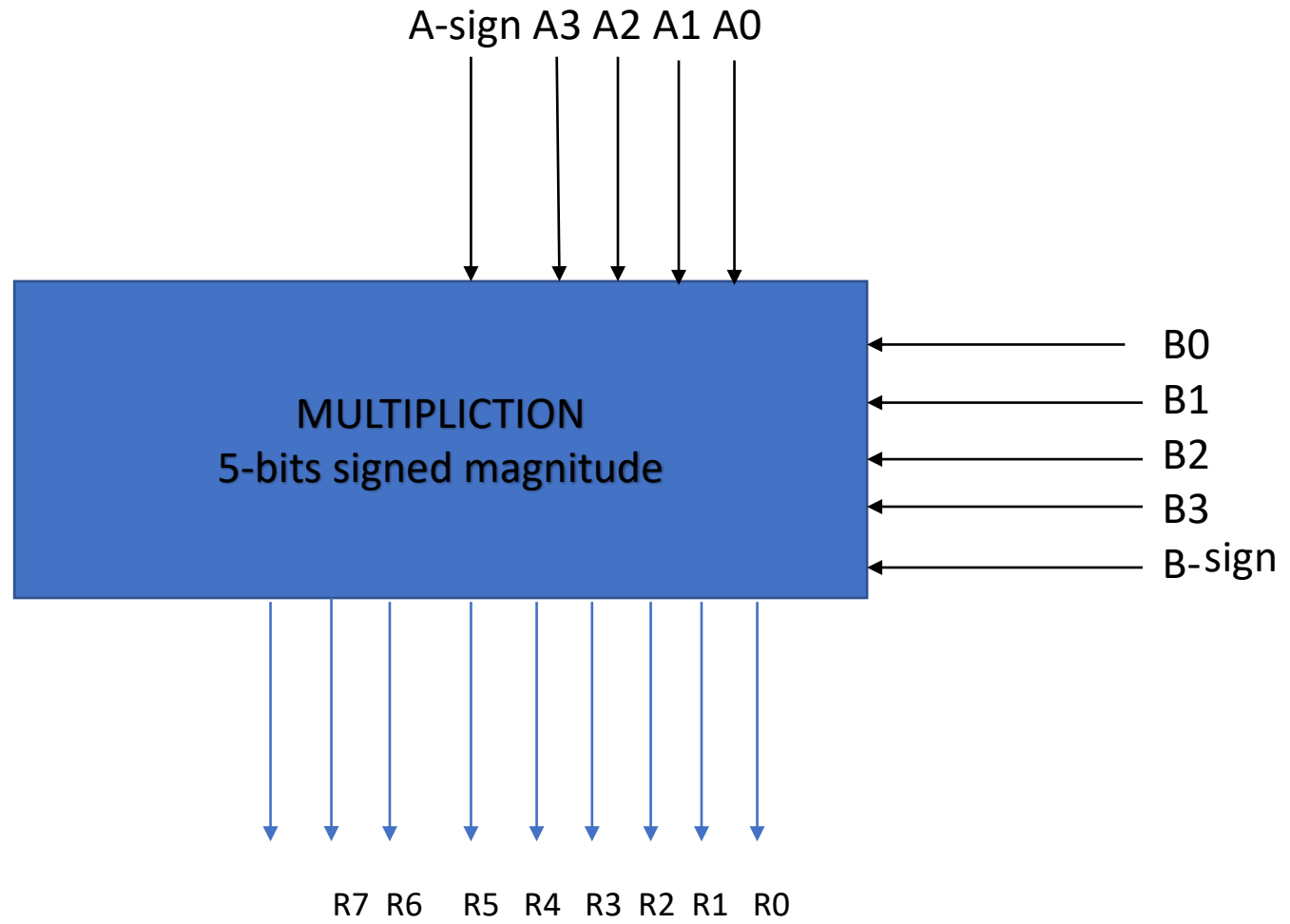


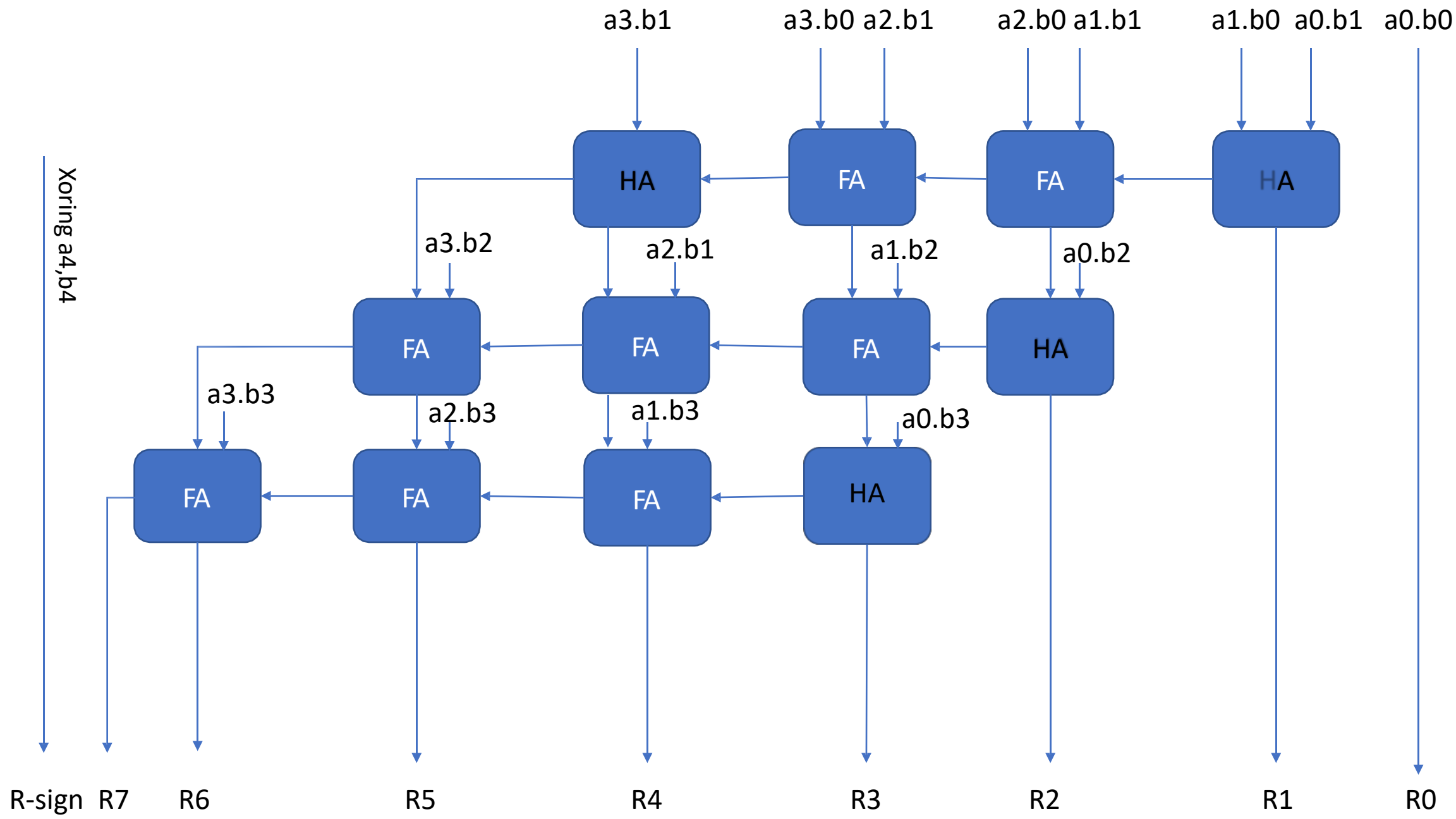
NAME	ID	BN	WORK DONE
Ahmed Fawzy Mohamed Ibrahim	9202153	7	Addition & Subtraction
Khaled Hesham Sayed Taha	9202513	21	Addition & Subtraction
Shredan Abdullah Kamal Moghazy	9202707	33	<ul style="list-style-type: none"> • Multiplication • Binary to BCD
Sarah Mohamed Hossam Hassan	9202618	29	<ul style="list-style-type: none"> • Division • BCD to 7-Segment display

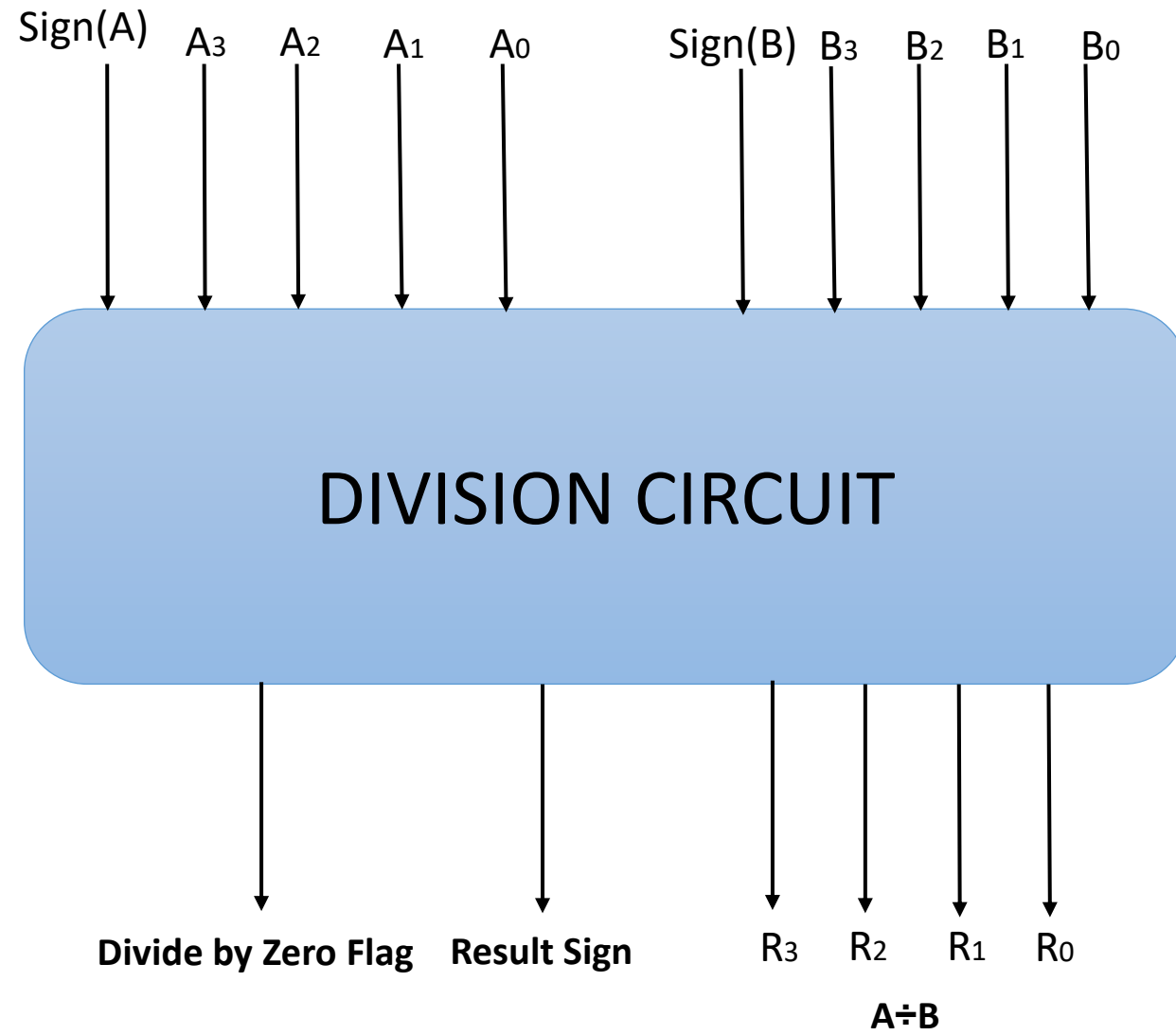
ALL TEAM MEMBERS WORKED ON THE INTEGRATION OF THE DIFFERENT CIRCUITS



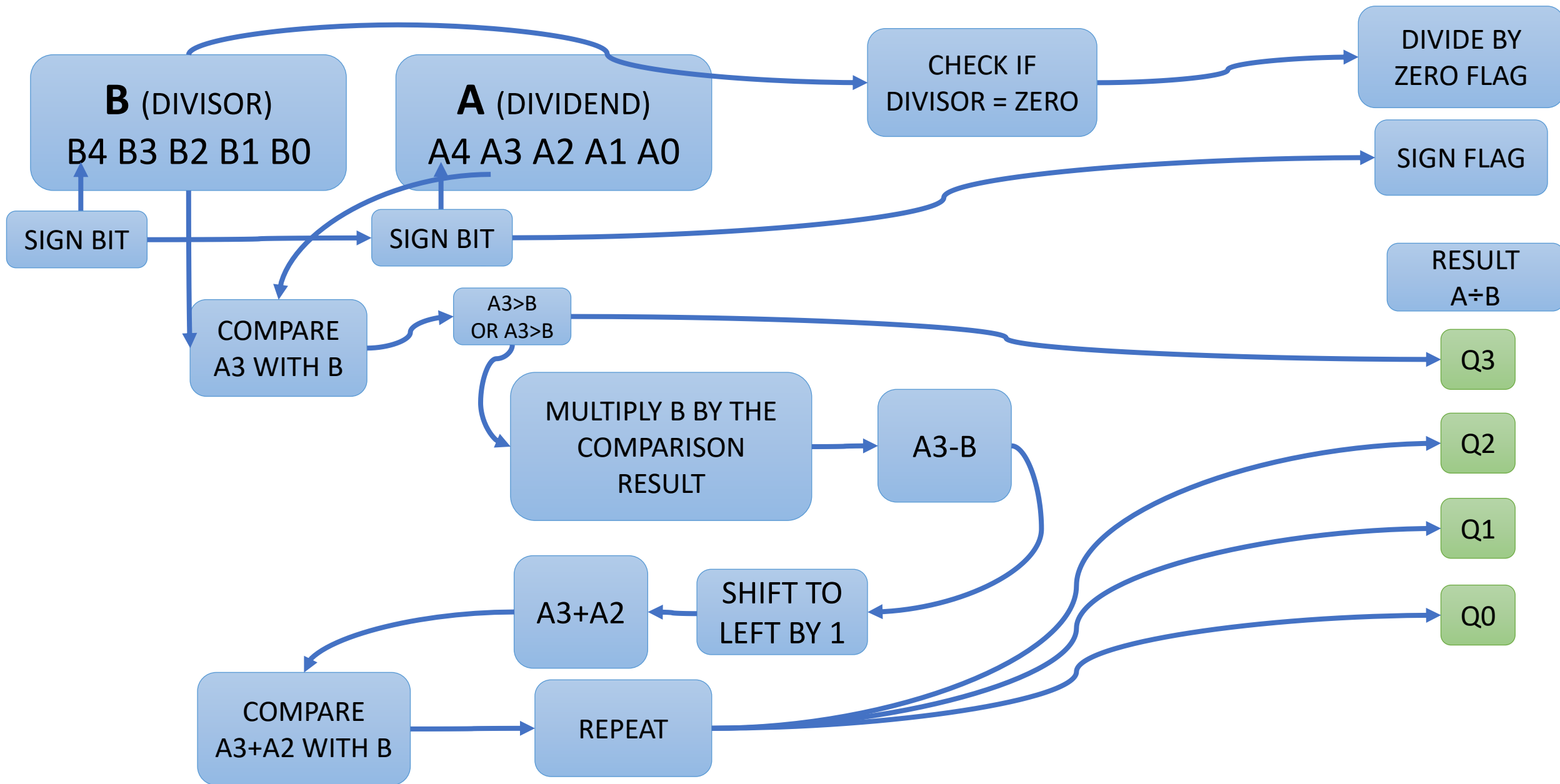




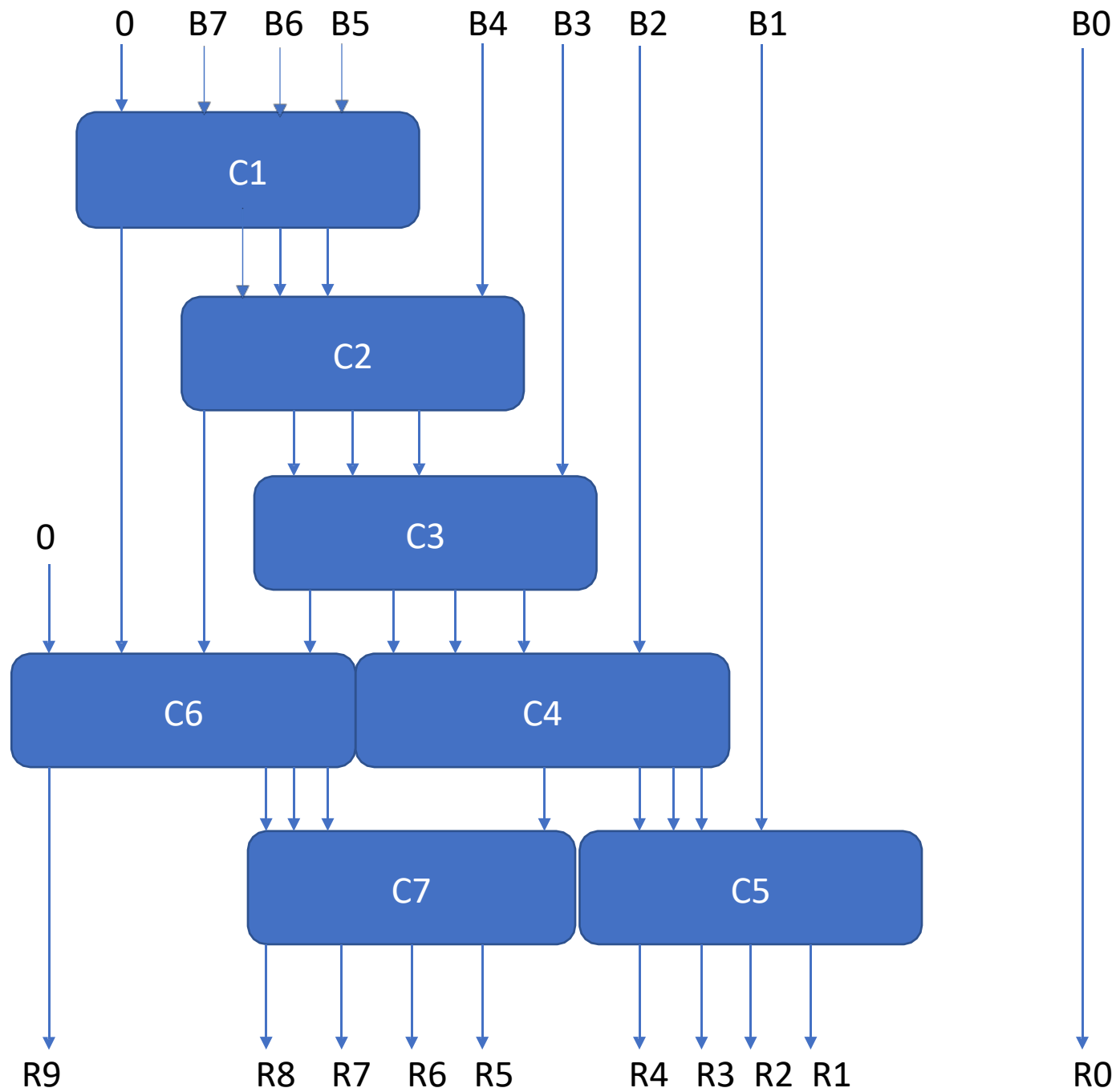




DIVISION BLOCK DIAGRAM



Binary (input)



Number of Gates Used

Logisim: main Statistics



Component	Library	Simple	Unique	Recursive
Add/Subtract Circuit	Final Project	0	1	1
Full Adder	Final Project	0	4	4
Half Adder	Final Project	0	5	5
Divider Circuit	Final Project	0	1	1
4x1 multiplier	Final Project	0	3	3
Multiplier Circuit	Final Project	0	1	1
Multiplier FA	Final Project	0	12	32
Multiplier HA	Final Project	0	4	4
Multiplication	Final Project	0	1	1
Divison	Final Project	0	1	1
Binary To BCD	Final Project	1	1	1
Adding and Subtracting	Final Project	0	1	1
Integration	Final Project	1	1	1
Shifter	Final Project	0	3	3
Comparator	Final Project	0	5	5
BCD-7 Segment	Final Project	3	3	3
Unsigned Adder/Subtractor	Final Project	0	6	6
4 Bit Binary Conv	Final Project	0	7	7
Splitter	Wiring	0	30	53
Pin	Wiring	12	221	563
Constant	Wiring	0	3	3
Ground	Wiring	2	12	14
NOT Gate	Gates	0	23	103
AND Gate	Gates	0	76	366
OR Gate	Gates	0	43	166
NOR Gate	Gates	0	2	2
XOR Gate	Gates	0	22	111
XNOR Gate	Gates	0	4	20
Multiplexer	Plexers	0	2	2
LED	Input/Output	3	3	3
7-Segment Display	Input/Output	3	3	3
Label	Base	7	67	121
TOTAL (without project's subcircuits)		27	511	1530
TOTAL (with subcircuits)		32	571	1610