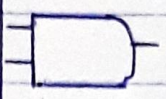
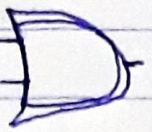
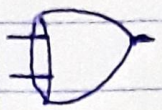
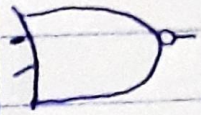
Practical 5

logic gates

PART A 1.

inputs outputs truth table

A	B	AND	OR	XOR	NAND	NOR
0	0	0	0	0	1	0
0	1	0	1	0	0	1
1	0	0	0	1	1	0
1	1	1	1	0	1	0
Gate Symbol						
Functional Notation		$\wedge$	$\vee$	$\oplus$	$\uparrow$	$\downarrow$

PART B

2. AND Truth table

A	B	AND
0	0	1
1	1	1
Functional Notation		$\wedge$

PART B

2.

Inputs		
A	B	Xor
0	0	0
0	1	1
1	0	1
1	1	0

3.