RESUME

SARAVANAKUMAR A

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LinkedIn

CAREER OBJECTIVE:

To build a meaningful career in a progressive organization where I can contribute my ideas, learn from real-world challenges, and grow into a responsible professional. I strive to bring a positive attitude, teamwork, and a problem-solving mindset to every task while continuously improving my skills and adding value to the organization's success.

EDUCATION:

Qualification	School/College	University/Board Name	Year of Completion	% of Marks
B.E- ECE	Chettinad College of Engineering and Technology, Karur	Anna University, Chennai	2026	7.6 (at present)
HSC	Shree Kalaimagal Matric Higher Secondary School, Kulithalai.	State Board	2022	71%
SSLC	Saraswathy Vidhyalaya Matriculation School, Lalapet.	State Board	2020	91.8%

SKILLS:

Technical Skill:

- **Programming** : Verilog, Python, C
- **Hardware** : Arduino, Microcontrollers (8051)
- **Networking Tools** : Cisco Packet Tracer
- **Simulation Tools** : MATLAB, EDA playground, Wokwi, Xilinx, Multisim
- **Hardware Tools** : Arduino IDE, Microwind, Tinkercad
- **Software Tools** : VS Code, Python IDE
- **Firmware & Tools:** I2C, SPI, CAN

Soft Skill:

- Problem-Solving & Critical Thinking.
- Multitasking & Leadership.

ACHIEVEMENT & AWARDS:

- **Awarded Robotics Starter Kit by AMD participating in the Pervasive AI Developer Contest** with a project **"AI-powered electric scooter"** concept in the Robotics AI category.
- **Secured 3rd Prize** in Inter-College Mini Project Expo for the project **"Low Power High Performance Vedic Multiplier".**

PROJECT:

Smart Bridge Using Arduino (September 2023 – October 2023)

- Developed a flood-aware smart bridge system using Arduino, servo motors, and soil moisture sensors.
- Enabled automated bridge response based on water level detection to prevent flood-related failures.
- Tools Used: Arduino, Servo Motor, Soil Moisture Sensor, Embedded C.

Low Power High Performance Vedic Multiplier (September 2024 – October 2024)

- Designed and simulated a power-efficient Vedic multiplier architecture using Verilog.
- Focused on optimizing speed and power consumption of the multiplier and the processors.
- Tools Used: Xilinx ISE, Verilog.

AeroGuard – Real-Time Missile-Inspired Stabilization with Wireless Monitoring (On going)

- Designed and developed an intelligent fin stabilization system inspired by missile dynamics using ESP8266, MPU6050, and servo motors.
- Implemented real-time angle correction through PID control for roll, pitch, and yaw stabilization.
- Integrated wireless data transmission for monitoring and visualization in MATLAB to analyze before-and-after stabilization performance.
- Tools Used: ESP8266 (NodeMCU), MPU6050 IMU Sensor, SG90 Servo Motors, MATLAB, Arduino IDE, Embedded C.

WORK EXPERIENCE:

VLSI Intern, TARAS systems & Solutions, Coimbatore. **07/2025 – 08/2025**

- Gained hands-on experience in Digital Electronics fundamentals and design flow.
- Practiced coding in Verilog for logic design and simulation.
- Enhanced programming proficiency in C related to hardware description.
- Understood the application of VLSI design principles in real-world projects.

VLSI Intern, TARAS systems & Solutions, Bangalore. **12/2024– 06/2025**

- Completed a 6-month online internship focused on VLSI Design and Verification.
- Gained hands-on experience in Verilog, System Verilog, and UVM for RTL design and functional verification.
- Improved understanding of the VLSI design flow through structured sessions and project-based learning.
- Strengthened skills in digital design principles and simulation methodologies.

CERTIFICATION:

- **VLSI Course Level-1** Completed a 1-week hands-on training at Taras Solutions on VLSI design, focusing on digital logic, layout design, and circuit simulation.
- Successfully completed the **AMD AI Challenge Workshop** organized by the **Shooting Stars Foundation**, as part of the **AMD Pervasive AI Developer Contest**, providing a platform to explore and present innovative AI project ideas.
- Participated in a workshop on **"Vehicular Ad-Hoc Networks (VANETs)"** conducted by **Netsim – TETCOS LLP, Bangalore**, during the **National Level Technical Symposium, PINNACLE 2K24**, organized by the **ECE Association, IETE Students' Forum, and IE(I) Students' Chapter** on **22nd March 2024**.

PERSONAL DETAILS:

Father's name : Arumugam K

Date of birth : 12/04/2004

Marital Status : Unmarried

Address : 1/35, Muslim street, Lalapet, Karur, Tamil Nadu.

Language :

Languages	Read	Write	Speak
Tamil	yes	yes	yes
English	yes	yes	yes

DECLARATION:

I hereby declare that the information provided above is true to the best of my knowledge and belief.

Date :

Place : **Signature**