

Uva Wellassa University of Sri Lanka
Faculty of Applied Sciences
Department of Computer Science and Informatics
Assignment on Memory Management

Instructions: For all the following questions, write the correct answer, scan or take a picture of the written document, and upload it to the VLE on or before 3rd September 2023.

1. Compare paging with segmentation regarding the amount of memory required by the address translation structures to convert virtual addresses to physical addresses. (5 marks)
2. Draw the diagram of the segmentation memory management scheme and explain its principle. (5 marks)
3. Explain why sharing a reentrant module is easier when segmentation is used than when pure paging is used, with an example. (5 marks)
4. Explain in detail about paging in 32-bit and 64-bit architectures (5 marks)
5. Discuss the given memory management techniques with diagrams.
 - a) Partition Allocation Methods (5 marks)
 - b) Paging and Translation Look-aside Buffer (5 marks)