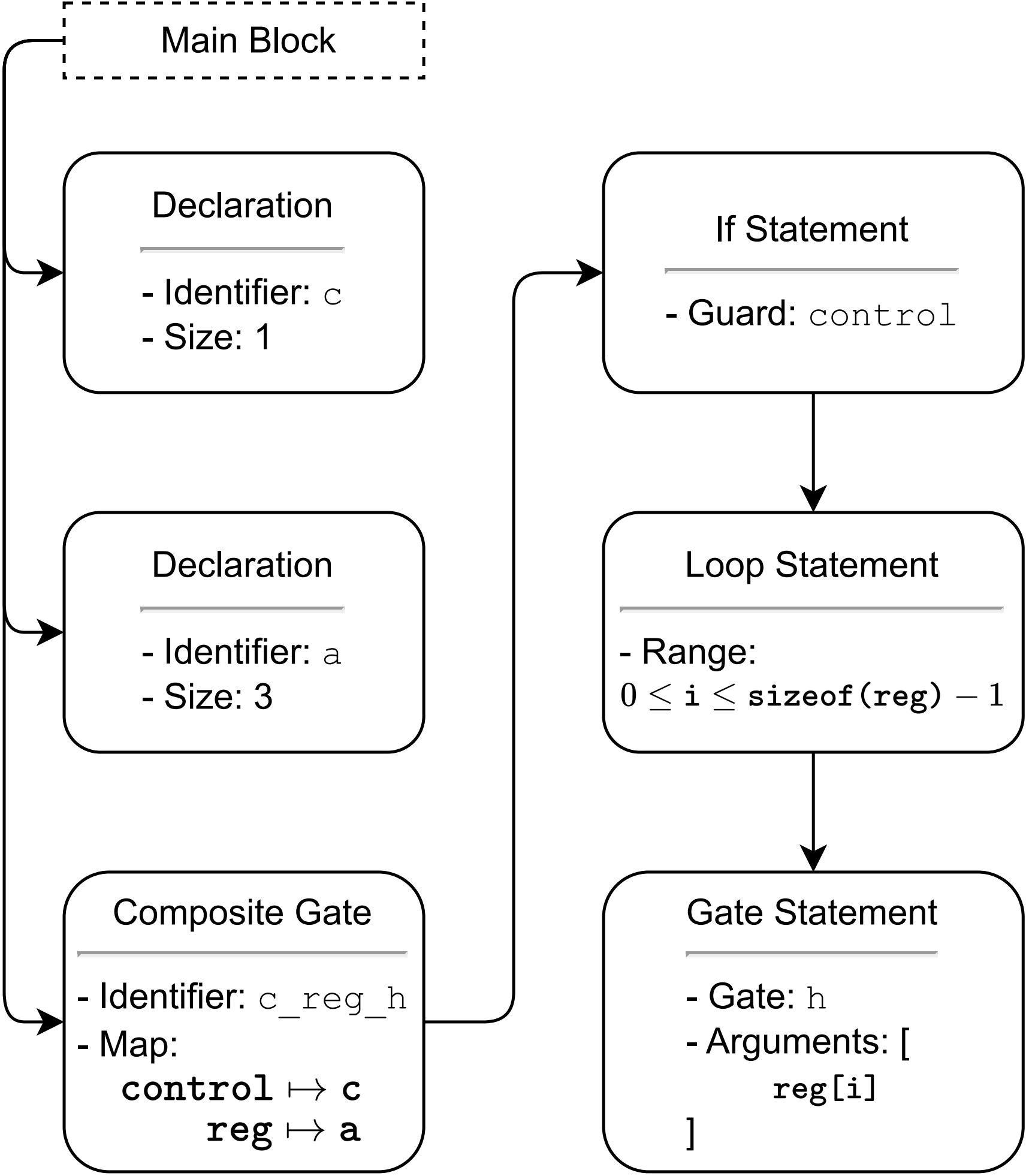
Main Block
Declaration
- Identifier: c
- Size: 1
Declaration
- Identifier: a
- Size: 3
Composite Gate
- Identifier: c_reg_h
- Map:
control ↦ c
reg ↦ a
If Statement
- Guard: control
Loop Statement
- Range:
0 ≤ i ≤ sizeof(reg) − 1
Gate Statement
- Gate: h
- Arguments: [
reg[i]
]