

#### T. Y. B. Tech (ECE)

Semester: VI Subject: ESD&RTOS

Name: Satej Zunjarrao Division: B

Roll No: PB-30 Batch: B3

**Experiment No: 05** 

Name of the Experiment: Programming UART of LPC 2148

Performed on:	Mark	Teacher's Signature with date
	S	
Submitted on:		

**Aim:** Write Embedded C program to interface LPC2148 based ARM EPB7 with PC using UART.

- a) Transfer data from LPC2148 to PC.
- b) Receive data sent from PC on LPC2148 and display on hyper terminal.

#### **Part List:**

- Educational practice board for ARM7 LPC2148
- +9V Power supply
- USB A to B type cable
- PC
- Eclipse IDE
- Flash Magic Utility

### **Theory:**

LPC2148 has two inbuilt UART (Universal Asynchronous Receiver Transmitter) modules viz. UART0 and UART1. UART0 provides only standard transmit and receive data lines. UART1 provides a full modem control handshake interface along with standard transmit and receive data lines. UART0 in asynchronous mode is used to connect the LPC2148 to PC serial port for the purpose of full duplex serial data transfer. Baud rate is a significant factor for serial communication





of microcontroller with other devices. For communication with PC the baud rate of 9600 is selected.

# **Hardware Connection:**

UART0 signals	Signal
TXD0	P0.0
RXD0	P0.1

```
Program:
#include "lpc214x.h"
#define FOSC 12000000
void Transm(char);
char Receivem();
int main()
      //char data
      PINSEL0 = 0X00000005;
      VPBDIV = 0x01;
      IO0DIR = 0x01;
      U0FCR = 0x07;
      U0LCR = 0x87;
      U0DLL = 0x4E;
      U0DLM = 0x00;
      U0LCR = 0x07;
      U0TER = 0x80;
      Transm('A');
      while(1)
```





```
{
             char k = Receiver();
             Transm(k);
      }
      return 0;
}
void Transm(char a)
{
      U0THR=a;
      while (!(U0LSR & 0x20));
}
char Receivem()
{
      while(!(U0LSR & 0x01));
      char b = U0RBR;
      return b;
}
```

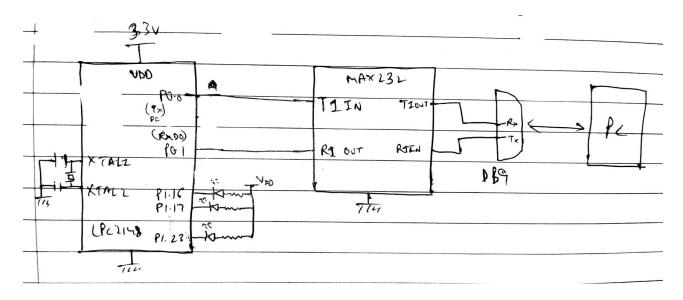
### **Result:**

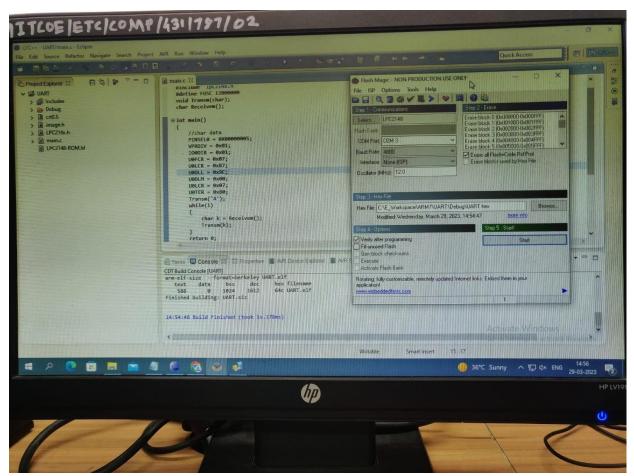
- i) The string should be displayed on HyperTerminal.
- ii) The received character should be displayed on HyperTerminal.



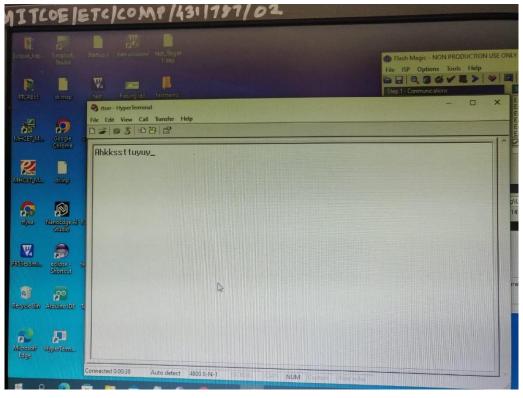


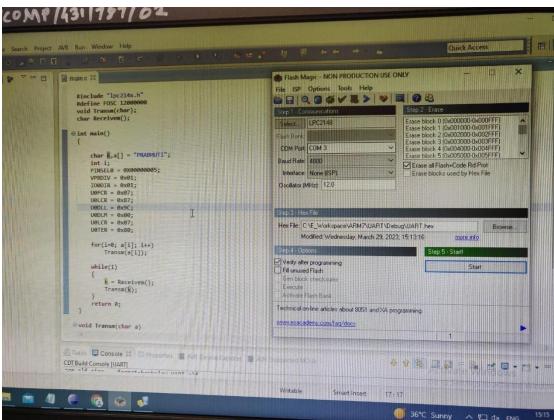
# **Interfacing Diagram:**













## **Conclusion:**

We wrote an embedded C program to interface LPC2148 based ARM EPB7 with a PC using UART. We transmitted from UART to PC and vice versa as well as transmitted our names andreceived them back.



	Classmate.  Date
	Name: Sater Zungarrao Pr. B Sub:- ESD Batch - B3
	Roll no - PB 30
	Assignment - 5
Tol	Post Lab Questions.
	Management description of the business of the second secon
91]	Explain feature of LPC2199 UARTO.
Ans:] ?)	generated with a programmable divide that
11)	allow the use to set band rate according UARTO supports even, odd and no parity generate and checking for error detection in data tramission
Cit	UARTO can generate 101 2 stop bits for data being transmitted
(4.)	that are used notify transmitted or received.
V.)	16 bits receive and transists transmit FIFO's
Civ	Software flow cartral through TAEN bit in transmit enable regist.



# Dr. Vishwanath Karad MIT WORLD PEACE UNIVERSITY | PUNE

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	classmate
	Date
	Page
	e uarto t
	Explain how to set band rate is vARTO to
0.2	Explain how to set 1 6MH2
7	Explain how to set band rate 9600 using PCLK of 15MHz
-	Lit (DLAB) in the
- 2 2	The 1° od division latch access the
Hins:	The division latch access bit (DLAB) in the
	The divid division latch access the line control register (CLE) to access the band rate division register.
	band rate division ing
	1. frequency
(;;	Baud rate division - polly frequency
1	Baud rate division - political rate.
	' 00 d vota division = 15000000
	16 * 9600
2 201	07 6626
-	= 97.65625
	2 = 98 or = 97
	noisement stab of
000)	97 is less the 256 and register value cannot
111	too for the way will man M = 0
	contain fraction, we will vooLM = 0.
23 (339)	UODLOW = 97.
(1)	Make DLA = 0 using volck register
	PINSELO = 0200000005;
	10018 - 0.03
	VOCLR = 0x83
	UOBCH = 0+00
	VOOLL = 0x4E
	VOLCR = 0x07