

U1B V853



VCC PC



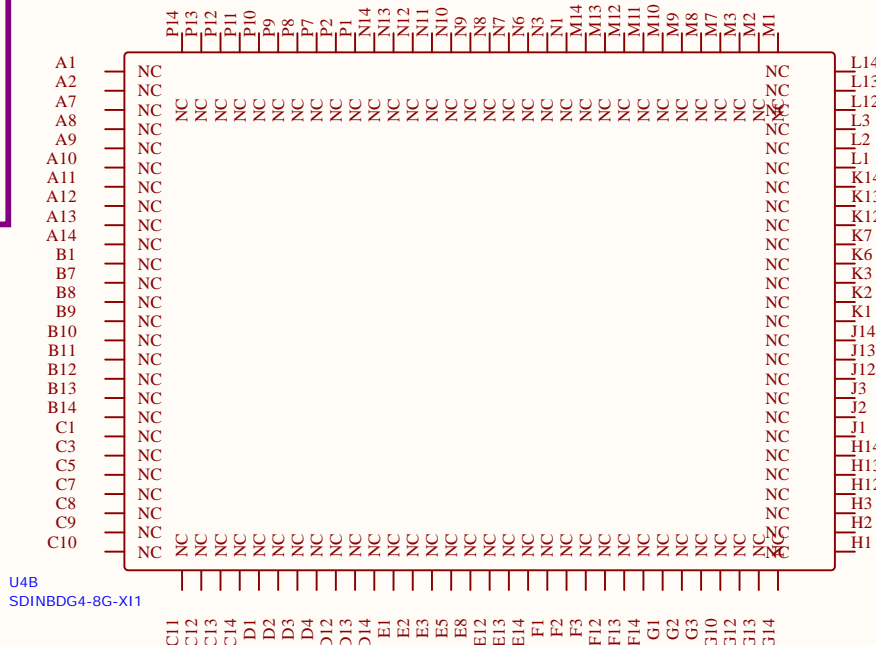
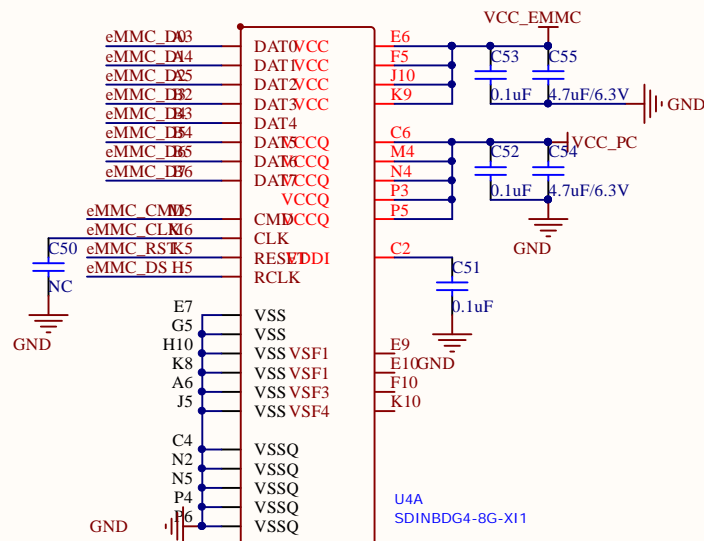
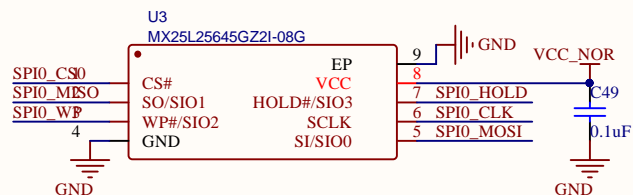
BOOT_SEL0 R67 4.7K NC



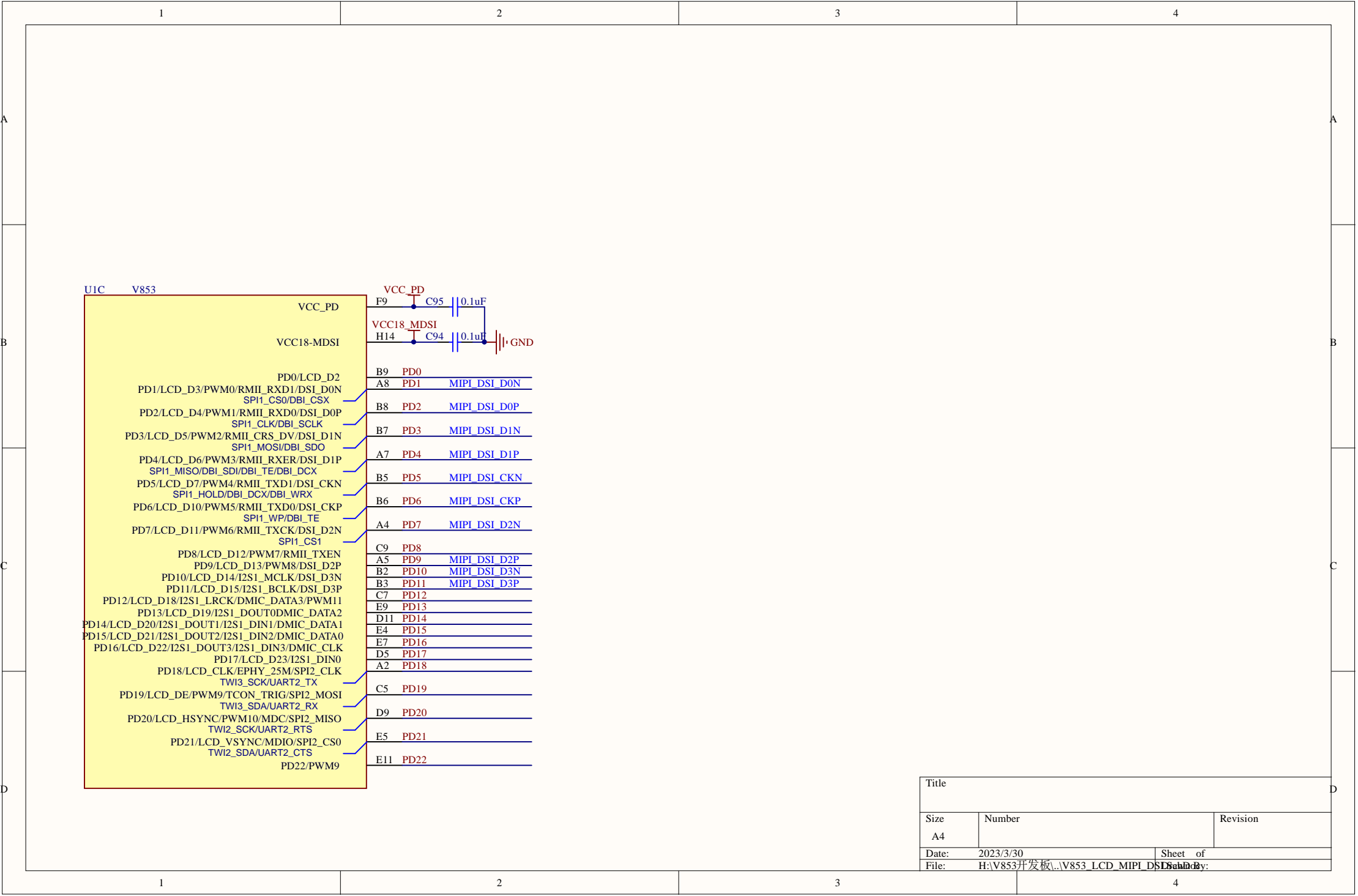
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BOOT_SEL[1:0]
0 0 SPI_NAND>SPI_NOR(4线)>SPI_NOR(1线)>USB
0 1 SPI_NOR(4线)>SPI_NOR(1线)>SPI_NAND>USB(快起)
1 0 SDC0>SPI_NAND>SPI_NOR(4线)>SPI_NOR(1线)>UART BRUN>USB
1 1(默认) SDC0>SPI_NAND>SPI_NOR(4线)>SPI_NOR(1线)>EMMC2_USR>
EMMC2_BOOT>SPI0_NAND>UART BRUN>USB

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Title			
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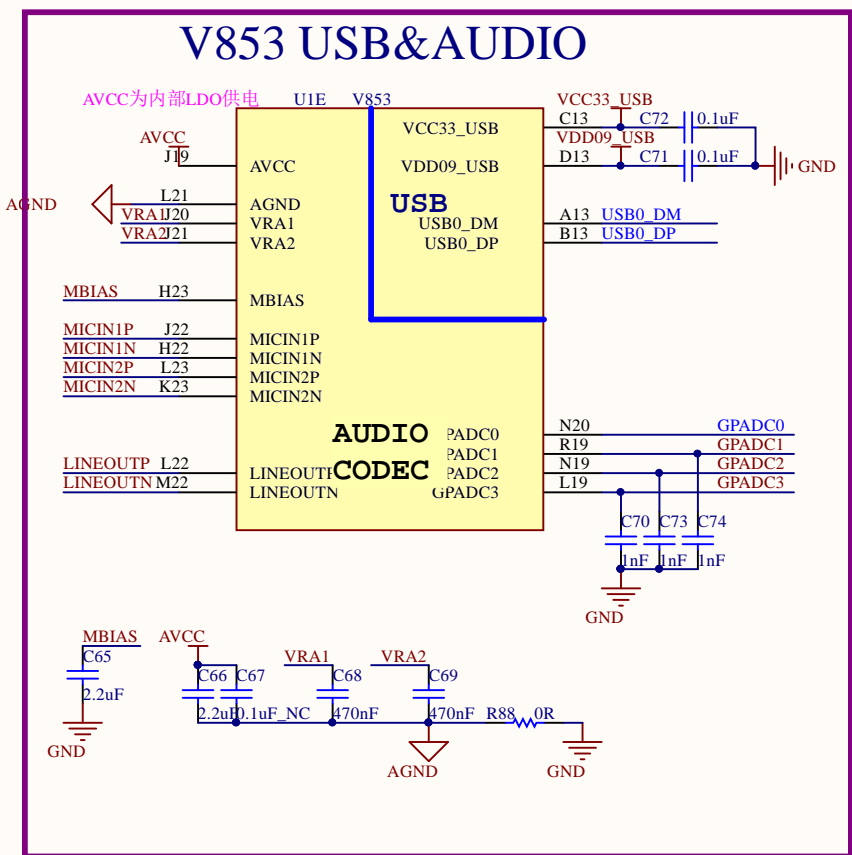


1

2

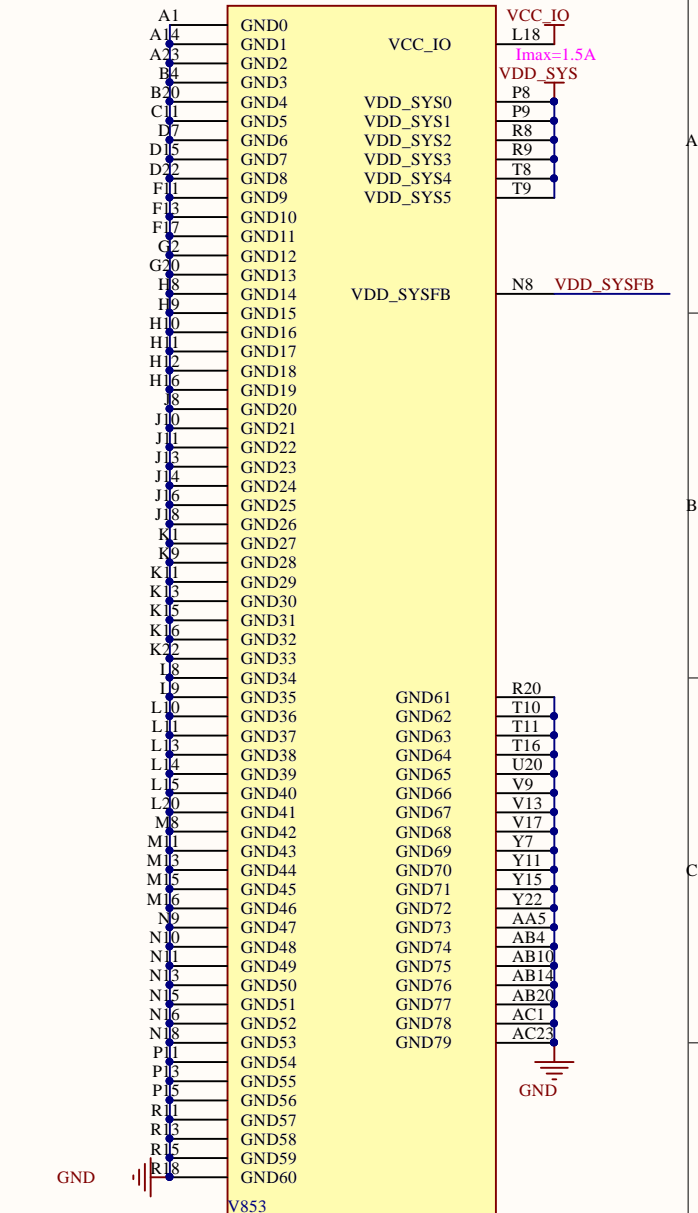
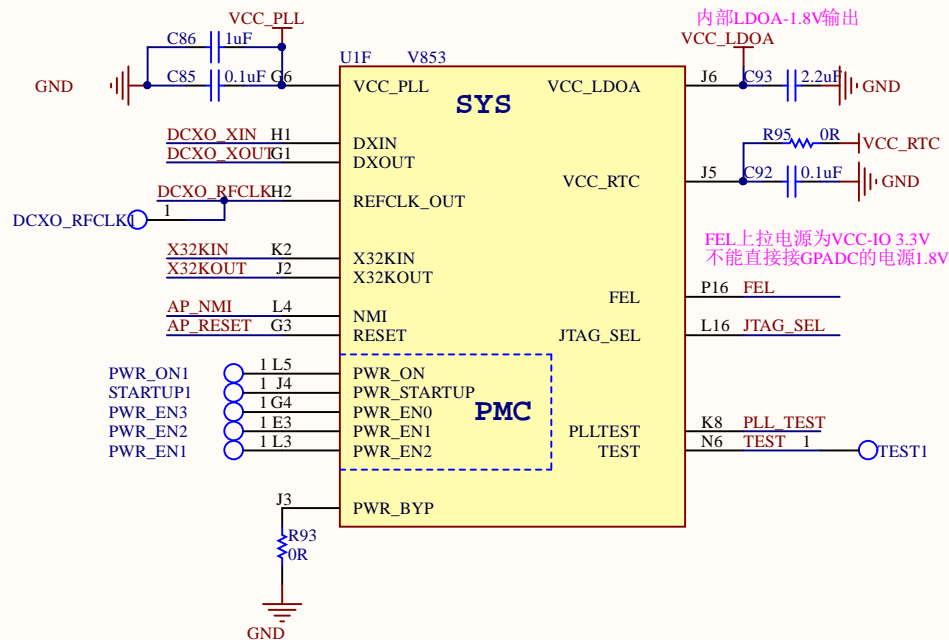
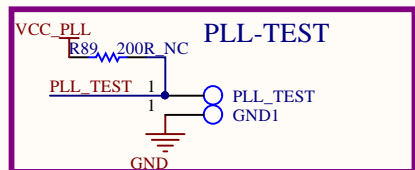
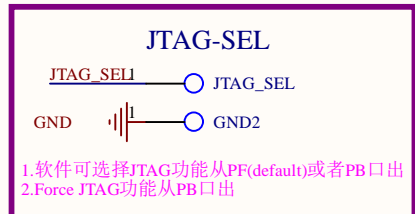
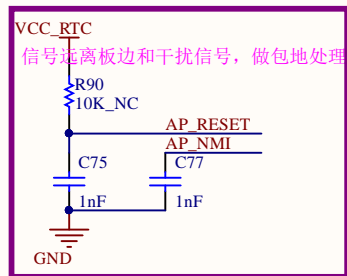
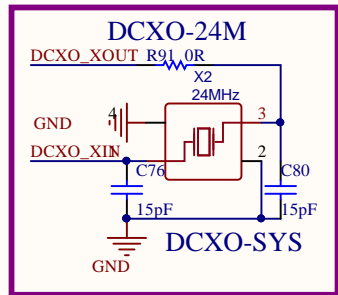
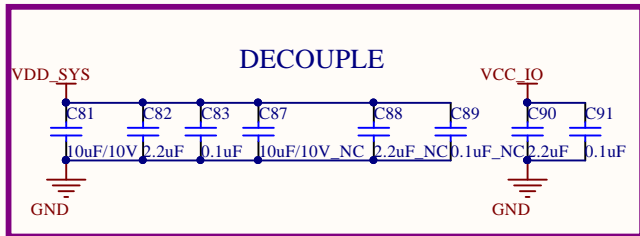
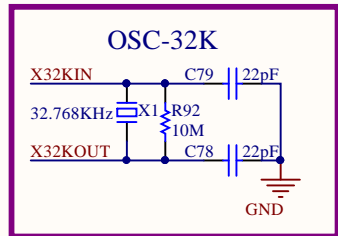
3

4



IO	两组MIPI-2 lane	MIPI-4 lane
PA0	MIPIA_CSL_CK0P	MIPL_CSL_CK0P
PA1	MIPIA_CSL_CK0N	MIPL_CSL_CK0N
PA2	MIPIA_CSL_D1N	MIPL_CSL_D1N
PA3	MIPIA_CSL_D1P	MIPL_CSL_D1P
PA4	MIPIA_CSL_D0P	MIPL_CSL_D0P
PA5	MIPIA_CSL_D0N	MIPL_CSL_D0N
PA6	MIPIB_CSL_D0N	MIPL_CSL_D2N
PA7	MIPIB_CSL_D0P	MIPL_CSL_D2P
PA8	MIPIB_CSL_D1N	MIPL_CSL_D3N
PA9	MIPIB_CSL_D1P	MIPL_CSL_D3P
PA10	MIPIB_CSL_CK0N	
PA11	MIPIB_CSL_CK0P	

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Title		
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