| Proposal | Contact | Proposal Online | Feedbacks |
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| **Multi NPU support [Dell]** | Mihai_Lazar@Dell.com | https://github.com/opencomputeproject/SAI/tree/master/doc/multiNPU | <ul><li>Should SAI expose single SAI instance to hide multi-chips and expose capability to control each chip separately?<ul><li>Current SAI already supports this single SAI instance.</li><li>Need flexibility</li></ul></li><li>Using switch_id as an argument versus using it as an attribute. What's the difference?</li><li>Switch ID should be local , Device ID  should be global</li><li>Full hardware learning or software learning?</li><li>Switch ID is fine for SAI 1.0, need to have more details in the design</li></ul> |
| **Bidge Model / Instance [MLNX]** | mattyk@mellanox.com | https://github.com/opencomputeproject/SAI/tree/master/doc/bridge | <ul><li>Need to make Ingress L2 Interface type table more clear</li><li>Pipeline definition confusing</li><li>Need to support multiple .1Q bridge in SAI (take offline)</li><li>Open questions to the forum: should we expose or hide the Bridge Router Object to user or not?</li><li>SAI_FDB_Entry_T type, could have a br_id for 1Q</li><li>To make it more clear, terminology can use VLAN Aware or VLAN Unaware</li></ul> |
| **ACL Model [Cavium]** | zubin.shah@caviumnetworks.com | Please upload to Github | <ul><li>Table ID - In the meta data, how many ACL table IDs  accompany one packet ? If each pipeline has one ACL table, that means a packet will actually has multiple ACL IDs.</li><li>Suggest to support the capability of binding  ACL table according to a packet type, e.g. ACL table type 1 matches to IPv6 packets, ACL table type 2 matches to IPV4 packets.  **Follow up on this needed**, Vitaly to talk about ACL table attribute.</li><li>Leave the ACL Table as it is.</li><li>Binding places - port ACL, Bridge ACL, Routing ACL, 3 logical types. **Need to clarify**, if we have 4 binding points, do we still need different stages? Adding pipeline and stages to SAI, where should be the binding points?</li></ul> |

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| **L2/L3 Multicast [Centec]** | yaom@centecnetworks.com | https://github.com/opencomputeproject/SAI/tree/master/doc/L2-Multicast | • Suggest to change the next_hop_group to some group containers for general purpose that the container contains a group of objects and has the attribute type e.g. ECMP or L2MC<br>• Make a generic group type that defines the list of port for multicast FDB entry<br>• Add Switch-level attribute to define whether it's fdb entry/src IP based snooping. (RO)<br>• The L3 multicast condition should be changed, need to include the case where the IN_PORT is a bridge port.<br>• uRFP needed for L2/L3 multicast. |
| **Broadview [Broadcom]** | bhaskar.chinni@broadcom.com | https://github.com/opencomputeproject/SAI/pull/167 | • What's the performance to gather statistics?<br>• Extend to have hysteresis to track the peak.<br>• Snapshot to be atomic (yes)<br>• Multiple BST objects to create a group of counters<br>• Suggest to dynamically query the object tables and populate them<br>• Would like to standardize a minimum subset of the functionalities (attributes)<br>• Whether to separate level (critical resource) counters and monotonic (incremental) counters<br>• Need to take it offline with MSFT, MLNX, Dell, Cavium etc. who are interspersed in monitoring to come up with one solution |
| **Telemetry Infrastructure [Mellanox]** | ilyav@mellanox.com | https://github.com/opencomputeproject/SAI/pull/166 | • Change from probe to sample<br>• This proposal is focusing on infrastructure (framework) not details of implementation<br>• There is a concern that whether histogram can be implemented in ASIC or not. It seems to be a software feature for some folks in the room<br>• Community suggests to have one proposal for counter collection infrastructure<br>• Proposal to have follow meeting with DELL/MSFT/BRCM/MLNX/CAVM to discussion the next step for telemetry infrastructure. |

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| **Host Interface V2 [Mellanox]** | mattyk@mellanox.com | https://github.com/opencomputeproject/SAI/blob/master/doc/Host-Interface | • Describe the egress flow for the outgoing packet from CPU<br>• Need to specify the promiscuous mode on the host interface<br>• Host table match: priority, object id key (can be port id, lag id, and vlan id), trap id key, (either key can be masked) |
| **802.1BR [Dell]** | Ravikumar_Sivasankar@Dell.com | https://github.com/opencomputeproject/SAI/pull/52/ | • Add support to VPLAG<br>• Separate PE and CB pipeline<br>• See if we can integrate the CB pipeline into SAI tunnel. |